



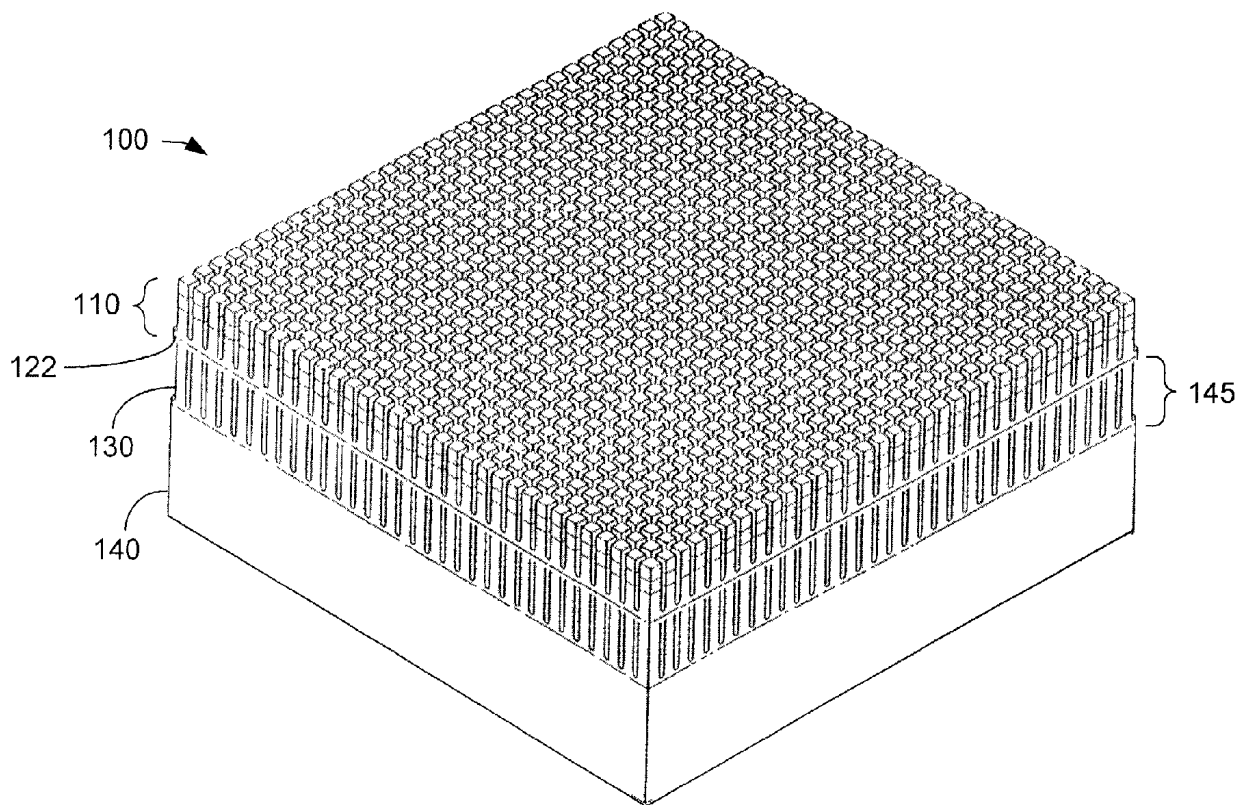
US 20030024317A1

(19) **United States**(12) **Patent Application Publication****Miller**(10) **Pub. No.: US 2003/0024317 A1**(43) **Pub. Date: Feb. 6, 2003**(54) **ULTRASONIC TRANSDUCER WAFER
HAVING VARIABLE ACOUSTIC
IMPEDANCE**(76) **Inventor: David G. Miller, North Andover, MA
(US)**

Correspondence Address:
AGILENT TECHNOLOGIES, INC.
Legal Department, DL429
Intellectual Property Administration
P.O. Box 7599
Loveland, CO 80537-0599 (US)

(21) **Appl. No.: 09/919,241**(22) **Filed: Jul. 31, 2001****Publication Classification**(51) **Int. Cl.⁷ G01N 9/24; G01N 29/04**(52) **U.S. Cl. 73/643; 73/626**(57) **ABSTRACT**

The invention is a wafer having variable acoustic properties. The wafer may be used as a substrate over which to form an ultrasonic transducer, an IC, or may be used as a circuit board. An ultrasonic transducer formed on the wafer may include piezoelectric ceramic transducer elements or MUT elements. By controlling the acoustic impedance of the wafer upon which the integrated control circuitry for an ultrasonic transducer is formed, the acoustic impedance of the wafer can be matched to the acoustic impedance requirements of the ultrasonic transducer. Furthermore, by the addition of internal voids, the wafer reduces or eliminates the lateral propagation of acoustic energy through the wafer.



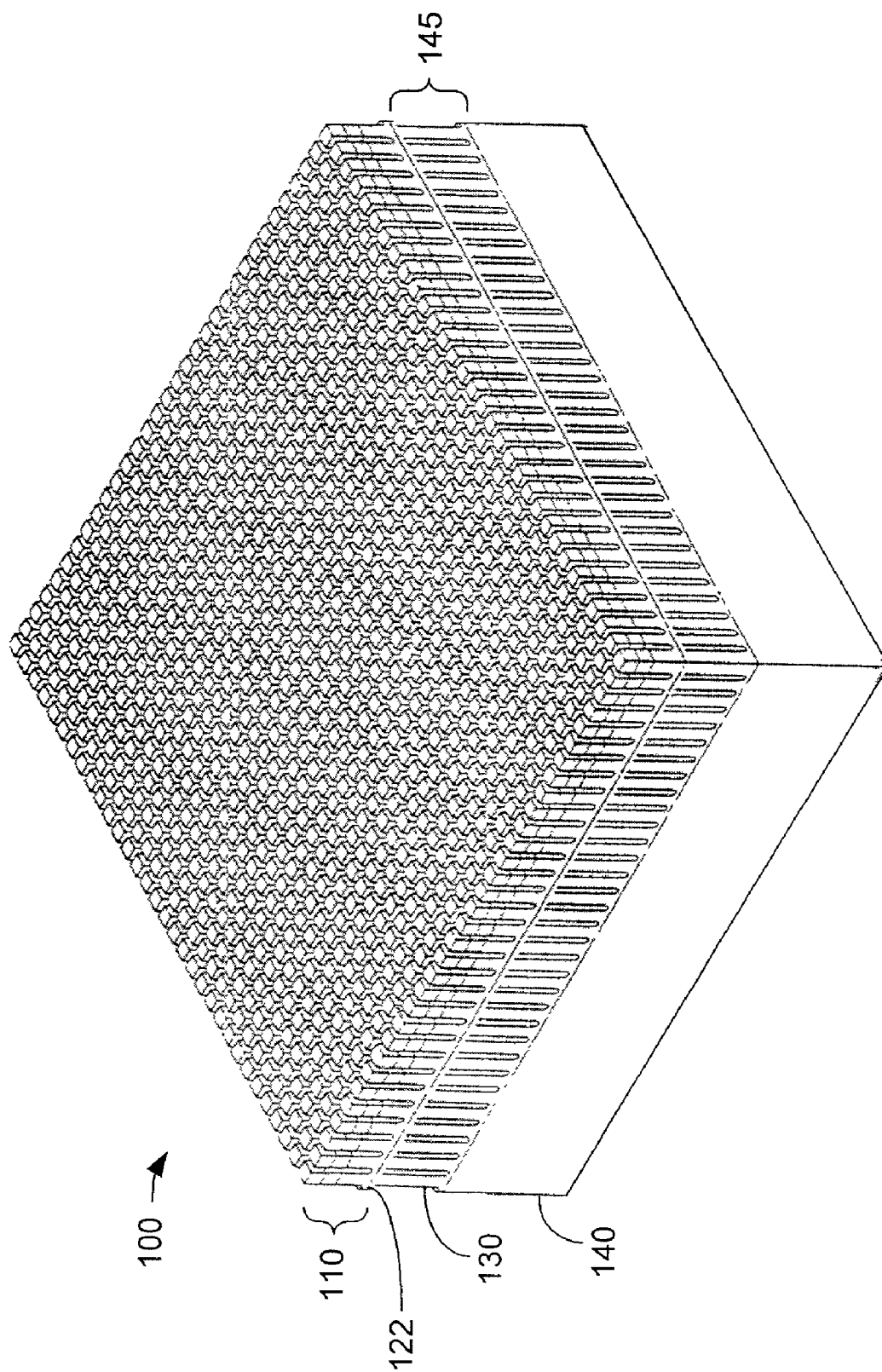


FIG. 1

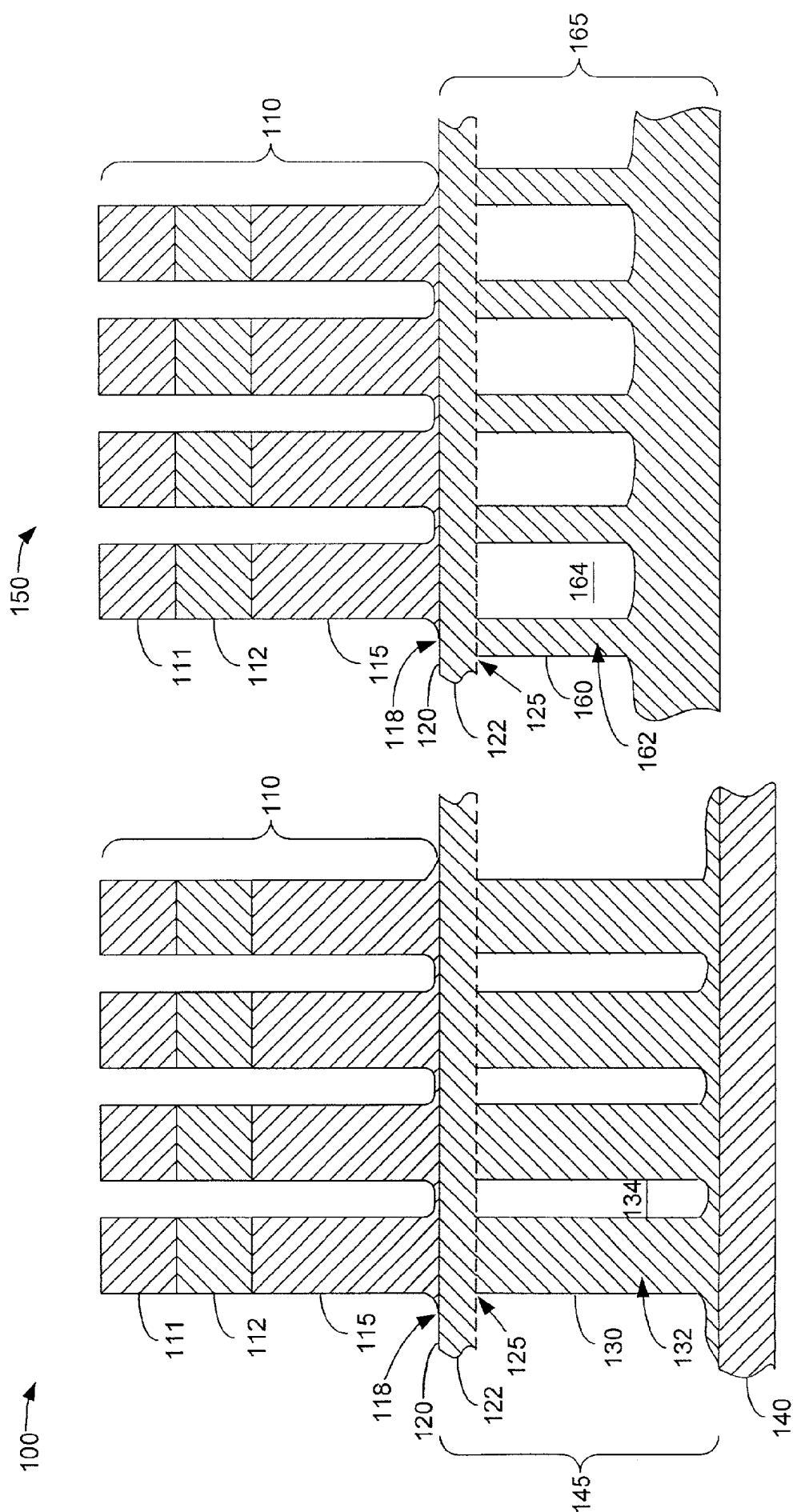


FIG. 2B

FIG. 2A

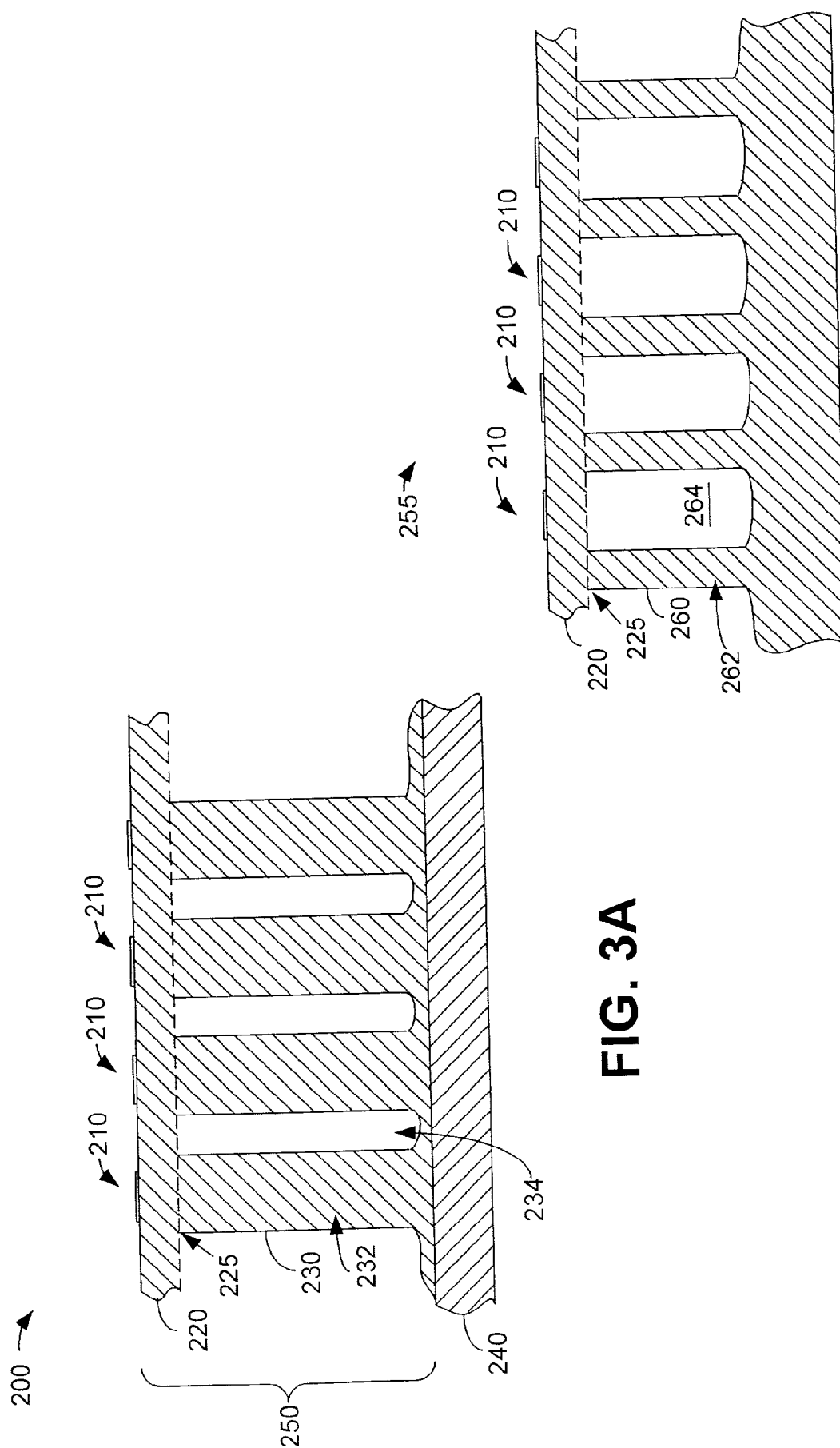


FIG. 3B

ULTRASONIC TRANSDUCER WAFER HAVING VARIABLE ACOUSTIC IMPEDANCE

TECHNICAL FIELD

[0001] The present invention relates generally to ultrasonic transducers, and, more particularly, to an ultrasonic transducer wafer, or substrate, having variable acoustic impedance.

BACKGROUND OF THE INVENTION

[0002] Ultrasonic transducers have been available for quite some time and are particularly useful for non-invasive medical diagnostic imaging. Ultrasonic transducers are typically formed of either piezoelectric elements or of micro-machined ultrasonic transducer (MUT) elements. The piezoelectric elements typically are made of a piezoelectric ceramic such as lead-zirconate-titanate (abbreviated as PZT), with a plurality of elements being arranged to form a transducer array. A MUT is formed using known semiconductor manufacturing techniques resulting in a capacitive ultrasonic transducer cell that comprises, in essence, a flexible membrane supported around its edges over a silicon substrate. By applying contact material, in the form of electrodes, to the membrane, or a portion of the membrane, and to the base of the cavity in the silicon substrate, and then by applying appropriate voltage signals to the electrodes, the MUT may be energized such that an appropriate ultrasonic wave is produced. Similarly, when electrically biased, the membrane of the MUT may be used to receive ultrasonic signals by capturing reflected ultrasonic energy and transforming that energy into movement of the electrically biased membrane, which then generates a receive signal.

[0003] The transducer elements may be combined with control circuitry forming a transducer assembly, which is then further assembled into a housing possibly including additional control electronics, in the form of electronic circuit boards, the combination of which forms an ultrasonic probe. This ultrasonic probe, having either bulk wave piezoelectric elements or MUTs and which may include various acoustic matching layers, backing layers, and de-matching layers may then be used to send and receive ultrasonic signals through body tissue.

[0004] In the past, joining an acoustic sensor, such as a piezoelectric ceramic transducer element or a MUT element, to the electrical control circuitry required the use of many individual wires to connect each element of the transducer array to the control circuitry. In the case of large transducer arrays having many hundreds or thousands of elements, large wiring harnesses were required. Unfortunately, a large wiring harness increases the bulk and the cost of the ultrasonic probe. For ultrasonic probes that are designed to be used inside the human body, it is desirable to reduce the overall size of the ultrasonic probe and related cabling. One manner of reducing the size of the probe and the cabling is to provide the transducer control electronics on an integrated circuit (IC) assembly or on a circuit board. An IC in proximity to the transducer array may be used to transmit and receive from many small transducer elements and may also be used to multiplex the signals, thereby reducing or eliminating the bulky and expensive cables that typically connect the ultrasonic probe elements to the control electronics.

[0005] Placing the transducer array over the IC results in greater packaging efficiency, but often causes undesirable coupling of acoustic energy between transducer elements through the substrate material because the substrate material on which the IC is formed comes into contact with all of the transducer elements.

[0006] Therefore, it would be desirable to have a way to connect the elements of an ultrasonic transducer array directly to an integrated circuit or circuit board while reducing or eliminating the undesirable lateral propagation of acoustic energy through the substrate of the IC and the circuit board.

SUMMARY

[0007] The invention is directed to a wafer having variable acoustic properties. The wafer may be used as a substrate over which to form an ultrasonic transducer, an IC, or may be used as a circuit board. An ultrasonic transducer formed on the wafer may include piezoelectric ceramic transducer elements or MUT elements. By controlling the acoustic impedance of the wafer upon which the integrated control circuitry for an ultrasonic transducer is formed, the acoustic impedance of the wafer can be matched to the acoustic impedance requirements of the ultrasonic transducer. Furthermore, by the addition of internal voids, the wafer reduces or eliminates the lateral propagation of acoustic energy through the wafer.

[0008] Other systems, methods, features, and advantages of the invention will be or will become apparent to one with skill in the art upon examination of the following drawings and detailed description. It is intended that all such additional systems, methods, features, and advantages be included within this description, be within the scope of the present invention, and be protected by the accompanying claims.

BRIEF DESCRIPTION OF THE DRAWINGS

[0009] The present invention, as defined in the claims, can be better understood with reference to the following drawings. The components within the drawings are not necessarily to scale relative to each other, emphasis instead being placed upon clearly illustrating the principles of the present invention.

[0010] FIG. 1 is a perspective view of an ultrasonic transducer array formed on a wafer.

[0011] FIG. 2A is a cross-sectional schematic view of a portion of the transducer of FIG. 1.

[0012] FIG. 2B is a cross-sectional schematic view illustrating an alternative embodiment of the ultrasonic transducer of FIG. 2A.

[0013] FIG. 3A is a cross-sectional schematic view illustrating a transducer constructed in accordance with an aspect of the invention and including micro-machined ultrasonic transducer (MUT) elements.

[0014] FIG. 3B is a cross-sectional schematic view illustrating an alternative embodiment of the ultrasonic transducer of FIG. 3A.

DETAILED DESCRIPTION OF THE INVENTION

[0015] The invention to be described hereafter is applicable to piezoelectric and micro-machined ultrasonic trans-

ducer (MUT) elements connected to a substrate on which an integrated circuit (IC) can be formed.

[0016] FIG. 1 is a perspective view of an ultrasonic transducer 100. For illustration purposes only, the ultrasonic transducer 100 comprises a plurality of elements, referred to as an array, constructed using a piezoelectric material. However, the ultrasonic transducer 100 can also be fabricated using MUT elements, as will be described below with respect to FIGS. 3A and 3B. An exemplar one of the plurality of elements is denoted by reference numeral 110. The collective plurality of transducer elements 110 comprises an ultrasonic sensor. Although illustrated as uniformly spaced, the transducer elements 110 need not be spaced as illustrated. The transducer element 110 may include one or more matching layers (to be described below) and is attached to an integrated circuit (IC) (not shown in FIG. 1). In order to maximize packaging efficiency, it is desirable to assemble the transducer elements 110 over the IC. The IC can be fabricated on an acoustically variable wafer 145 constructed in accordance with an aspect of the invention. The wafer 145 can be fabricated using, for example, silicon (Si), or can alternatively be fabricated using other substrate materials such as, for example, but not limited to, germanium (Ge). As will be described below, the acoustically variable wafer 145 is formed by bonding two wafer components together. The two wafer components can also be referred to as individual wafers.

[0017] The first wafer component 130 is formed by dicing grooves with, for example, a dicing saw, or selectively etching to remove material to produce a pattern on the first wafer component 130. The pattern can be optimized for the particular application and configuration of the transducer array that will be formed over the wafer. In one embodiment the pattern includes a plurality of posts and voids. The second wafer component 122 is preferably an ultra-thin wafer and is bonded to the first wafer component 130. The first wafer component 130 and the second wafer component 122 form an acoustically variable wafer 145 having adequate stiffness and is useful for subsequent processing into any of an IC, a MUT or a circuit board with traces and vias. As will be explained in further detail below, locating the transducer array elements over the posts or the voids inside the acoustically variable wafer 145 changes the acoustic impedance experienced by each transducer element 110.

[0018] The IC is formed over the acoustically variable wafer 145. The acoustically variable wafer 145 is constructed having a variable impedance and includes an acoustically variable substrate, referred to herein as the first wafer component 130, over which is bonded a second wafer component 122. As will be described below, the first wafer component 130 includes, for example, a waffle patterned structure that allows for variable acoustic impedance. The pattern applied to the first wafer component 130 also reduces the lateral propagation of acoustic energy within the acoustically variable wafer 145. The IC can be formed on either surface of the acoustically variable wafer 145. Furthermore, instead of an IC, the acoustically variable wafer 145 could include a circuit board with traces and no active circuitry. Typically, the material from which the first wafer component 130 is formed is the same material as that of the second wafer component 122. Optionally, backing 140 can be

applied behind the first wafer component 130. The backing 140 acts as an acoustic absorption material.

[0019] In accordance with an aspect of the invention, and to be described in detail below, the first wafer component 130 includes a number of cavities that define a number of support elements, referred to below as posts. The average, as well as the local, acoustic impedance of the acoustically variable wafer 145 below an array element 110 can be controlled by altering the size, quantity, shape, location, and other parameters of the cavities. Furthermore, the cavities within the first wafer component 130 reduce or eliminate the propagation of acoustic energy, or waves, traveling laterally through the acoustically variable wafer 145. This arrangement reduces the coupling of acoustic energy between the individual transducer elements 110. By varying the acoustic impedance of the acoustically variable wafer 145, the acoustic impedance of the acoustically variable wafer 145 can be matched to the acoustic impedance of the transducer elements 110. Further, by adding the cavities to the first wafer component 130, the lateral cross-coupling of acoustic energy between transducer elements 110 is significantly reduced or eliminated. Further still, by varying the acoustic impedance requirement of the acoustically variable wafer 145 to match the acoustic impedance of the transducer elements 110, the bandwidth of the ultrasonic transducer 100 can be increased.

[0020] FIG. 2A is a cross-sectional schematic view of a portion of the transducer 100 of FIG. 1. Transducer 100 includes a plurality of transducer elements 110, each of which include a first matching layer 111, a second matching layer 112, and a piezoelectric element 115. Although shown having two matching layers 111 and 112, the transducer 100 can be made with only one matching layer or with no matching layers, depending upon the desired application. The matching layer or layers, as known to those having ordinary skill in the art, help to match the acoustic impedance of the piezoelectric element 115 to the acoustic impedance of the tissue being imaged. The piezoelectric element 115 can be fabricated using all PZT ceramic, or can be fabricated using a composite piezoelectric material, depending on the desired impedance matching characteristics of the transducer.

[0021] The transducer element 110, and in particular, the piezoelectric element 115, is joined to the IC 120 along joint line 118. For purposes of explanation, the following description will assume that the IC 120 is fabricated on a surface of the acoustically variable wafer 145. The acoustically variable wafer 145 is fabricated by joining the second wafer component 122 to the first wafer component 130. The second wafer component 122 is preferably on the order of less than 200 micrometers (μm), sometimes referred to as "microns," thick and can be joined to the first wafer component 130 using, for example, anodic bonding or fusion bonding, which are well known bonding techniques.

[0022] A number of different methodologies can be used to join the piezoelectric element 115 to the IC 120, many of which are disclosed in commonly assigned U.S. Patent Application entitled "System for Attaching an Acoustic Element to an Integrated Circuit," filed on even date herewith, and assigned Ser. No. XXXXX, (Attorney Docket No. 10004001).

[0023] In accordance with an aspect of the invention, the first wafer component 130 includes a plurality of cavities

134, which define a plurality of pillars, or posts 132. Preferably, the cavities are cut into the first wafer component 130 using a dicing saw, or are chemically etched into the first wafer component 130 using etching techniques that are known to those having ordinary skill in the art. Preferably, the cavities 134 are formed to be approximately 50-100 micrometers (μm) wide and 250 to 450 μm deep. However, other cavity dimensions are possible, depending upon the desired acoustic properties of the acoustically variable wafer 145.

[0024] In the example shown in FIG. 2A, each transducer element 110 resides over one of the posts, 132. By altering the physical characteristics of the cavities 134 the acoustic properties of the first wafer component 130, and thus, the acoustically variable wafer 145, can be altered. In this manner, the acoustic properties of the entire ultrasonic transducer 100 can be varied, and thereby controlled. The acoustic impedance of the first wafer component 130 can be designed to match the acoustic impedance of the element 110.

[0025] In another aspect of the invention, the cavities 134 within the first wafer component 130 reduce or eliminate the propagation of acoustic waves that travel laterally through the acoustically variable wafer 145. In this manner, the coupling of acoustic energy between elements 110 through the acoustically variable wafer 145 can be significantly reduced or eliminated.

[0026] After the cavities 134 are formed in the first wafer component 130, the second wafer component 122 is joined to the first wafer component 130 along line 125, thus forming acoustically variable wafer 145. In this example, the second wafer component 122 and the first wafer component 130 are both silicon. Therefore, the second wafer component 122 can be joined to the first wafer component 130 using, for example, anodic bonding. Alternatively, fusion bonding, or other techniques known to those having ordinary skill in the art, can be used to join the second wafer component 122 to the first wafer component 130. This results in an acoustically variable wafer 145 having an acoustic impedance defined by the configuration of the cavities 134.

[0027] After the second wafer component 122 is joined to the first wafer component 130, the IC 120 is formed on the exposed surface of the second wafer component 122 using conventional IC fabrication methodologies. In profile, the IC 120 is very thin with respect to the thickness of the acoustically variable wafer 145. After the IC 120 is formed, the material that forms the piezoelectric element 115 is joined to the surface of the IC 120 along line 118 as described above. The matching layers 112 and 111 are then applied over the piezoelectric elements 115 and then the transducer elements 110 are formed by, for example, dicing or etching.

[0028] Depending on the desired characteristics of the ultrasonic transducer 100, prior to assembling the second wafer component 122 to the first wafer component 130, the cavities 134 can be filled with air or an inert gas. Alternatively, the second wafer component 122 can be joined to the first wafer component 130 in a vacuum so that the gas within the cavities 134 can be at a pressure less than that of the surrounding area.

[0029] The following discussion illustrates one manner in which the acoustic impedance of the first wafer component

130, and therefore, the acoustically variable wafer 145, can be altered to match the acoustic impedance of the transducer elements 110. Assume that silicon has an acoustic impedance of approximately 19 Mrayls (a Mrayl is unit by which acoustic impedance is measured) and piezoelectric ceramic (the material from which each piezoelectric element 115 is formed) has an acoustic impedance of approximately 33 Mrayls. Assume that it is desirable to match the acoustic impedance of the acoustically variable wafer 145 (silicon) to the acoustic impedance of the piezoelectric element 115 (piezoelectric ceramic). Reducing the impedance of the piezoelectric ceramic can be done by using a composite of piezoelectric ceramic and various polymers, having an acoustic impedance of approximately 19 Mrayls. This prevents reflections at the interface 118 between the piezoelectric element 115 and the IC 120. In accordance with an aspect of the invention, to prevent acoustic reflections at the back of the silicon interface (the surface 125 at which the second wafer component 122 is bonded to the first wafer component 130) the acoustic impedance of the acoustically variable wafer 145 can be defined, by defining the posts 132 and cavities 134, so that the acoustic impedance of the first wafer component 130 matches the acoustic impedance of the piezoelectric element 115.

[0030] Acoustic backing 140 can be added to the back of the first wafer component 130 in order to absorb any acoustic energy that migrates through the acoustically variable wafer 145.

[0031] FIG. 2B is a cross-sectional schematic view illustrating an alternative embodiment of the ultrasonic transducer 100 of FIG. 2A. Ultrasonic transducer 150, similar to that described above with respect to ultrasonic transducer 100, includes a first matching layer 111 and a second matching layer 112 over a piezoelectric element 115. The piezoelectric element 115 can be a piezoelectric ceramic, or can be a piezoelectric composite material as described above. The first matching layer 111, the second matching layer 112 and the piezoelectric element 115 comprise transducer element 110. Each transducer element 110 is attached to the IC 120 in similar manner to that described above.

[0032] In a similar manner to that described above with respect to FIG. 2A, the first wafer component 160 has a plurality of cavities 164 formed therein either by dicing, etching, or some other manner known to those having ordinary skill in the art. The cavities 164 define a plurality of posts 162. However, in the embodiment described in FIG. 2B, each transducer element 110 resides above one of the cavities 164. As mentioned above, each of the cavities 164 can be filled with air, inert gas, or can be formed having a vacuum, thus creating a vacuum or gas-backed ultrasonic transducer 150. The air-backed ultrasonic transducer 150 provides high efficiency and wide bandwidth operation. Similar to that described above, the cavities 164 can be designed to alter the acoustic impedance of the first wafer component 160, and therefore, the acoustic impedance of the acoustically variable wafer 165, and also reduce or eliminate the transmission of acoustic energy laterally through the acoustically variable wafer 165.

[0033] FIG. 3A is a cross-sectional schematic view illustrating a transducer 200 constructed in accordance with another aspect of the invention and including micro-machined ultrasonic transducer (MUT) elements. The ultra-

sonic transducer **200** includes an acoustically variable wafer **250** over which a plurality of MUT elements **210** are formed. Each MUT element comprises a plurality of MUT cells (not shown). The acoustically variable wafer **250** includes first wafer component **230** and a second wafer component **220**, which also forms the substrate over which the MUT elements **210** are formed. The second wafer component **220** is similar to the second wafer component **122** described above, and may be silicon or another semiconductor substrate material.

[0034] In accordance with this aspect of the invention, cavities **234** are formed in the first wafer component **230**, prior to joining the second wafer component **220**, resulting in the acoustically variable wafer **250** having the structure shown. Each cavity **234** can be etched or cut into the first wafer component **230**, creating cavities **234** that are preferably approximately 50-100 micrometers (μm) wide and 250 to 450 μm deep.

[0035] After the cavities **234** are formed in the first wafer component **230**, the second wafer component **220** is bonded along line **225** to the first wafer component **230**, thus forming the acoustically variable wafer **250**. After the acoustically variable wafer **250** is thus formed, the MUT elements **210** are fabricated over a surface of the acoustically variable wafer **250** as known to those having ordinary skill in the art. The first wafer component **230** can be either silicon or any other substrate depending upon the acoustic performance desired of the ultrasonic transducer **200**. The second wafer component **220** can be bonded to the first wafer component **230** using, for example, anodic bonding or fusion bonding. Furthermore, in this example, other silicon to silicon bonding techniques can be used.

[0036] As described above, the cavities **234** can be filled with air, gas or can contain a vacuum in order to provide acoustic isolation and greatly reduce or eliminate any acoustic energy traveling laterally in the acoustically variable wafer **250**. This greatly reduces acoustic coupling, thus reducing acoustic cross-talk between MUT elements **210**. As shown in FIG. 3A, each MUT element **210** resides over one of the post **234**. By altering the size, shape, location and quantity of the cavities **234**, the acoustic impedance of the first wafer component **230**, and in particular, the acoustic impedance of each post **232**, can be designed to match the acoustic impedance of a MUT transducer element to a range of backing materials. As described above, backing **240** can be applied to the first wafer component **230** to absorb any acoustic energy that travels through the acoustically variable wafer **250**.

[0037] FIG. 3B is a cross-sectional schematic view illustrating an alternative embodiment **255** of the ultrasonic transducer **200** of FIG. 3A. As shown in FIG. 3B, the first wafer component **260** includes a plurality of cavities **264**, which define a plurality of posts **262**. However, in this embodiment, each MUT element **210** resides over one of the cavities **264**.

[0038] It will be apparent to those skilled in the art that many modifications and variations may be made to the present invention, as set forth above, without departing substantially from the principles of the present invention. For example, the present invention can be used with piezoelectric ceramic and MUT transducer elements. Furthermore, the invention is applicable to different substrate mate-

rials including, for example, silicon and germanium. All such modifications and variations are intended to be included herein.

What is claimed is:

1. An ultrasonic transducer, comprising:

an ultrasonic sensor having a plurality of elements; and
an integrated circuit formed on a wafer, the wafer including a plurality of cavities defining a plurality of posts such that the cavities alter the acoustic impedance of the wafer, and wherein the integrated circuit is joined to the ultrasonic sensor.

2. The transducer of claim 1, wherein the ultrasonic sensor comprises piezoelectric ceramic material.

3. The transducer of claim 1, wherein the ultrasonic sensor comprises a micro-machined ultrasonic transducer (MUT).

4. The transducer of claim 1, wherein each element of the ultrasonic sensor is located over one of the plurality of posts.

5. The transducer of claim 1, wherein each of the elements of the ultrasonic sensor are located over one of the plurality of cavities.

6. The transducer of claim 1, wherein the cavities reduce acoustic energy traveling laterally in the wafer.

7. The transducer of claim 1, wherein the wafer is silicon.

8. The transducer of claim 1, wherein the wafer is germanium.

9. The transducer of claim 1, wherein the cavities are designed to allow the acoustic impedance of the wafer to match the acoustic impedance of the transducer elements.

10. The transducer of claim 1, wherein altering the acoustic impedance of the wafer increases the effective bandwidth of the transducer elements.

11. The transducer of claim 1, wherein the wafer further comprises:

a first wafer component including the plurality of cavities; and

a second wafer component bonded to the first wafer component.

12. A method for forming an ultrasonic transducer, the method comprising the steps of:

forming a plurality of cavities in a first wafer component such that the cavities define the acoustic impedance of the first wafer component and such that the cavities define a plurality of posts;

joining a second wafer component to the first wafer component;

forming an integrated circuit on a surface of the second wafer component;

forming an ultrasonic sensor having a plurality of elements; and

joining the ultrasonic sensor to the integrated circuit.

13. The method of claim 12, wherein the ultrasonic sensor comprises piezoelectric ceramic material.

14. The method of claim 12, wherein the ultrasonic sensor comprises a micro-machined ultrasonic transducer (MUT).

15. The method of claim 12, further comprising the step of locating each of the elements of the ultrasonic sensor over one of the plurality of posts.

16. The method of claim 12, further comprising the step of locating each of the elements of the ultrasonic sensor over one of the plurality of cavities.

17. The method of claim 12, wherein the cavities reduce acoustic energy traveling laterally in the substrate.

18. The method of claim 12, wherein the first wafer component and the second wafer component are silicon.

19. The method of claim 12, wherein the first wafer component and the second wafer component are germanium.

20. The method of claim 12, wherein the first wafer component and the second wafer component form an acoustically variable wafer.

21. The method of claim 20, further comprising the step of designing the cavities to alter the acoustic impedance of the wafer to match the acoustic impedance of the transducer elements.

22. The method of claim 20, further comprising the step of altering the acoustic impedance of the wafer to increase the effective bandwidth of the transducer elements.

23. An acoustically variable wafer, comprising:

a first wafer component having a plurality of cavities defining a plurality of posts such that the cavities alter the acoustic impedance of the first wafer component; and

a second wafer component bonded to the first wafer component, the first wafer component and the second wafer component forming the wafer, where the wafer has a variable acoustic impedance.

24. The wafer of claim 23, further comprising an integrated circuit formed over a surface of the wafer.

25. The wafer of claim 23, further comprising a micro-machined ultrasonic transducer formed over a surface of the wafer.

26. The wafer of claim 23, wherein the wafer comprises a circuit board.

* * * * *

| | | | |
|----------------|---|---------|------------|
| 专利名称(译) | 具有可变声阻抗的超声换能器晶片 | | |
| 公开(公告)号 | US20030024317A1 | 公开(公告)日 | 2003-02-06 |
| 申请号 | US09/919241 | 申请日 | 2001-07-31 |
| [标]申请(专利权)人(译) | DAVID MILLER 摹 | | |
| 申请(专利权)人(译) | DAVID MILLER G. | | |
| 当前申请(专利权)人(译) | DAVID MILLER G. | | |
| [标]发明人 | MILLER DAVID G | | |
| 发明人 | MILLER, DAVID G. | | |
| IPC分类号 | G01N29/24 A61B8/00 B06B1/02 B06B1/06 G10K11/00 H04R17/00 H04R31/00 G01N9/24 G01N29/04 | | |
| CPC分类号 | B06B1/0292 B06B1/0629 G01N29/245 G01N2291/0426 G10K11/002 | | |
| 其他公开文献 | US6758094 | | |
| 外部链接 | Espacenet USPTO | | |

摘要(译)

本发明是一种具有可变声学特性的晶片。晶片可以用作衬底，在衬底上形成超声换能器，IC，或者可以用作电路板。形成在晶片上的超声换能器可包括压电陶瓷换能器元件或MUT元件。通过控制晶片的声阻抗，在其上形成用于超声换能器的集成控制电路，晶片的声阻抗可以与超声换能器的声阻抗要求相匹配。此外，通过添加内部空隙，晶片减少或消除了通过晶片的声能的横向传播。

