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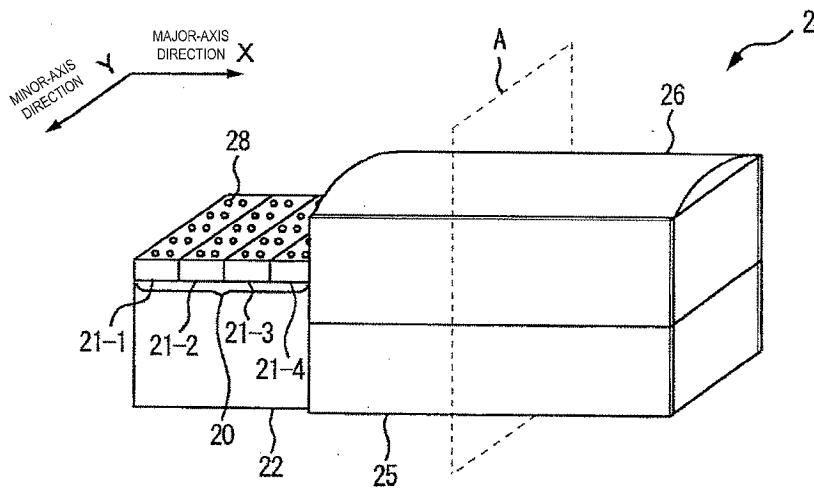
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(54) **ULTRASONIC PROBE, AND ULTRASONIC DIAGNOSIS DEVICE USING THE PROBE**

(57) An ultrasonic probe including a cMUT chip that has plural oscillation elements whose electromechanical coupling coefficient or sensitivity varies in accordance with a bias voltage and transmits/receives an ultrasonic wave, an acoustic lens provided at an ultrasonic wave transmission/reception side of the cMUT chip, a backing layer provided to the opposite surface of the cMUT chip

to the acoustic lens, and a substrate provided between the backing layer and the cMUT chip. The ultrasonic probe further includes thermal stress suppressing means for suppressing thermal stress occurring due to the difference in linear expansion coefficient caused by temperature variation between the substrate and the backing layer.

FIG.2



Description

Technical Field

[0001] The present invention relates to an ultrasonic probe using cMUT (Capacitive Micromachined Ultrasonic Transducer) chip, and particularly to a technique for securing long-term reliability of an ultrasonic probe. 5

Background Art

[0002] An ultrasonic diagnostic apparatus is an apparatus for picking up a diagnosis image on the basis of an echo signal output from an ultrasonic probe and a reflection signal thereof. Plural ultrasonic oscillators are arranged in the ultrasonic probe. The ultrasonic oscillator converts a driving signal to an ultrasonic wave and transmits the ultrasonic wave to an examinee, and also it receives a reflection echo signal generated from the examinee and converts the reflection echo signal into an electrical signal. 10

[0003] A broad-band ultrasonic probe using cMUT chip has been recently developed. The cMUT chip is a hyperfine capacitance type ultrasonic oscillator manufactured by a semiconductor microfabrication process (for example, Patent Document 1). 15

[0004] In an example of the structure of a backing layer of this type of ultrasonic probe, the acoustic impedances of the backing layer and the cMUT chip are matched with each other in order to suppress unnecessary vibration of an electrode at the backing layer side in the cMUT chip (for example, Patent Document 2). 20

[0005]

Patent Document 1: U.S. Patent No. 5,894,452
Patent Document 2: U.S. Patent No. 6,714,484

Disclosure of the Invention

Problem to be Solved by the Invention

[0006] However, with respect to the ultrasonic probe using the cMUT chip, when there is some difference in linear expansion coefficient between the cMUT chip formed of silicon wafer and a backing material, thermal stress is mutually applied to each other due to temperature variation, and thus exfoliation or the like occurs between the respective layers. Therefore, there has been an unsolved problem that long-term reliability is lost. 45

[0007] An object of the present invention is to provide an ultrasonic probe that can suppress an effect of thermal stress, and an ultrasonic diagnostic apparatus using the ultrasonic probe. 50

Means of solving the Problem

[0008] An ultrasonic probe of the present invention has the following constituent elements. 55

[0009]

(1) An ultrasonic probe including a cMUT chip that has plural oscillation elements whose electromechanical coupling coefficient or sensitivity varies in accordance with a bias voltage and transmits/receives an ultrasonic wave, an acoustic lens provided at an ultrasonic wave transmission/reception side of the cMUT chip, a backing layer provided to the opposite surface of the cMUT chip to the acoustic lens, and a substrate provided between the backing layer and the cMUT chip is characterized by further comprising thermal stress suppressing means for suppressing thermal stress occurring due to the difference in linear expansion coefficient caused by temperature variation between the substrate and the backing layer. 20

[0010]

(2) The thermal stress suppressing means may be formed of a material which makes the backing layer and the cMUT chip substantially coincident with each other in linear expansion coefficient.
(3) The thermal stress suppressing means may be formed of a third material having a linear expansion coefficient different from those of metal and resin constituting the backing layer.
(4) The third material may be a material containing silicon dioxide as a main component.
(5) The third material may be a fibrous material containing silicon dioxide as a main component.
(6) The metal may be tungsten, and the fiber may be polyamide resin.
(7) The linear expansion coefficient of the backing layer may be 50 to 60 ppm/°C with respect to the linear expansion coefficient of the semiconductor substrate. 30

[0011]

(8) The thermal stress suppressing means may be provided with an adjusting layer for adjusting the difference of the linear expansion coefficient between the cMUT chip and the backing layer.
(9) The adjusting layer may be formed of a material having a smaller modulus of elasticity than the cMUT chip and/or the backing layer.
(10) The adjusting layer may be an adhesive layer for fixing the cMUT chip to the backing layer, and formed of a material having a smaller modulus of elasticity than the cMUT chip and/or the backing layer.
(11) The adhesive layer may be formed of adhesive agent of epoxy type adhesive agent, polyurethane type adhesive agent or silicon type adhesive agent.
(12) The adjusting layer may be formed of a material that has rubber elasticity when hardened and has 40 45 50 55

adhesiveness to materials at the interface.

[0012] An ultrasonic diagnostic apparatus according to the present invention comprises: an ultrasonic probe for transmitting/receiving an ultrasonic wave to an examinee; an image processor for constituting an ultrasonic image on the basis of an ultrasonic reception signal output from the ultrasonic probe; and a display unit for displaying the ultrasonic image, wherein the ultrasonic probe is an ultrasonic probe described in any one of the foregoing (1) to (12).

Effect of the Invention

[0013] According to the present invention, there can be provided the ultrasonic probe which can suppress the effect of the thermal stress, and the ultrasonic diagnostic apparatus using the ultrasonic probe.

Brief Description of the Drawings

[0014]

[Fig. 1] Fig. 1 is a diagram showing an example of the construction of an ultrasonic diagnostic apparatus according to an embodiment of the present invention.

[Fig. 2] Fig. 2 is a perspective view of an ultrasonic probe adopted in Fig. 1.

[Fig. 3] Fig. 3 is a diagram showing an example of the construction of an oscillator of Fig. 2.

[Fig. 4] Fig. 4 is a cross-sectional view taken when one of oscillating elements of Fig. 3 is viewed from the side surface.

[Fig. 5] Fig. 5 is a diagram showing an ultrasonic probe according to an example 1.

[Fig. 6] Fig. 6 is a diagram showing wires of the ultrasonic probe 2.

Description of Reference Numerals

[0015] 20 cMUT chip, 22 backing layer, 25 ultrasonic probe cover, 26 acoustic lens, 70, 71 adhesive layer, 72 flexible substrate, 86 wire, 87 wire sealing resin, 90 adhesive layer

Best Mode for Carrying Out Invention

[0016] A preferred embodiment of an ultrasonic diagnosis probe according to the present invention and an ultrasonic diagnostic apparatus using the ultrasonic probe will be described in detail with reference to the accompanying drawings. In the following description and the accompanying drawings, the constituent elements having substantially the same function are represented by the same reference numerals, and the duplicative description thereof is omitted.

[0017] First, the construction of the ultrasonic diagnos-

tic apparatus 1 will be described with reference to Fig. 1.

[0018] Fig. 1 is a diagram showing the construction of the ultrasonic diagnostic apparatus 1.

5 The ultrasonic diagnostic apparatus 1 according to the present invention includes an ultrasonic probe 2, transmitting means 3, bias means 4, receiving means 5, phasing adding means 6, image processing means 7, display means 8, control means 9 and operating means 10.

[0019] The ultrasonic probe 2 is brought into contact 10 with an examinee to transmit/receive an ultrasonic wave to/from the examinee. An ultrasonic wave is emitted from the ultrasonic probe 2 to the examinee, and a reflection echo signal generated from the examinee is received by the ultrasonic probe 2.

[0020] The transmitting means 3 and the bias means 4 apply a bias voltage to electrodes which are disposed in the ultrasonic probe 2 so as to confront each other, and also apply a driving signal while superposed on the bias voltage, thereby emitting an ultrasonic wave.

[0021] The receiving means 5 receives a reflection echo signal to the ultrasonic probe 2.

The receiving means 5 further performs processing such as analog-digital conversion, etc. on the received reflection echo signal.

[0022] The phasing adding means 6 is a device for phasing and adding the received reflection echo signal.

[0023] The image processing means 7 is a device for generating a diagnosis image (for example, tomogram or blood flow image) on the basis of the phased and added reflection echo signal.

[0024] The display means 8 is a display device for displaying the diagnosis image generated by the image processing means 7.

[0025] The control means 9 is a device for controlling the respective constituent elements described above.

[0026] The operating means 10 is a device for supplying the control means 9 with an instruction such as a sign for starting diagnosis or the like, for example. The operating means 10 is input equipment such as a track ball, 40 a keyboard, a mouse or the like.

[0027] Next, the ultrasonic probe 2 will be described with reference to Figs. 2 to 4.

Fig. 2 is a diagram showing the construction of the ultrasonic probe 2. Fig. 2 is a perspective view of the ultrasonic probe 2, and is a partially fractured view. The upper side of Fig. 2 is brought into contact with the examinee, and an ultrasonic wave is transmitted from the upper side.

[0028] The ultrasonic probe 2 has a cMUT chip 20. The cMUT chip 20 is a one-dimensional array type oscillator group in which plural oscillators 21-1, 21-2, ... are arranged in a strip-shape. Plural oscillating elements 28 are arranged in the oscillators 21-1, 21-2, ... The ultrasonic probe 2 shown in Fig. 2 is a linear type probe, however, another type oscillator group such as a two-dimensional array type, a convex type or the like may be used.

[0029] A backing layer 22 is provided at the back surface side (the lower side in Fig. 2) of the cMUT chip 20. An acoustic lens 26 is provided at the ultrasonic wave

emission side of the cMUT chip 20. The cMUT chip 20, the backing layer 22, etc. are stored in an ultrasonic probe cover 25.

[0030] In the cMUT chip 20, on the basis of application of a bias voltage of the bias means 4, a driving signal from the transmitting means 3 is converted to an ultrasonic wave, and the thus-converted ultrasonic wave is transmitted to an examinee.

[0031] The receiving means 5 converts an ultrasonic wave generated from the examinee to an electrical signal, and receives the ultrasonic wave as a reflection echo signal.

[0032] The backing layer 22 is a layer for absorbing propagation of an ultrasonic wave emitted from the cMUT chip 20 to the back surface side thereof to suppress surplus oscillation.

[0033] The acoustic lens 26 is a lens for converging an ultrasonic beam transmitted from the cMUT chip 20. The curvature of the acoustic lens 26 is determined on the basis of a desired focal length.

[0034] A matching layer may be provided between the acoustic lens 26 and the cMUT chip 20. The matching layer is a layer for matching the acoustic impedances of the cMUT chip 20 and the examinee to enhance the transmission efficiency of ultrasonic waves.

[0035] Fig. 3 is a diagram showing the construction of the oscillators 21 of Fig. 2.

Upper electrodes 46-1, 46-2, ... are arranged at the examinee side of the plural oscillating elements 28 constituting the oscillators 21-1, 21-2, ..., and the upper electrodes are divided into plural parts in the longitudinal axis direction X and connected to one another every oscillator 21. That is, the upper electrode 46-1, the upper electrode 46-2, ... are arranged in juxtaposition with one another in the longitudinal axis direction X.

[0036] Lower electrodes (48-1 to 48-4) are arranged at the opposite side of the plural oscillating elements 28 constituting the oscillators 21 to the examinee, and they are divided into plural parts (four lines in Fig. 3) in the short axis direction Y and connected to one another. That is, the lower electrode 48-1, the lower electrode 48-2, the lower electrode 48-3, ... are arranged in juxtaposition with one another in the short axis direction Y.

[0037] Fig. 4 is a side view (cross-sectional view) showing the construction of one of the oscillating elements 28 of Fig. 3.

[0038] The oscillating element 28 comprises a substrate 40, a film body 44, a film body 45, an upper electrode 46, a frame body 47 and a lower electrode 48. The oscillating element 28 is formed by microfabrication based on a semiconductor process. The oscillating element 28 corresponds to one element of cMUT.

[0039] The substrate 40 is a semiconductor substrate such as silicon wafer or the like, and disposed at the lower electrode side.

[0040] The film body 44 and the frame body 47 are formed of a semiconductor compound such as silicon compound or the like. The film body 44 is provided so as

to be nearest to the examinee side (ultrasonic wave emission side) in the oscillating element 28, and the frame body 47 is disposed on the back surface (the opposite side to the examinee side) of the film body 44. The upper electrode 46 is provided between the film body 44 and the frame body 47. The film body 45 is provided between the frame body 47 and the substrate 40, and the lower electrode 48 is provided in the film body 45. The internal space 50 compartmented by the frame body 47 and the film body 45 is set to a vacuum-state or filled with predetermined gas.

[0041] The upper electrode 46 and the lower electrode 48 are connected to the transmitting means 3 for supplying a AC high-frequency voltage as a driving signal and the bias means 4 for applying a DC voltage as a bias voltage.

[0042] When an ultrasonic wave is transmitted, a DC bias voltage (Va) is applied to the upper electrode 46 and the lower electrode 48 of the oscillating element 28, and electric field is generated by the bias voltage (Va). Tension occurs in the film body 44 due to the generated electric field, and it has a predetermined electromechanical coupling coefficient (Sa). When a driving signal is supplied from the transmitting means 3 to the upper electrode 46, an ultrasonic wave whose intensity is based on the electromechanical coupling coefficient (Sa) is emitted from the film body 44.

[0043] Furthermore, when another DC bias voltage (Vb) is applied to the upper electrode 46 and the lower electrode 48 of the oscillating element 28, electric field is generated by the bias voltage (Vb). Tension occurs in the film body 44 due to the generated electric field, and it has a predetermined electromechanical coupling coefficient (Sb). When a driving signal is supplied from the transmitting means 3 to the upper electrode 46, an ultrasonic wave whose intensity is based on the electromechanical coupling coefficient (Sb) is emitted from the film body 44.

[0044] Here, when the bias voltage satisfies "Va<Vb", the electromechanical coupling coefficient satisfies "Sa<Sb".

[0045] On the other hand, when an ultrasonic wave is received, the filmbody 44 is excited by a reflection echo signal generated from the examinee, and the capacitance of the internal space 50 varies. The variation amount of the internal space 50 is detected as an electrical signal through the upper electrode 46.

[0046] The electromechanical coupling coefficient of the oscillating element 28 is determined by tension loaded on the film body 44. Accordingly, the tension of the film body 44 is controlled by changing the magnitude of the bias voltage applied to the oscillating element 28, whereby the intensity (or sound pressure, amplitude) of the ultrasonic wave emitted from the oscillating element 28 can be varied even when a driving signal having the same amplitude is input.

[0047] Next, an example 1 of the present invention will be described with reference to Figs. 5 and 6.

Fig. 5 is a diagram showing an ultrasonic probe 2 according to an example 1. Fig. 5 is a cross-sectional view of a plan A of the ultrasonic probe of Fig. 2.

According to Fig. 5, the back surface of the acoustic lens 26 (the opposite side to the examinee disposing side, the backing layer side) has a recess portion so that the cMUT chip 20 is disposed in the recess portion. The connection portion

(wire preventing resin 88) between the cMUT chip 20 and a flexible substrate 72 is fitted in the recess portion.

[0048] The cMUT chip 20 is mounted on the upper surface of the backing layer 22 through an adhesive layer 70. The flexible substrate 72 (Flexible Printed Circuits: FPC) is provided so as to extend from the peripheral edge of the upper surface of the backing layer 22 to the four side surfaces. The flexible substrate 72 is mounted on the peripheral edge of the upper surface of the backing layer 22 through an adhesive layer 71.

[0049] The adhesive layer 70 and the adhesive layer 71 are adhesive agent formed of epoxy resin, for example. The position in the height direction of the cMUT chip 20 and the flexible substrate 72 can be adjusted by arbitrarily adjusting the layer thickness of the adhesive layer 70 and the adhesive layer 71.

[0050] The flexible substrate 72 and the cMUT chip 20 are electrically connected to each other through a wire 86. The wire 86 is connected by a wire bonding method. An Au wire or the like may be used as the wire 86. Wire sealing resin 88 is filled around the wire 86.

[0051] The acoustic lens 26 is mounted on the upper surface of the cMUT chip 20 through an adhesive layer 90. Silicon rubber is used as a material of the acoustic lens 26, for example. With respect to the material of the adhesive layer 90, the same material as the acoustic lens 26 (for example, silicon) is desired.

[0052] The upper surface of the acoustic lens 26 is designed in a convex shape within at least an area 23 which corresponds to an area where an ultrasonic wave is emitted. The oscillating element 28 is disposed within the range corresponding to at least the area 23. An ultrasonic wave is emitted from the convex portion at the ultrasonic wave emission side (the examinee side) of the acoustic lens 26.

[0053] The ultrasonic probe cover 25 is provided to the four side surfaces of the ultrasonic probe 2. The ultrasonic probe cover 25 is fixed to the four side surfaces of the acoustic lens 26. An examiner manipulates the ultrasonic probe 2 while gripping the ultrasonic probe cover 25 by his/her hand.

[0054] Fig. 6 is a diagram showing the wires of the ultrasonic probe 2.

The substrate 40 of the cMUT chip 20 is fixed to the upper surface of the backing layer 22. The flexible substrate 72 is fixed to the peripheral edge of the upper surface of the backing layer 22.

[0055] On the flexible substrate 72 are disposed signal patterns 38-1 to signal patterns 38-n which are respectively paired at the upper and lower sides on the paper

surface, and signal patterns 41-1 to signal patterns 41-4 which are respectively paired at the right and left sides on the paper surface.

[0056] The upper electrode 46-1 to the upper electrode 46-n are connected to the signal pattern 38-1 to the signal pattern 38-n. The lower electrode 48-1 to the lower electrode 48-4 are connected to the signal pattern 41-1 to the signal pattern 41-4. The adjacent lower electrodes 48-1 to 48-4 are respectively insulated from one another.

[0057] The upper electrodes 46 and the lower electrodes 48 are respectively connected to the flexible substrate 72 through wires 86 by the wire bonding method.

[0058] The shape of the lower electrodes 48-1 to 48-4 is desirably set to the shape (for example, wave form) corresponding to the shape of the oscillating element 28 (for example, hexagonal shape), whereby each oscillating element 28 can be disposed so as to correspond to only one of the lower common electrodes 48-1 to 48-4.

[0059] Furthermore, the four lower electrodes 48-1 to 48-4 are disposed, however, the number of the lower electrodes is not limited to this value.

[0060] The signal patterns 38-1 to 38-n are provided so as to be paired at both the upper and lower sides on the paper surface, and the signal patterns 48-1 to 48-4 are provided so as to be paired at both the right and left sides on the paper surface. However, the present invention is not limited to this arrangement, and they may be provided at only one side without being paired.

[0061] The signal pattern and the upper electrode or the lower electrode are connected to each other by the wire bonding method. However, the present invention is not limited to this method, and a flip chip bonding method for connecting them through pads may be used.

35 Example 1

[0062] First, an example 1 of the present invention will be described.

In this example, the backing layer 22 which absorbs propagation of ultrasonic waves emitted from the cMUT chip 20 to the back surface side to suppress surplus oscillation has the following feature.

[0063] First, with respect to the backing layer according to this example, the linear expansion coefficient thereof is set to a low value so that it approaches to that of the substrate 40 (silicon wafer) of the cMUT chip 20.

[0064] When there is some difference in linear expansion coefficient between the substrate 40 (for example, silicon wafer) of the cMUT chip 20 and the backing layer, there has been hitherto a problem in long-term reliability of the probe because unnecessary friction or force is applied to the contact surface every time temperature rises and thus the contact surface is worn. For example, silicon wafer has a linear expansion coefficient of about 3 ppm/°C, and the conventional backing layer is formed of materials such as metal and resin and thus has a linear expansion coefficient of 100 ppm/°C order. However, with respect to the backing layer according to this exam-

ple, the linear expansion coefficient thereof is set to 50 ppm/ $^{\circ}\text{C}$, for example. Therefore, the long-term reliability problem of the probe which is caused by the application of unnecessary friction or force to the contact surface every increase of temperature and thus wearing of the contact surface can be solved.

[0065] More specifically, a new material for reducing the linear expansion coefficient of the backing layer is mixed in the backing layer as thermal effect reducing means for reducing a thermal effect caused by the difference in linear expansion coefficient between the backing layer and the substrate of the cMUT chip. The new material is a third material different from the metal (for example, tungsten, ferrite, Pt, ceramic fine particle, etc.) and the resin (for example, polyamide resin, epoxy resin, copolymer of vinyl chloride and vinyl acetate, rubber, etc.) which constitute the backing layer. The third material is formed of glass-like material containing silica (SiO_2) as a main component, and it may be glass fiber or the like, for example. The new material has a linear expansion coefficient of about 0.5 ppm/ $^{\circ}\text{C}$ (in the case of quartz glass) or about 9 ppm/ $^{\circ}\text{C}$ (in the case of general glass), and thus the linear expansion coefficient of the backing layer can be reduced as a whole. The linear expansion coefficient of the backing layer is near to 50 to 60 ppm/ $^{\circ}\text{C}$ as a whole with respect to the linear expansion coefficient (3 ppm/ $^{\circ}\text{C}$) of the substrate (for example, silicon wafer) of the cMUT chip 20. The difference in linear expansion coefficient between the backing layer and the substrate of the cMUT chip may be set within the difference range of 50 to 60 ppm/ $^{\circ}\text{C}$, and more preferably the linear expansion coefficient is coincident therebetween.

[0066] The backing material according to this example contains tungsten particles (W particle), polyamide resin and glass fiber.

In the above example, the difference in linear expansion coefficient between the substrate of the cMUT chip 20, for example, silicon wafer and the backing layer is reduced, and the situation that unnecessary friction or force is applied to the contact surface every increase of temperature and thus the contact surface is worn is suppressed, and thus the long-term reliability of the probe can be secured.

[0067] By forming the backing layer of the material as described above, the acoustic impedance of the backing material itself is equal to 7 to 20 MRayl, and it is not necessarily coincident with the impedance of the substrate of the cMUT chip (for example, 20 MRayl), however, this does not greatly affect the acoustic characteristic from an experiment result obtained by the inventor. According to the structure of this example, it is not necessarily required that a material having an intermediate linear expansion coefficient is installed between the cMUT chip and the backing material, and thus there is an advantage that the manufacturing can be easily performed.

Furthermore, in the backing layer disclosed in this invention, the thermal deformation temperature is equal to

about 210°C under 0.45 MPa, and thus thermal deformation hardly occurs even when thermal temperature increase occurs.

5 Example 2

[0068] Next, an example 2 of the present invention will be described.

Absorbing means for absorbing the difference in linear expansion coefficient between the cMUT chip and the backing layer is provided between the cMUT chip and the backing layer of this example as thermal effect reducing means for reducing a thermal effect caused by the difference in linear expansion coefficient between the backing layer and the cMUT chip substrate. The absorbing means is formed of a material having a small elasticity modulus.

[0069] In this example, the adhesive agent for bonding the cMUT chip and the backing layer is formed of a material having small elasticity modulus. Since the adhesive agent having a small elasticity modulus is used, the difference in linear expansion coefficient between the cMUT chip and the backing layer can be absorbed. Therefore, the situation that unnecessary friction or force is applied to the contact surface every increase of temperature and thus the contact surface is worn is suppressed, and thus an ultrasonic probe having long-term reliability can be provided.

[0070] More specifically, the absorbing means according to this example has a smaller elasticity modulus as compared with any one or both of the substrate constituting the cMUT chip and the backing layer. For example, a material which has rubber elasticity (for example, elongation percentage of 100% or more) when it is hardened and has adhesiveness to materials at the interface may be considered as the material constituting the absorbing means. When the material constituting the absorbing means is adhesive agent, epoxy resin type adhesive agent, polyurethane type adhesive agent, silicon type adhesive agent or the like which has low elasticity is considered.

[0071] According to the above example, with respect to the ultrasonic probe using the cMUT chip and the ultrasonic diagnostic apparatus using the ultrasonic probe, the structure of the backing layer for absorbing the propagation of ultrasonic waves emitted from the back surface side of the cMUT chip can suppress the effect of the thermal stress.

[0072] The preferred embodiment of the medical image display device according to the present invention has been described, however, the present invention is not limited to the above embodiment. It is apparent that persons skilled in the art can make various modifications or alterations within the scope of the technical idea disclosed in this application, and it is necessarily understood that these modifications and alterations belong to the technical compass of this invention.

Claims

1. An ultrasonic probe including:

a cMUT chip that has plural oscillation elements whose electromechanical coupling coefficient or sensitivity varies in accordance with a bias voltage and transmits/receives an ultrasonic wave; an acoustic lens provided at an ultrasonic wave transmission/reception side of the cMUT chip; a backing layer provided to the opposite surface of the cMUT chip to the acoustic lens; and a substrate provided between the backing layer and the cMUT chip, **characterized by** further comprising thermal stress suppressing means for suppressing thermal stress occurring due to the difference in linear expansion coefficient caused by temperature variation between the substrate and the backing layer.

2. The ultrasonic probe according to claim 1, wherein the thermal stress suppressing means is formed of a material which makes the backing layer and the cMUT chip substantially coincident with each other in linear expansion coefficient.

3. The ultrasonic probe according to claim 1, wherein the thermal stress suppressing means is formed of a third material having a linear expansion coefficient different from those of metal and resin constituting the backing layer.

4. The ultrasonic probe according to claim 3, wherein the third material is a material containing silicon dioxide as a main component.

5. The ultrasonic probe according to claim 3, wherein the third material is a fibrous material containing silicon dioxide as a main component.

6. The ultrasonic probe according to claim 3, wherein the metal is tungsten, and the fiber is polyamide resin.

7. The ultrasonic probe according to claim 2, wherein the linear expansion coefficient of the backing layer is equal to 50 to 60 ppm/ $^{\circ}\text{C}$ with respect to the linear expansion coefficient of the semiconductor substrate.

8. The ultrasonic probe according to claim 1, wherein the thermal stress suppressing means is provided with an adjusting layer for adjusting the difference of the linear expansion coefficient between the cMUT chip and the backing layer.

9. The ultrasonic probe according to claim 8, wherein the adjusting layer is formed of a material having a

smaller modulus of elasticity than the cMUT chip and/or the backing layer.

10. The ultrasonic probe according to claim 8, wherein the adjusting layer is an adhesive layer for fixing the cMUT chip to the backing layer, and formed of a material having a smaller modulus of elasticity than the cMUT chip and/or the backing layer.

10 11. The ultrasonic probe according to claim 10, wherein the adhesive layer is formed of adhesive agent of epoxy type adhesive agent, polyurethane type adhesive agent or silicon type adhesive agent.

15 12. The ultrasonic probe according to claim 8, wherein the adjusting layer is formed of a material that has rubber elasticity when hardened and has adhesive-ness to materials at the interface.

20 13. An ultrasonic diagnostic apparatus comprising an ultrasonic probe for transmitting/receiving an ultrasonic wave to an examinee, an image processor for constituting an ultrasonic image on the basis of an ultrasonic reception signal output from the ultrasonic probe, and a display unit for displaying the ultrasonic image, **characterized in that** the ultrasonic probe is an ultrasonic probe described in any one of claims 1 to 12.

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FIG.1

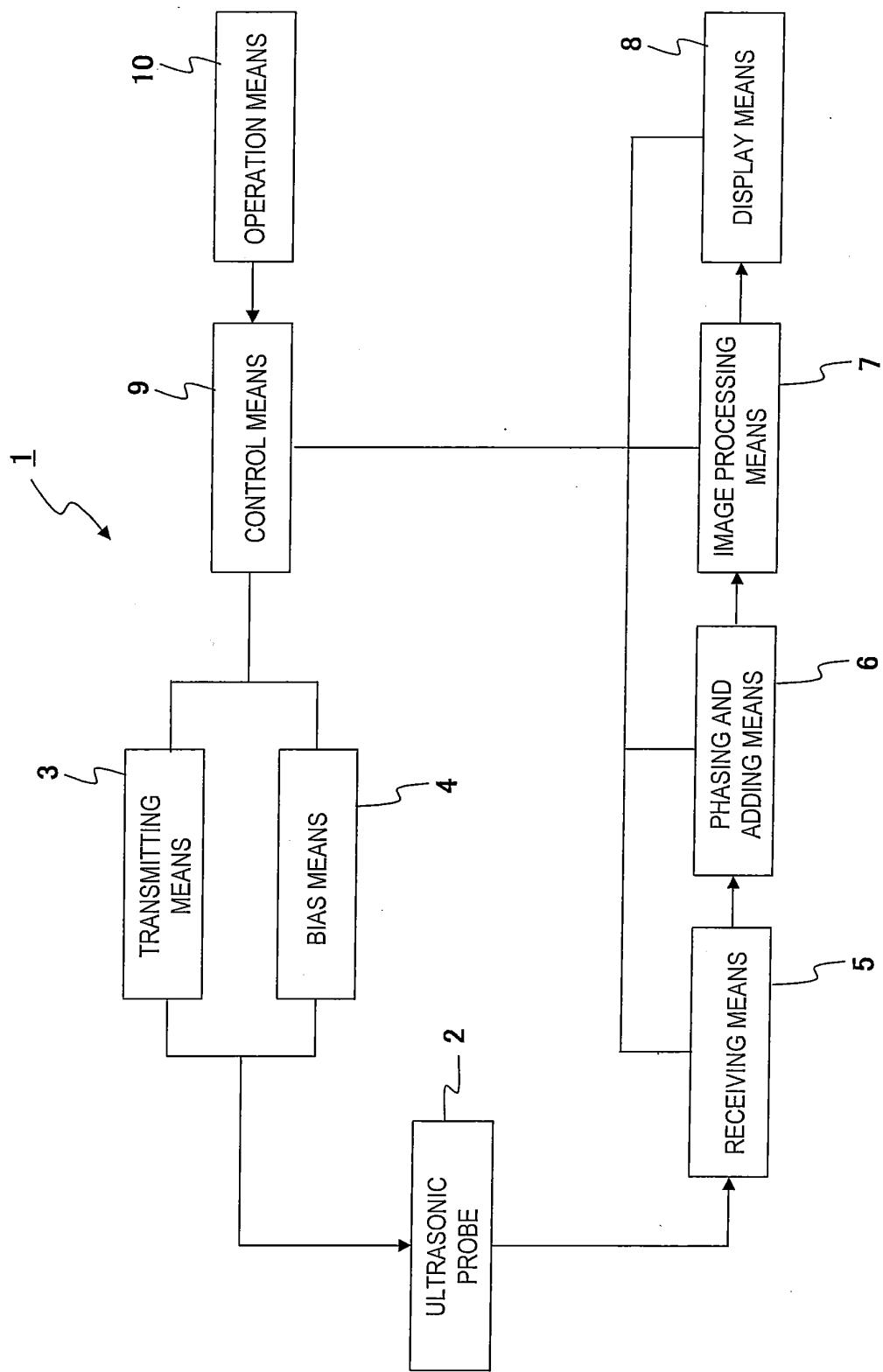


FIG.2

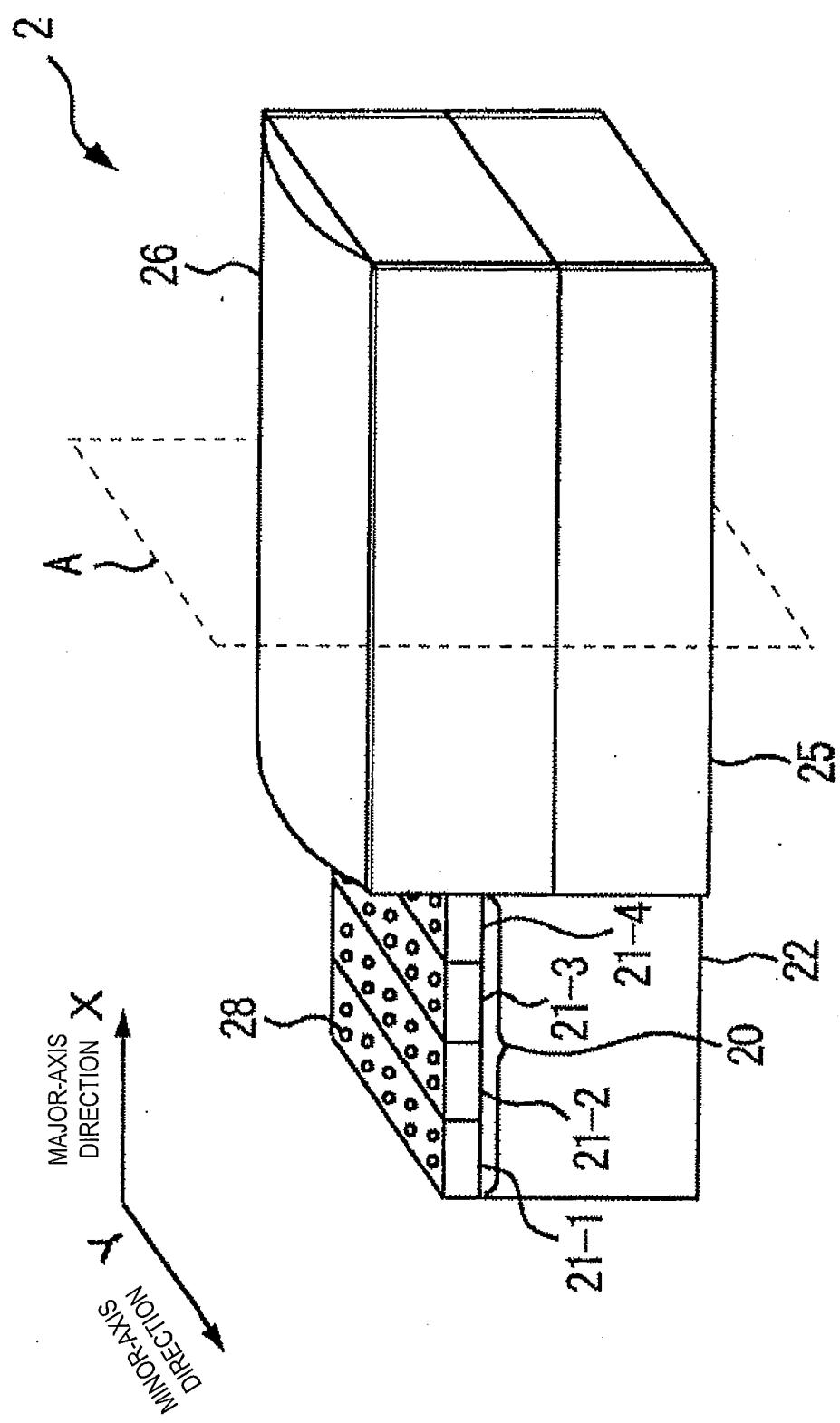


FIG.3

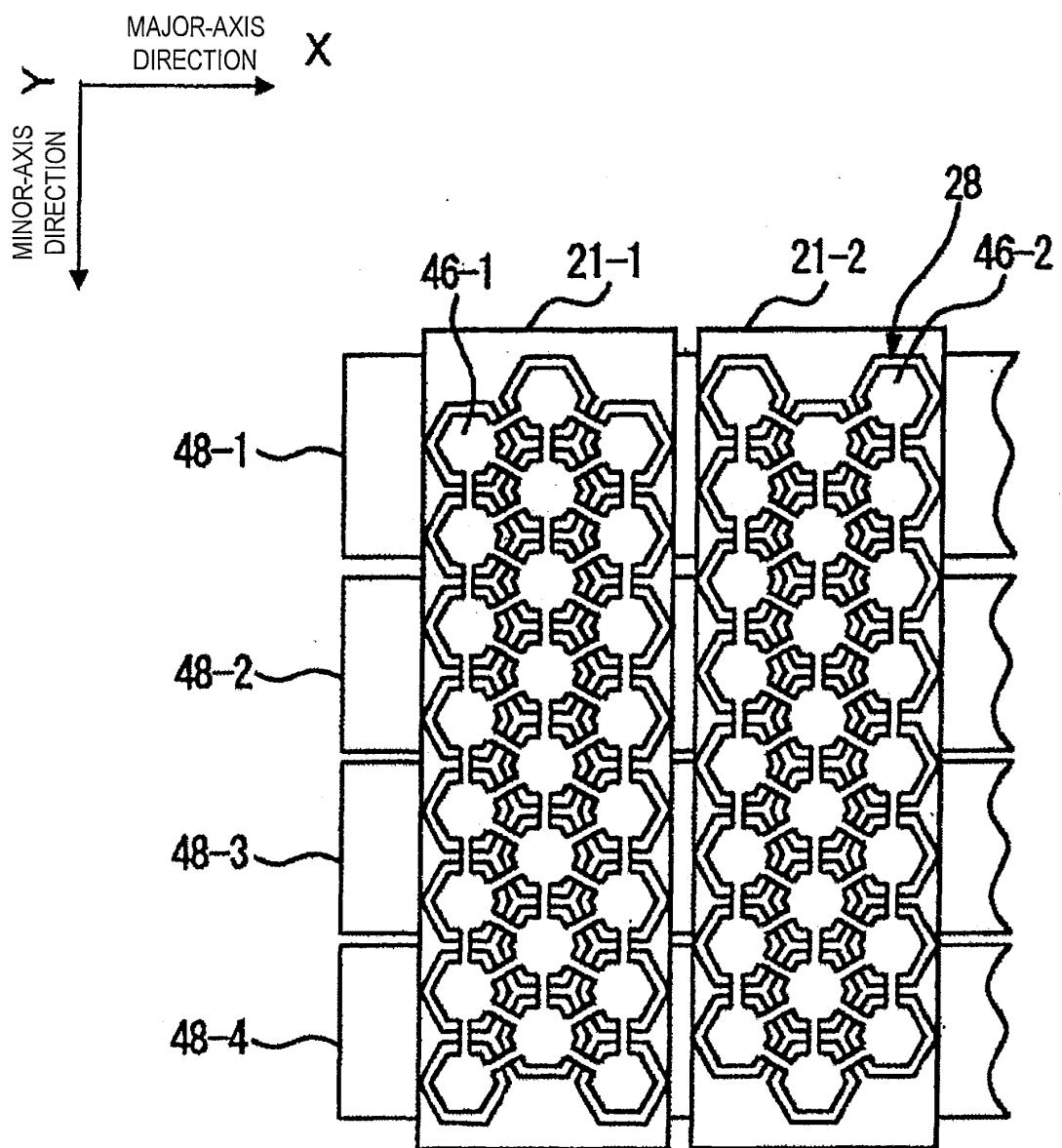


FIG.4

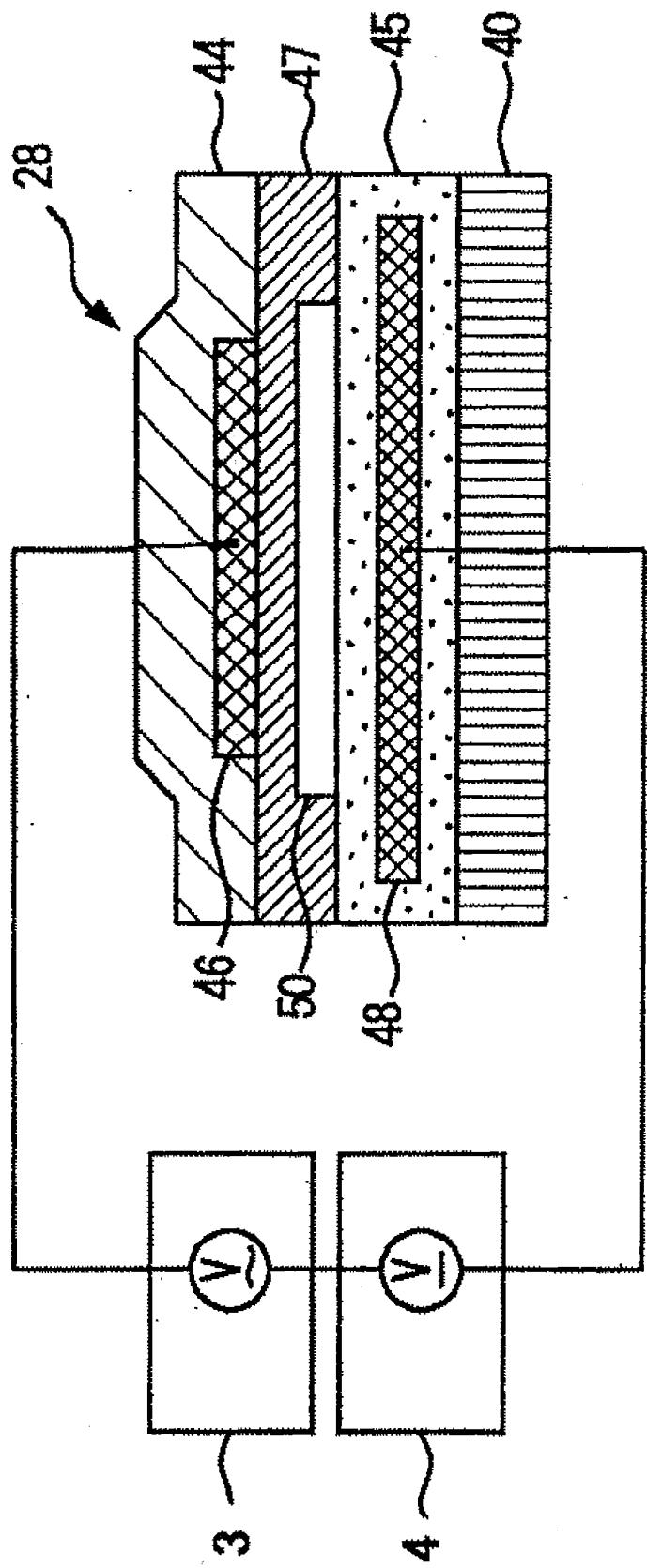


FIG.5

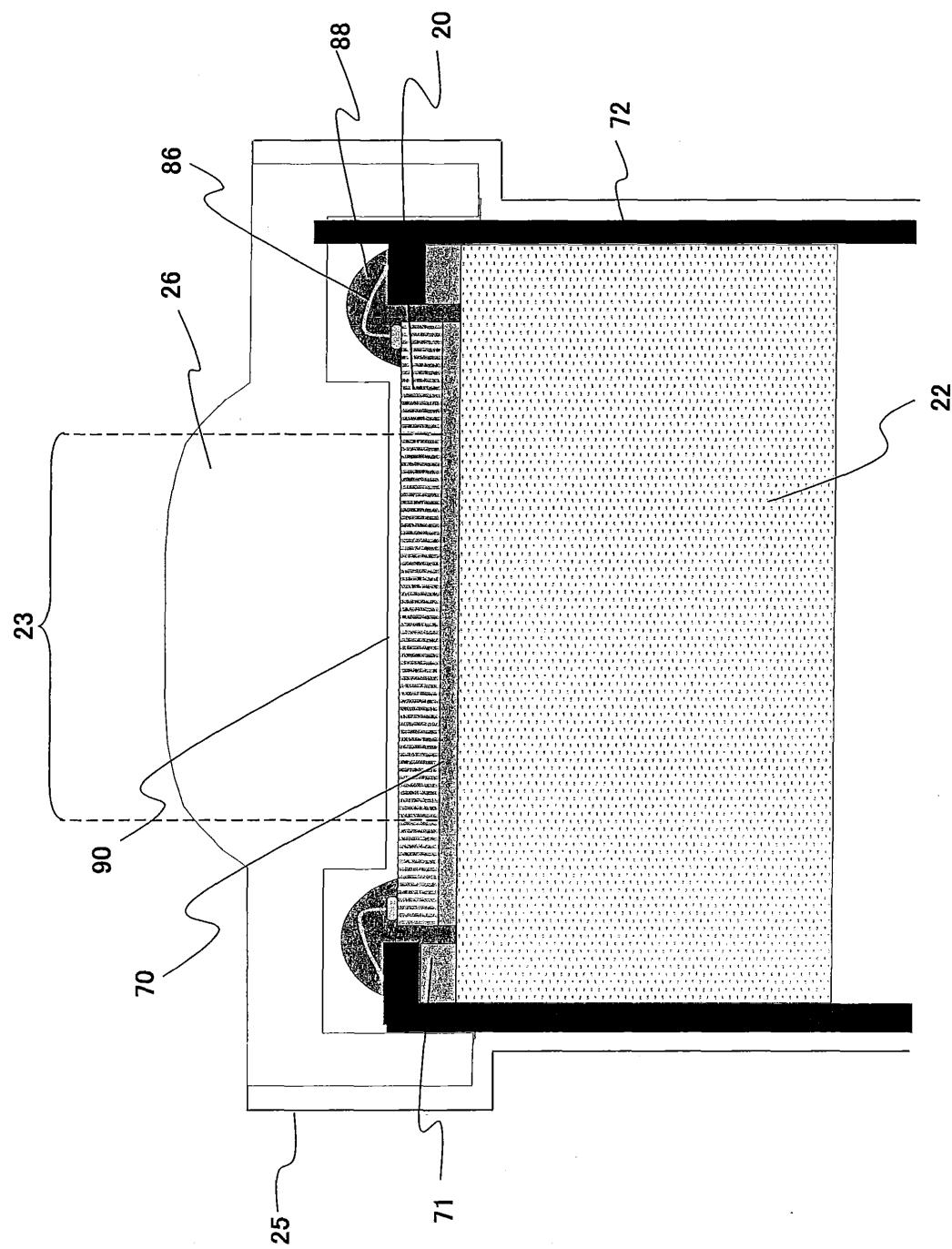
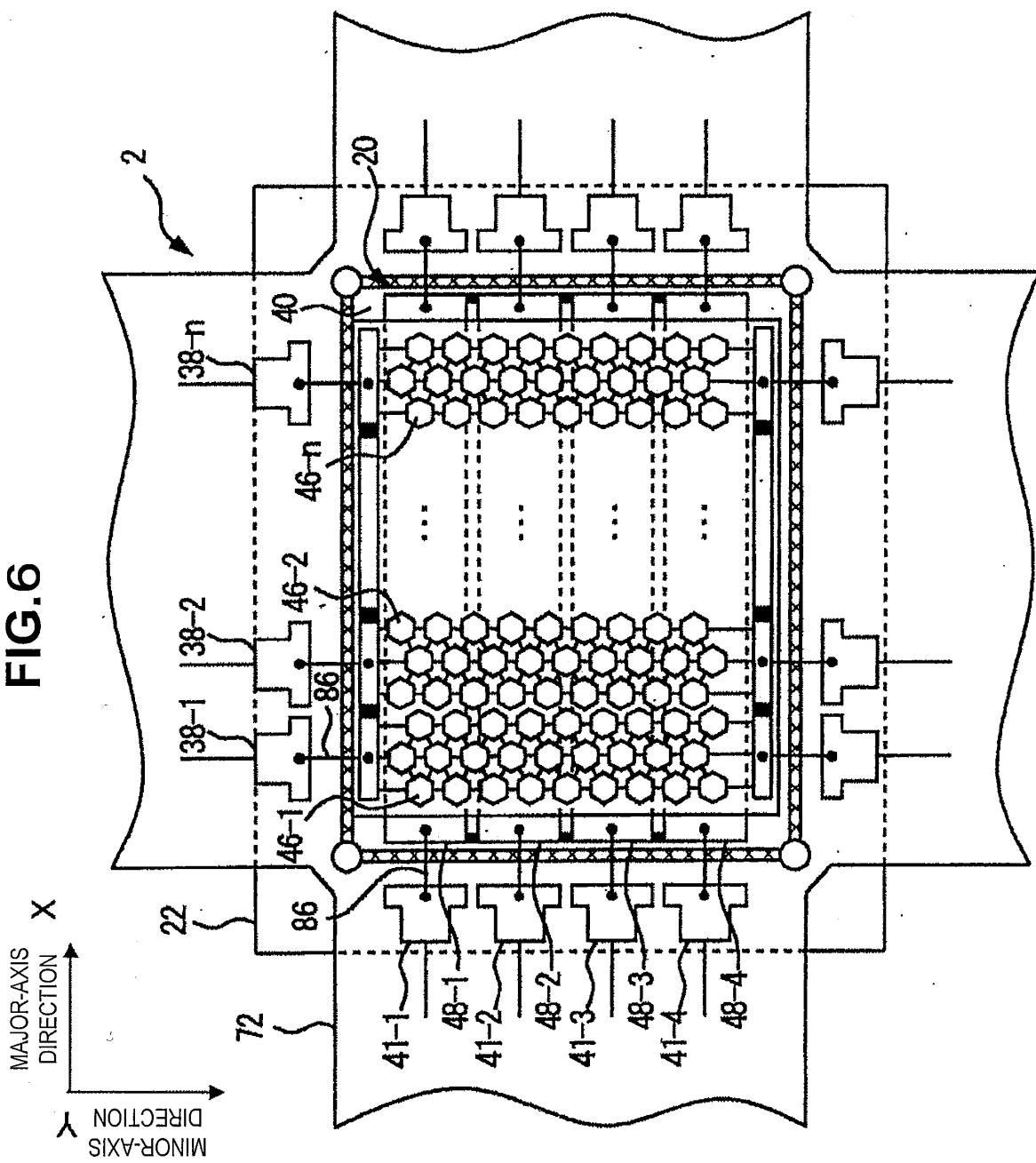


FIG. 6



INTERNATIONAL SEARCH REPORT		International application No. PCT/JP2008/071239												
A. CLASSIFICATION OF SUBJECT MATTER <i>H04R19/00 (2006.01) i, A61B8/00 (2006.01) i, G01N29/24 (2006.01) i</i>														
According to International Patent Classification (IPC) or to both national classification and IPC														
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) <i>H04R19/00, A61B8/00, G01N29/24</i>														
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched <i>Jitsuyo Shinan Koho 1922-1996 Jitsuyo Shinan Toroku Koho 1996-2009 Kokai Jitsuyo Shinan Koho 1971-2009 Toroku Jitsuyo Shinan Koho 1994-2009</i>														
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)														
C. DOCUMENTS CONSIDERED TO BE RELEVANT <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left; padding: 2px;">Category*</th> <th style="text-align: left; padding: 2px;">Citation of document, with indication, where appropriate, of the relevant passages</th> <th style="text-align: left; padding: 2px;">Relevant to claim No.</th> </tr> </thead> <tbody> <tr> <td style="text-align: center; padding: 2px;">Y</td> <td style="padding: 2px;">JP 2007-201753 A (Hitachi, Ltd. (et al.)), 09 August, 2007 (09.08.07), Par. Nos. [0009] to [0027]; Figs. 1 to 5 & WO 2007/086180 A1</td> <td style="text-align: center; padding: 2px;">1,2,7,13</td> </tr> <tr> <td style="text-align: center; padding: 2px;">Y</td> <td style="padding: 2px;">JP 62-115197 A (Fujitsu Ltd.), 26 May, 1987 (26.05.87), Page 2, upper right column, line 18 to page 3, upper right column, line 9 (Family: none)</td> <td style="text-align: center; padding: 2px;">1,2,7,13</td> </tr> </tbody> </table>			Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.	Y	JP 2007-201753 A (Hitachi, Ltd. (et al.)), 09 August, 2007 (09.08.07), Par. Nos. [0009] to [0027]; Figs. 1 to 5 & WO 2007/086180 A1	1,2,7,13	Y	JP 62-115197 A (Fujitsu Ltd.), 26 May, 1987 (26.05.87), Page 2, upper right column, line 18 to page 3, upper right column, line 9 (Family: none)	1,2,7,13			
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<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.														
<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%; padding: 2px;">* Special categories of cited documents:</td> <td style="width: 50%; padding: 2px;">"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</td> </tr> <tr> <td style="padding: 2px;">"A" document defining the general state of the art which is not considered to be of particular relevance</td> <td style="padding: 2px;">"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</td> </tr> <tr> <td style="padding: 2px;">"E" earlier application or patent but published on or after the international filing date</td> <td style="padding: 2px;">"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</td> </tr> <tr> <td style="padding: 2px;">"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</td> <td style="padding: 2px;">"&" document member of the same patent family</td> </tr> <tr> <td style="padding: 2px;">"O" document referring to an oral disclosure, use, exhibition or other means</td> <td></td> </tr> <tr> <td style="padding: 2px;">"P" document published prior to the international filing date but later than the priority date claimed</td> <td></td> </tr> </table>			* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention	"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone	"E" earlier application or patent but published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art	"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family	"O" document referring to an oral disclosure, use, exhibition or other means		"P" document published prior to the international filing date but later than the priority date claimed	
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"O" document referring to an oral disclosure, use, exhibition or other means														
"P" document published prior to the international filing date but later than the priority date claimed														
Date of the actual completion of the international search 17 February, 2009 (17.02.09)		Date of mailing of the international search report 03 March, 2009 (03.03.09)												
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer												
Facsimile No.		Telephone No.												

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2008/071239

Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:

3. Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

For the following reasons, this international patent application contains the three inventions which do not comply with the requirement of unity of invention.

Main invention: claims 1, 2, 7 and 13

Second invention: claims 3 - 6

Third invention: claims 8 - 12

Document 1: JP2007-201753A (Hitachi, Ltd. (et al.)), 9 August, 2007 (09.08.07), [0009] - [0027], and Figs. 1 - 5
(continued to extra sheet)

1. As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. As all searchable claims could be searched without effort justifying additional fees, this Authority did not invite payment of additional fees.
3. As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:

4. No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:
Claims 1, 2, 7 and 13.

Remark on Protest

the

- The additional search fees were accompanied by the applicant's protest and, where applicable, payment of a protest fee.
- The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- No protest accompanied the payment of additional search fees.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2008/071239

Continuation of Box No.III of continuation of first sheet (2)

Document 2: JP 62-115197 A (Fujitsu Ltd.), 26 May, 1987 (26.05.87), line 18, upper right column, page 2 - line 9, upper right column, page 3.

Searches have been performed by assuming that claims 1, 2 and 7 are "the firstly described invention (i.e., the "main invention")". As a result, it is clarified by Documents 1 and 2 that the technical feature of claim 1 and 2 does not involve any inventive step. The "cMUT chip" in claim 1 is disclosed as the "ultrasonic transmit-receive element" of Document 1. The "acoustic lens" in claim 1 is disclosed as the "acoustic lens" of Document 1. The "backing layer" in claim 1 is disclosed as the "backing layer" in Document 1. The "substrate" in claim 1 is disclosed as the "second substrate" of Document 1.

Document 1 does not describe the matter of having "thermal-stress suppressing means" of materials in which "the linear expansion coefficient of said backing layer and the linear expansion coefficient of said cMUT chip are substantially identical" as in claim 2. However, it is the well-known technical matter, as described in Document 2, that the thermal stress is suppressed by making the linear expansion coefficient of the backing layer closer to that of the ultrasonic transmit-receive element. Therefore, the concept that the thermal stress is suppressed by making the linear expansion coefficient of the backing layer closer to that of the ultrasonic transmit-receive element as in Document 1 is what a person skilled in the art could have easily assumed. Here, setting the substantially identical linear expansion coefficient is only a design matter.

Hence, the technical feature of claims 1 and 2 is not considered as the "special technical feature" within the meaning of PCT Rule 13.2, second sentence.

So long as claim 7 and the aforementioned prior art are compared, moreover, the "(tentative) special technical feature" of the main invention resides in that "the linear expansion coefficient of said backing layer is 50 - 60 ppm/ $^{\circ}$ C with respect to the linear expansion coefficient of said semiconductor substrate".

So long as claim 3 (the second invention) and the aforementioned prior art are compared, moreover, the "(tentative) special technical feature" of the second invention resides in that "said thermal-stress suppressing means is made of a third material having a linear expansion coefficient different from those of the metal and resin constituting said backing layer".

So long as claim 8 (the third invention) and the aforementioned prior art are compared, moreover, the "(tentative) special technical feature" of the third invention resides in that "said thermal-stress suppressing means includes an adjusting layer between said cMUT chip and said backing layer for adjusting the difference in said linear expansion coefficients".

Hence, the main, second and third inventions are not so technically related as to involve one or two or more of the same or corresponding special technical features.

Here, claim 13 belongs to the same invention category as that of claim 1, since it is just an addition of the well-known technique to claim 1.

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

- US 5894452 A [0005]
- US 6714484 B [0005]

专利名称(译)	超声波探头和使用该探头的超声波诊断装置		
公开(公告)号	EP2227037A4	公开(公告)日	2016-11-30
申请号	EP2008853854	申请日	2008-11-21
[标]申请(专利权)人(译)	株式会社日立医药		
申请(专利权)人(译)	日立医疗器械股份有限公司		
当前申请(专利权)人(译)	株式会社日立制作.		
[标]发明人	SANO SHUZO FUKADA MAKOTO SAKO AKIFUMI		
发明人	SANO, SHUZO FUKADA, MAKOTO SAKO, AKIFUMI		
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其他公开文献	EP2227037A1		
外部链接	Espacenet		

摘要(译)

一种超声波探头，包括：cMUT芯片，具有机电耦合系数或灵敏度根据偏置电压而变化并且发送/接收超声波的多个振动元件；声透镜，设置在所述cMUT芯片的超声波发送/接收侧；设置在cMUT芯片与声透镜的相对表面上的背衬层，以及设置在背衬层和cMUT芯片之间的基板。超声波探头还包括热应力抑制装置，用于抑制由于基板和背衬层之间的温度变化引起的线膨胀系数的差异而产生的热应力。