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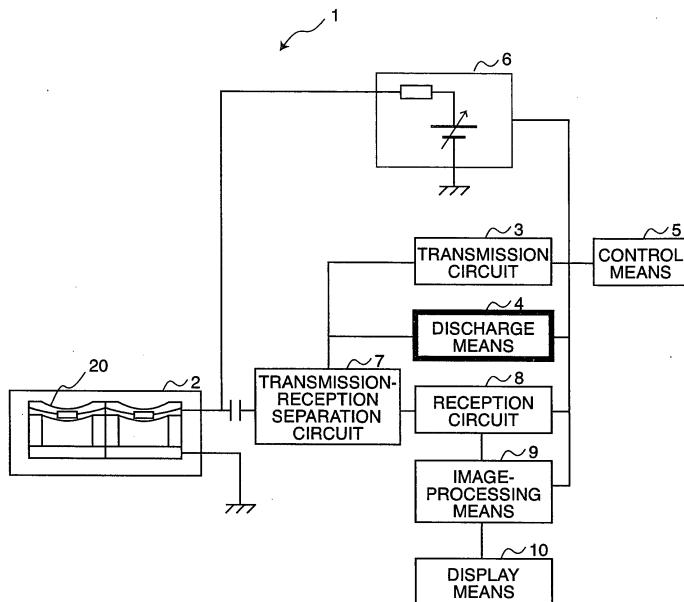
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(54) ULTRASOUND DIAGNOSTIC APPARATUS AND METHOD OF CALIBRATING THE SAME

(57) An ultrasound diagnostic apparatus 1 causes discharge means 4 to apply a discharge pulse (having a peak value equal to or greater than a collapse voltage) between electrodes of an ultrasound transducer 20 a plurality of time while inverting the polarity of the discharge pulse each time, to thereby accelerate discharge of the charge accumulated in the diaphragm of the ultrasound

transducer 20, so that a transmission-reception sensitivity offset can be caused to quickly approach zero. The charge is discharged instantaneously. That is, it is possible to quickly calibrate a transmission-reception sensitivity drift stemming from time-course accumulation of charge flowing into the diaphragm between the electrodes upon application of a DC bias thereto.

FIG.1



Description

TECHNICAL FIELD

[0001] The present invention relates to an ultrasound diagnostic apparatus equipped with an ultrasound probe in which a DC bias is applied between electrodes thereof and which transmits and receives ultrasound waves, and to a method of calibrating such an ultrasound diagnostic apparatus.

The present application claims priority benefits of Paris Convention from Japanese Patent Application No. 2005-333797 duly filed under the Japanese Patent Law, the contents of which are incorporated herein by reference.

BACKGROUND ART

[0002] Conventionally, a capacitive micro-machined ultrasound transducer (cMUT) has been manufactured through use of a semiconductor deposition technique. The cMUT is a very small diaphragm-like device which includes electrodes for converting a sonic vibration of an ultrasound signal to a modulated capacitance. The cMUT has a sacrificial layer between the electrodes. A DC bias and AC pulses are applied between the electrodes so as to modulate a capacitance charge to thereby generate an electric field, so that a diaphragm vibrates and thus an ultrasound wave is generated.

Further, there has been proposed a capacitive micro-machined ultrasound transducer (cMUT) whose capacitance is monitored by means of a capacitance regulator so as to adjust a bias charge thereof. The capacitance regulator measures a minute AC voltage stemming from charging or discharging at a diaphragm electrode to thereby measure the capacitance of the cMUT. Thus, the bias charge is produced and maintained (see, for example, "Patent Document 1").

[0003] Patent Document 1: Japanese *kohyo* (PCT) Patent Publication No. 2004-503312

DISCLOSURE OF THE INVENTION

PROBLEMS TO BE SOLVED BY THE INVENTION

[0004] However, such a cMUT has, between the electrodes, not only a sacrificial layer, but also a diaphragm which prevents formation of a short circuit and has a conductivity of about 10^{11} to 10^{14} Ω/m , and a rim for providing a vertical clearance between the electrodes. Therefore, such a cMUT has a problem in that when a DC bias is applied between the electrodes, a leak current flows (a charge moves) into the diaphragm and the rim between the electrodes in accordance with the impedance thereof in a circuit determined by the conductivity, shape, etc., although the leak current is slight.

[0005] In particular, once injection of a charge into the diaphragm occurs, due to influence of the inter-electrode

field intensity, the sensitivity associated with transmission and reception of ultrasound waves (hereinafter referred to as "transmission-reception sensitivity") changes with time. Further, the charge injected into the diaphragm remains within the diaphragm even after the application of the DC bias is interrupted. Due to application of the DC bias over a long period, charge accumulates in the diaphragm, and the charge accumulated in the diaphragm causes a drift in the sensitivity associated with transmission and reception of ultrasound waves (hereinafter referred to as "transmission-reception sensitivity drift"), resulting in generation of a transmission-reception sensitivity distortion. The transmission-reception sensitivity distortion causes an increase in harmonic components.

[0006] In the technique disclosed in "Patent Document 1", charging or discharging occurs at the electrodes. However, such charging or discharging does not remove the charge injected into an insulating portion between the electrodes. Therefore, the disclosed technique has a problem in that it cannot suppress transmission-reception sensitivity distortion stemming from the above-described transmission-reception sensitivity drift.

[0007] The present invention has been accomplished in view of the above-described problems, and an object of the present invention is to provide an ultrasound diagnostic apparatus which quickly calibrates a transmission-reception sensitivity drift stemming from time-course accumulation of charge flowing into an insulating portion between electrodes upon application of a DC bias thereto.

MEANS FOR SOLVING THE PROBLEMS

[0008] In order to achieve the above-described object, according to a first invention, there is provided an ultrasound diagnostic apparatus comprising an ultrasound probe equipped with an ultrasound transducer including a pair of electrodes and an insulating portion and a sacrificial layer provided between the electrodes; DC-bias application means for applying a DC bias between the electrodes; transmission means for causing a portion of the insulating portion to vibrate through application of an ultrasound transmission pulse between the electrode in addition to the DC bias, to thereby transmit an ultrasound wave toward a subject; reception means for receiving an ultrasound echo from the subject; and image-processing means for producing an ultrasound image on the basis of a signal output from the reception means, the ultrasound diagnostic apparatus being characterized by further comprising discharge means for discharging a charge accumulated in the insulating portion between the electrodes.

[0009] The ultrasound diagnostic apparatus is configured such that any charge which is accumulated in the insulating portion between the electrodes as a result of application of a DC bias over a long time is discharged through application of a predetermined voltage between

the electrodes of the ultrasound transducer at least one time such that the polarity of the voltage is inverted repeatedly.

With this operation, the ultrasound diagnostic apparatus accelerates discharge of the charge accumulated in the insulating portion between the electrodes, so that the transmission-reception sensitivity offset quickly approaches zero. That is, the ultrasound diagnostic apparatus can quickly calibrate a transmission-reception sensitivity drift stemming from time-course accumulation of charge which flows into a diaphragm between electrodes upon application of a DC bias thereto.

[0010] Preferably, the discharge means accelerates the discharge of the charge through application of a voltage between the electrodes, the voltage being equal to or higher than a collapse voltage.

With this, a collapse phenomenon occurs in the ultrasound transducer, and the gap width of the sacrificial layer becomes zero. As a result, the impedance decreases sharply, so that the charge discharge speed increases. The discharge of charge takes place instantaneously.

[0011] According to a second invention, there is provided a method of calibrating an ultrasound diagnostic apparatus comprising an ultrasound probe equipped with an ultrasound transducer including a pair of electrodes and an insulating portion and a sacrificial layer provided between the electrodes, and DC-bias application means for applying a DC bias between the electrodes, the method being characterized by comprising a monitor step of monitoring an amount of charge accumulated in the insulating portion between the electrodes; a discharge step of discharging the charge accumulated in the insulating portion between the electrodes on the basis of the amount of charge; and a step of repeating the monitor step and the discharge step.

EFFECT OF THE INVENTION

[0012] According to the present invention, it is possible to provide an ultrasound diagnostic apparatus which quickly calibrates a transmission-reception sensitivity drift stemming from time-course accumulation of charge flowing into an insulating portion between electrodes upon application of a DC bias thereto.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013]

- [FIG. 1] Schematic configurational diagram of an ultrasound diagnostic apparatus 1 (first embodiment).
- [FIG. 2] View showing an ultrasound transducer 20 provided in an ultrasound probe 2.
- [FIG. 3] Configurational diagram of a transmission circuit 3.
- [FIG. 4] Configurational diagram of a discharge circuit 4.
- [FIG. 5] Configurational diagram of control means 5.

[FIG. 6] Cross sectional view of an ultrasound transducer 60 showing one embodiment of the ultrasound transducer 20.

[FIG. 7] Cross sectional view of an ultrasound transducer 70 showing another embodiment of the ultrasound transducer 20.

[FIG. 8] Graphs showing the relation among ultrasound transmission pulse voltage, transmission-reception sensitivity, etc.

[FIG. 9] Flowchart showing discharge processing.

[FIG. 10] Graphs showing the relation among discharge pulse voltage, transmission-reception sensitivity, transmission-reception sensitivity drift, etc.

[FIG. 11] Illustration showing a table 110, which is one embodiment of applied bias historical information 54 and state transition information 56.

[FIG. 12] Illustration showing one embodiment of a screen 120 of display means 10.

[FIG. 13] Schematic configurational diagram of an ultrasound diagnostic apparatus 1a (second embodiment).

[FIG. 14] Schematic configurational diagram of an ultrasound diagnostic apparatus 1b (third embodiment).

[FIG. 15] Schematic configurational diagram of an ultrasound diagnostic apparatus 1c and an external control apparatus 153 (fourth embodiment).

DESCRIPTION OF REFERENCE NUMERALS

30

[0014]

1, 1a, 1b, 1c	ultrasound diagnostic apparatus
2	ultrasound probe
3, 3a	transmission circuit
4, 4a, 80	discharge means
5	control means
6, 6b	DC bias circuit
7	transmission-reception separation circuit
8	reception circuit
9	image-processing means
10	display means
20, 60, 70	ultrasound transducer
21, 61, 71	upper electrode
22	diaphragm
23	rim
24, 62, 72	lower electrode
25, 64, 74	sacrificial layer
27	gap width
31	ultrasound transmission pulse timing generation means
32	ultrasound transmission pulse waveform producing means
33	ultrasound transmission pulser
41	discharge pulse timing generation means
42	discharge pulse waveform producing

43	means
51	discharge pulser
52	computation section
53	main memory
54	storage section
55	applied bias historical information
56	discharge pulse waveform information
63, 73	state transition information
69, 79	insulating portion
151, 155	charge injection
152	communication section
153	network
154	external control apparatus
159	remote maintenance center
160	historical information
	storage section

BEST MODE FOR CARRYING OUT THE INVENTION

[0015] A preferred embodiment of an ultrasound diagnostic apparatus according to the present invention will next be described in detail with reference to the attached drawings. In the following description and the attached drawings, structural elements having generally identical functional configurations are denoted by like reference numerals, and their repeated descriptions are omitted.

(1. Configuration of an ultrasound diagnostic apparatus 1)

[0016] First, an ultrasound diagnostic apparatus 1 according to a first embodiment of the present invention will be described with reference to FIGS. 1 to 5.

(1-1. Schematic configuration of the ultrasound diagnostic apparatus 1)

[0017] FIG. 1 is a schematic configurational diagram of the ultrasound diagnostic apparatus 1.

The ultrasound diagnostic apparatus 1 is composed of an ultrasound probe 2, a transmission circuit 3, discharge means 4, control means 5, a DC bias circuit 6, a transmission-reception separation circuit 7, a reception circuit 8, image-processing means 9, and display means 10. The ultrasound diagnostic apparatus 1 of the first embodiment employs a configuration in which respective outputs of the transmission circuit 3 and the discharge means 4 are fed to the transmission-reception separation circuit 7 after being combined together.

[0018] The ultrasound diagnostic apparatus 1 transmits ultrasound waves to a subject and receives ultrasound waves from the subject by means of the ultrasound probe 2, and performs image processing by means of the image-processing means 9 so as to output a captured image of the subject to the display means 10. The ultrasound diagnostic apparatus 1 applies a DC bias between electrodes of each ultrasound transducer 20 provided in the ultrasound probe 2 to thereby generate an electric

field. In this state, the ultrasound diagnostic apparatus 1 applies an ultrasound transmission pulse between the electrodes so as to vibrate a diaphragm to thereby transmit an ultrasound wave toward the subject. The ultrasound diagnostic apparatus 1 receives the ultrasound wave by detecting a vibration of the diaphragm caused by an ultrasound echo from the subject.

[0019] The DC bias circuit 6 and the transmission-reception separation circuit 7 are connected to the ultrasound probe 2. The transmission circuit 3, the reception circuit 8, and the discharge means 4 are connected to the transmission-reception separation circuit 7. The image-processing means 9 equipped with the display means 10 is connected to the reception circuit 8. The DC bias circuit 6, the transmission circuit 3, the discharge means 4, the reception circuit 8, and the image-processing means 9 are connected to the control means 5.

[0020] The transmission circuit 3 is a circuit for transmitting ultrasound transmission pulses to the transmission-reception separation circuit 7. The discharge means 4 is an apparatus for discharging any charge injected into an insulating portion of the ultrasound transducer 20 between the electrodes thereof. The DC bias circuit 6 is a circuit for controlling a DC bias applied to the ultrasound transducer 20 of the ultrasound probe 2. The transmission-reception separation circuit 7 is a circuit for separating ultrasound transmission pulses and ultrasound reception pulses from each other.

The reception circuit 8 receives ultrasound transmission pulses from the transmission-reception separation circuit 7. The image-processing means 9 is a computation apparatus which performs detection processing, etc. on the reception signals sent from the reception circuit 8 and reconstructs an ultrasound image. The display means 10 is a display apparatus, such as a monitor, which displays the ultrasound image sent from the image-processing means 9.

The control means 5 is an apparatus for controlling the state of the ultrasound diagnostic apparatus 1 and the respective apparatuses.

(1-2. Ultrasound transducer 20)

[0021] FIG. 2 is a view showing the ultrasound transducer 20 provided in the ultrasound probe 2. Notably, the ultrasound transducer 20 shown in FIG. 2 is a cMUT (Capacitive Micro-machined Ultrasound Transducer).

[0022] The ultrasound transducer 20 is composed of an upper electrode 21; a diaphragm 22 formed of a deformable insulating material; a rim 23 formed of an insulating material such as silicon nitride (Si_3N_4) and supporting the periphery of the diaphragm 22; a lower electrode 24; and a sacrificial layer 25, which is a vacuum gap layer formed between the upper electrode 21 and the lower electrode 24. The gap width 27 of the sacrificial layer 25 changes in accordance with a DC bias applied between the electrodes and vibration of the diaphragm

22. Notably, the "insulating portion" will be described as including the diaphragm 22 and the rim 23 between the upper electrode 21 and the lower electrode 24.

[0023] The ultrasound transducer 20 is a transducer whose ultrasound-wave transmission-reception sensitivity changes in accordance with a bias voltage superposed on a drive signal that drives the transducer. When ultrasound waves are transmitted and received by the ultrasound transducer 20, a predetermined bias is applied to the ultrasound transducer 20 so as to change the electromechanical coupling coefficient of the transducer to thereby adjust the transmission-reception sensitivity of the ultrasound transducer 20 to a predetermined value. When a proper voltage signal is applied between the upper electrode 21 and the lower electrode 24, the ultrasound transducer 20 functions as a capacitive ultrasound transducer cell. An ultrasound echo signal is captured in the form of a change in kinetic energy of a sensor portion (the diaphragm 22 and the sacrificial layer 25), and a resultant change in current is detected, whereby a reception electric signal is obtained.

(1-3. Transmission circuit 3)

[0024] FIG. 3 is a configurational diagram of the transmission circuit 3.

The transmission circuit 3 is composed of ultrasound transmission pulse timing generation means 31 including a clock counter, etc.; ultrasound transmission pulse waveform producing means 32 including a selector, a memory; a memory control section, etc.; and ultrasound transmission pulser 33 including a clock divider, a buffer, a level shifter, a power driver, etc.

[0025] The control means 5 controls the transmission circuit 3; i.e., controls the waveform production by controlling the center frequency (e.g., 0.1 MHz to several tens MHz), wave number, envelope, etc., controls the generation timing by controlling the repeating frequency, delay for wave-transmission focusing, etc., and controls the output intensity by controlling the amplification factor, power source voltage, etc.

(1-4. Discharge circuit 4)

[0026] FIG. 4 is a configurational diagram of the discharge circuit 4.

The discharge circuit 4 is composed of discharge pulse timing generation means 41 including a clock counter, etc.; discharge pulse waveform producing means 42 including a selector, a memory, a memory control section, etc.; and a discharge pulser 43 including a clock divider, a buffer, a level shifter, a power driver, etc.

[0027] The control means 5 controls the discharge circuit 4; i.e., controls the waveform production by controlling the center frequency (e.g., several Hz to several tens kHz) and wave number, controls the generation timing by controlling the discharge trigger, the charge accumulation amount, etc., and controls the output intensity by

controlling the amplification factor, power source voltage, etc.

[0028] The discharge pulse timing generation means 41 generates a discharge pulse generation timing signal with the clock division rate, the clock counter number, etc. being controlled by the control means 5.

5 The discharge pulse waveform producing means 42 selects a selector in accordance with the conditions, such as center frequency, wave number, envelope, etc. set by the control means 5. The memory control section controls the memory by performing read/write of waveform data and address setting in accordance with the condition selected by the selector, and produces a discharge pulse waveform when the discharge pulse timing generation 10 means 41 generates the discharge pulse generation timing signal.

15 The discharge pulser 43 buffers, by means of a buffer, the discharge pulse waveform produced by the discharge pulse waveform producing means 42, and shifts the level 20 of the discharge pulse waveform by means of a level shifter while amplifying the same. The power driver applies a discharge pulse between the electrodes of each ultrasound transducer 20 of the ultrasound probe 2.

[0029] FIG. 5 is a configurational diagram of the control 25 means 5.

The control means 5 is composed of a computation section 51 such as a CPU, a main memory 52 such as RAM, and a storage section 53 such as a hard disk.

30 The storage section 53 holds applied bias historical information 54, discharge pulse waveform information 55, and state transition information 56.

[0030] The applied bias historical information 54 is a piece of information (application voltage, application time, etc.) regarding the history of application of a bias 35 between the electrodes of the ultrasound transducer 20, and may assume the form of a Log file or the like.

The discharge pulse waveform information 55 is a piece of information regarding the discharge pulse waveform produced by the discharge means 4, and may assume 40 the form of a Config file or the like.

The state transition information 56 is a piece of information 45 regarding the state transition between an ordinary ultrasound-wave transmission-reception processing state and a discharge processing state in which the accumulated charge is discharged, and may assume the form of a State file, a flag, or the like.

[0031] The control means 5 retrieves the applied bias historical information 54, the discharge pulse waveform information 55, and the state transition information 56, and stores them in the main memory 52. The computation section 51 calculates, as information regarding the waveform of discharge pulses, the number of pulses and the application voltage and application time of each pulse (peak value v_h (t_h), discharge time t_h , etc. of FIG. 10(b)) 55 on the basis of the applied bias historical information 54 and in accordance with the conditions of the discharge pulse waveform information 55. On the basis of the state transition information 56 and in accordance with the con-

ditions, the control means 5 effects switching between the ultrasound-wave transmission-reception processing state and the discharge processing state to thereby discharge any charge accumulated in the diaphragm 22 present between the electrodes of each ultrasound transducer 20 of the ultrasound probe 2.

(2. Charge Injection)

[0032] Next; injection of charge into an inter-electrode insulating portion will be described with reference to FIGS. 6 and 7.

[0033] FIG. 6 is a cross sectional view of an ultrasound transducer 60 showing one embodiment of the ultrasound transducer 20.

The ultrasound transducer 60 is fabricated through micro-machining by a semiconductor process. The ultrasound transducer 60 is composed of an upper electrode 61, a lower electrode 62, an insulating portion 63, a sacrificial layer 64, and a substrate 65. The insulating portion 63 is composed of a film 66, a frame 67, and a film 68. The insulating portion 63 is formed of an insulating material such as a silicon compound. The sacrificial layer 64 may be a vacuum chamber or a gas-charged chamber.

[0034] When a DC bias is applied between the upper electrode 61 and the lower electrode 62, an electric field is generated therebetween. Although the insulating portion 63 is high in electrical resistance, a very small current flows through the insulating portion 63 upon generation of the electric field between the electrodes, with the resultant charge injection 69 from the upper electrode 61 and the lower electrode 62 to the insulating portion 63. When application of the DC bias is interrupted, the charges in the upper electrode 61 and the lower electrode 62 become zero. However, the charge injected into the insulating portion 63 remains and accumulates. The accumulation amount of charge is proportional to the sum of product of the application voltage and application time of the DC bias.

[0035] A portion of the insulating portion 63 forms a diaphragm. If a charge is injected into the diaphragm portion of the insulating portion 63 and is accumulated therein, due to the influence of the inter-electrode field intensity, the transmission-reception sensitivity changes. Further, the accumulated charge causes a transmission-reception sensitivity drift, whereby a transmission-reception sensitivity distortion occurs.

[0036] FIG. 7 is a cross sectional view of an ultrasound transducer 70 showing another embodiment of the ultrasound transducer 20.

The ultrasound transducer 70 is composed of an upper electrode 71, a lower electrode 72, an insulating portion 73, and a sacrificial layer 74. The insulating portion 73 is composed of a film 76, a frame 77, and a film 78. The upper electrode 71, the insulating portion 73, and the sacrificial layer 74 of FIG. 7 are similar to the upper electrode 61, the insulating portion 63, and the sacrificial layer

64 of FIG. 6. The lower electrode 72 of FIG. 7 corresponds to the substrate 65 of FIG. 6, which is used as a lower electrode in FIG. 7.

[0037] In the ultrasound transducer 70 of FIG. 7 as well, like the ultrasound transducer 60 of FIG. 6, there occurs charge injection 79 from the upper electrode 71 and the lower electrode 72 to the insulating portion 73.

(3. Transmission-reception sensitivity drift)

[0038] Next, transmission-reception sensitivity drift will be described with reference to FIG. 8.

FIG. 8 is a set of graphs showing the relation among ultrasound transmission pulse voltage, transmission-reception sensitivity, etc.

[0039] FIG. 8(a) is a graph showing the relation between ultrasound transmission pulse voltage and transmission-reception sensitivity. The horizontal axis represents ultrasound transmission pulse voltage v_s , and the vertical axis represents transmission-reception sensitivity $p(v_s)$.

FIG. 8(b) is a graph showing the relation between ultrasound transmission pulse voltage and time. The horizontal axis represents ultrasound transmission pulse voltage $v_s(t_s)$, and the vertical axis represents ultrasound-wave transmission time t_s .

FIG. 8(c) is a graph showing the relation between ultrasound-wave transmission time and transmission-reception sensitivity. The horizontal axis represents ultrasound-wave transmission time t_s , and the vertical axis represents transmission-reception sensitivity $p(t_s)$.

[0040] A line 201 shows the relation between ultrasound transmission pulse voltage and transmission-reception sensitivity for the case where a DC bias is applied between the electrodes of the ultrasound transducer 20 for a relatively short time. The line 201 is a straight line passing through the origin 202. When an ultrasound transmission pulse voltage v_s (AC pulse) having the same amplitude in the positive and negative polarities as indicated by a curve 203 is applied, the transmission-reception sensitivity $p(t_s)$ has neither deviation nor distortion in the positive and negative polarities as indicated by a curve 204.

[0041] Meanwhile, in the case where a DC bias is applied between the electrodes of the ultrasound transducer 20 for a long time, if a charge is injected into the diaphragm 22 of the ultrasound transducer 20, a deviation of the transmission-reception sensitivity in the positive and negative polarities; i.e., a transmission-reception sensitivity drift 205, is produced. When such transmission-reception sensitivity drift 205 is produced, the relation between ultrasound transmission pulse voltage and transmission-reception sensitivity moves as indicated by a line 206. This line 206 does not pass through the origin 202. When an ultrasound transmission pulse voltage v_s (AC pulse) having the same amplitude in the positive and negative polarities as indicated by the curve 203 is applied, the transmission-reception sensitivity $p(t_s)$ has de-

vation and distortion in the positive and negative polarities as indicated by a curve 207. This transmission-reception sensitivity distortion causes an increase in harmonic components.

[0042] (4. Discharge processing of the ultrasound diagnostic apparatus 1)

Next, the discharge processing of the ultrasound diagnostic apparatus 1 will be described with reference to FIGS. 9 to 11.

The ultrasound diagnostic apparatus 1 of the present invention is characterized in that the charge accumulated between the electrodes of the ultrasound transducer 20 is discharged within a short time by making use of a collapse phenomenon and a snapback phenomenon.

(4-1. Collapse phenomenon and Snapback phenomenon)

[0043] If the magnitude of the DC bias applied between the electrodes of the ultrasound transducer 20 falls within a predetermined range, because of balance between an elastic force of the diaphragm 22 and a Coulomb force generated through application of the DC bias, the gap width 27 of the sacrificial layer 25 becomes a predetermined width, and a balanced state is created. Meanwhile, when the DC bias applied between the electrodes of the ultrasound transducer 20 increases, the Coulomb force increases, and balance is attained when the gap width 27 of the sacrificial layer 25 has decreased. Further, when the DC bias applied between the electrodes of the ultrasound transducer 20 increases beyond the predetermined range, the elastic force of the diaphragm 22 becomes unable to support the Coulomb force generated through application of the DC bias, and an imbalanced state is created. As a result, the gap width 27 of the sacrificial layer 25 quickly decreases to zero. This phenomenon and state are called a "collapse phenomenon" and a "collapse state," respectively. A threshold value of the application voltage at which the collapse state is created is called a "collapse voltage."

[0044] In a state in which the collapse phenomenon has not yet occurred, injection of charge into the insulating portion occurs in accordance with the ratio between the impedance of the diaphragm 22 and the rim 23 and the DC bias application voltage.

Meanwhile, in the collapse state, injection of charge occurs in accordance with the ratio between the impedance of the diaphragm 22 only and the DC bias application voltage.

In the collapse state, since the impedance decreases quickly, the charge injection speed increases.

[0045] When the DC bias application voltage is reduced in the collapse state, the Coulomb force decreases. As a result, a phenomenon which is inverse of the collapse phenomenon occurs, so that the gap width of the sacrificial layer 25 quickly changes from zero to the predetermined width. This phenomenon is called a "snapback phenomenon." A threshold value of the appli-

cation voltage at which the snapback phenomenon occurs is called a "snapback voltage."

That is, when the collapse phenomenon occurs, the elastic force of the diaphragm 22 and the Coulomb force become unbalance. However, the diaphragm 22 does not lose its elastic property. Accordingly, when the snapback phenomenon occurs, the balance between the elastic force of the diaphragm 22 and the Coulomb force is restored.

5 10 After occurrence of the snapback phenomenon, injection of charge occurs in accordance with the ratio of the DC bias application voltage to the impedance of the diaphragm 22 and the rim 23.

15 (4-2. Details of the discharge processing)

[0046] FIG. 9 is a flowchart showing the discharge processing.

FIG. 10 is a set of graphs showing the relation among 20 discharge pulse voltage, transmission-reception sensitivity, transmission-reception sensitivity drift, etc.

[0047] FIG. 10(a) is a graph showing the relation between discharge pulse voltage and transmission-reception sensitivity. The horizontal axis represents discharge 25 pulse voltage V_h , and the vertical axis represents transmission-reception sensitivity $p(v_h)$.

FIG. 10(b) is a graph showing the relation between discharge pulse voltage and time. The horizontal axis represents discharge pulse voltage $v_h(t_h)$, and the vertical axis represents discharge time t_h .

30 FIG. 10(c) is a graph showing the relation between discharge time and transmission-reception sensitivity drift. The horizontal axis represents discharge time t_h , and the vertical axis represents transmission-reception sensitivity drift $pd(t_h)$.

[0048] When the ultrasound diagnostic apparatus 1 continues ultrasound-wave transmission processing, a charge whose polarity corresponds to that of the DC bias application voltage accumulates in the insulating portion 40 between the electrodes 21 and 24 of the ultrasound transducer 20 (step 101).

The control means 5 obtains, through measurement, information regarding the application voltage and application time of the DC bias, and records them as the applied 45 bias historical information 54 (step 102). The control means 5 determines whether or not the sum of product of the application voltage and application time of the DC bias has reached a predetermined reference value.

When the sum has reached the predetermined reference 50 value (Yes in step 103), the control means 5 proceeds to the discharge processing in step 104 and steps subsequent thereto.

[0049] In order to discharge the injected charge, the discharge means 4 applies between the electrodes of 55 the ultrasound transducer 20 a discharge pulse (H_1) whose polarity is opposite that of the ordinary DC bias application voltage, for a predetermined time (t_0 to t_1) (step 104). Notably, the predetermined time (t_0 to t_1) is

desirably set such that a reverse charge is injected slightly, in consideration of variations of conditions.

The peak value ($v_h(t_h)$) of the discharge pulse (H_1) is equal to or greater than the collapse voltage, so that the collapse phenomenon occurs. The gap width 27 of the sacrificial layer 25 becomes zero, and the discharge of the charge is accelerated (step 105).

[0050] Next, in order to discharge the remaining reverse charge, the discharge means 4 applies between the electrodes of the ultrasound transducer 20 a discharge pulse (H_2) whose polarity is opposite that of the previous discharge pulse (H_1), for a predetermined time (t_1 to t_2) (step 106). Notably, the predetermined time (t_1 to t_2) is desirably set such that a reverse charge is injected slightly, in consideration of variations of conditions. Further, the predetermined time (t_1 to t_2) desirably satisfies the relation the predetermined time (t_0 to t_1) > the predetermined time (t_1 to t_2).

The peak value ($v_h(t_h)$) of the discharge pulse (H_2) is equal to or greater than the collapse voltage, so that the collapse phenomenon occurs. The gap width 27 of the sacrificial layer 25 becomes zero, and the discharge of the charge is accelerated (step 107).

[0051] When it is determined that the processing of steps 104 and 105 has been repeated a predetermined number of times (step 108), the discharge means 4 ends the application of the discharge pulse (H) (step 109). Preferably, the discharge means 4 gradually shortens the application time such that the predetermined time (t_0 to t_1) > the predetermined time (t_1 to t_2) > . . . > the predetermined time (t_{m-1} to t_m) > the predetermined time (t_m to t_{m+1}).

When the voltage applied between the electrodes of the ultrasound transducer 20 becomes equal to or lower than the collapse voltage, the snapback phenomenon occurs, and the gap width 27 of the sacrificial layer 25 is restored to the predetermined width (step 110). The transmission-reception sensitivity drift ($pd(t_h)$) generally converges to "0."

[0052] FIG. 10 shows an example in which the discharge pulse (H) is applied four times, while its polarity is inverted each time. When the discharge means 4 applies the discharge pulse (H_1) between the electrodes of the ultrasound transducer 20, the transmission-reception sensitivity drift $pd(t_0)$ changes to a transmission-reception sensitivity drift $pd(t_1)$. Similarly, when the discharge means 4 successively applies the discharge pulse (H_2), the discharge pulse (H_3), and the discharge pulse (H_4) between the electrodes of the ultrasound transducer 20, the transmission-reception sensitivity drift changes in such a manner that $pd(t_2) \rightarrow pd(t_3) \rightarrow pd(t_4)$. The transmission-reception sensitivity drift $pd(t_4)$ generally converges to "0."

[0053] Notably, the number of times the discharge pulse is applied with its polarity reversed each time is at least one. The ultrasound probe 2 is composed of a large number of ultrasound transducers 20. Since the characteristics vary among the ultrasound transducers 20, the

transmission-reception sensitivity drifts of all the ultrasound transducers 20 cannot be made zero simultaneously through a single-time application of a reverse-polarity discharge pulse. Although the transmission-reception sensitivity drift can be improved to some degree through the single-time application of a reverse-polarity discharge pulse, it is desired to apply the discharge pulse a plurality of times with its polarity reversed each time to thereby converge the transmission-reception sensitivity drift to zero as shown in FIGS. 9 and 10.

The discharge processing shown in steps 104 to 109 of FIG. 9 and FIG. 10 completes within a short time not longer than 1 sec. Accordingly, the discharge processing is desirably performed when the power is turned on or off, or when the transmission and reception of ultrasound waves is stopped (at the time of freeze).

[0054] As described above, the ultrasound diagnostic apparatus 1 of the first embodiment causes the discharge means 4 to apply a discharge pulse (having a peak value equal to or greater than the collapse voltage) between the electrodes of the ultrasound transducer 20 a plurality of times while inverting the polarity of the discharge pulse alternately, to thereby accelerate discharge of the charge accumulated in the diaphragm 22 of the ultrasound transducer 20, so that the transmission-reception sensitivity offset can be caused to quickly approach zero. The charge is discharged instantaneously. That is, it is possible to quickly calibrate the transmission-reception sensitivity drift stemming from time-course accumulation of charge flowing into the insulating portion between the electrodes upon application of the DC bias thereto.

(4-3. Monitoring of charge accumulation)

[0055] FIG. 11 is an illustration showing a table 110, which is one embodiment of the applied bias historical information 54 and the state transition information 56. Various items; i.e., date 111, time 112, application time 113, application voltage 114, sum of product 115, and state 116, are recorded in the table 110. The date 111, the time 112, the application time 113, the application voltage 114, and the sum of product 115 correspond to the applied bias historical information 54 of FIG. 5. The state 116 corresponds to the state transition information 56 of FIG. 5.

[0056] The date 111 and the time 112 show the date and time of each application of a DC bias. The application time 113 shows the application time of the DC bias; for example, "2h." The application voltage 114 shows the application voltage of the DC bias; for example, "100 V." The sum of product 115 shows the sum of product of the application voltage and application time of the DC bias after the previous discharge processing; for example, "200 V·h." The state 116 shows the charge accumulation state or the execution of the discharge processing. For example, the reaching ratio of the sum of product 115 in relation to a predetermined reference value (e.g., "30000 V·h") is recorded as the state 116.

[0057] In the processing of step 102 of FIG. 9, the control means 5 records the date 111, the time 112, the application time 113, the application voltage 114, and the sum of product 115 of the table 110. In the processing of step 103 of FIG. 9, the control means 5 determines whether or not the sum of product 115 of the table 110 has reached the predetermined reference value. When the sum has reached the predetermined reference value (Yes in step 103), the control means 5 performs the discharge processing in step 104 and subsequent steps of FIG. 9. The control means 5 calculates the execution timing of the discharge processing and the waveform information (the pulse number and the application voltage and application time of each pulse) of discharge pulses (H) shown in FIG. 10(b) by reference to the table 110, and applies the discharge pulses between the electrodes.

Thus, the start timing of the discharge processing can be properly determined.

(4-4. Display of DC bias application history and state)

[0058] FIG. 12 is an illustration showing one embodiment of the screen 120 of the display means 10. The display means 10 displays the charge accumulation state in the form of a graph on the basis of the applied bias historical information 54 and the state transition information 56.

On the screen 120, the vertical axis indicates the sum of product of the application voltage and application time of the DC bias, and the horizontal axis indicates date and time. At a time point 121, the sum of product of the application voltage and application time of the DC bias reaches a predetermined reference value 122, and therefore, the discharge processing is performed. Further, the reaching ratio 124 of the sum of product, as calculated at the present time point 123 in relation to the predetermined reference value 122, may be displayed. Further, necessity of the discharge processing may be displayed. The screen 120 allows grasping of the charge accumulation amount, and the necessity and timing of the discharge processing. Notably, in the present embodiment, the control means 5 determines the timing at which the discharge processing is to be started, and automatically executes the discharge processing. However, a discharge switch or the like may be provided on the apparatus main body so as to manually execute the discharge processing. In this case, desirably, the manual execution of the discharge processing is effected on the basis of the above-described reaching ratio and the necessity of the discharge processing displayed by the display means 10.

(5. Second Embodiment)

[0059] Next, an ultrasound diagnostic apparatus 1 according to a second embodiment will be described with reference to FIG. 13.

FIG. 13 is a schematic configurational diagram of the ultrasound diagnostic apparatus 1a.

[0060] In the ultrasound diagnostic apparatus 1 of the first embodiment, the discharge means 4 is configured independently. In contrast, in the ultrasound diagnostic apparatus 1a of the second embodiment, a transmission circuit 3a and discharge means 4a share a portion or the entirely of pulse timing generation means, pulse waveform producing means, and a pulser. For example, the transmission circuit 3a may be configured to contain the discharge means 4 therein.

[0061] In the second embodiment, as in the first embodiment, the ultrasound diagnostic apparatus 1a causes the discharge means 4a to apply a discharge pulse (having a peak value equal to or greater than the collapse voltage) between the electrodes of the ultrasound transducer 20 a plurality of time while inverting the polarity of the discharge pulse each time, to thereby accelerate discharge of the charge accumulated in the diaphragm 22 of the ultrasound transducer 20, so that the transmission-reception sensitivity offset can be caused to quickly approach zero.

[0062] Further, in the second embodiment, the transmission circuit 3a and the discharge means 4a share a portion of constituent elements. Therefore, the configuration of the apparatus can be simplified.

(6. Third Embodiment)

[0063] Next, an ultrasound diagnostic apparatus 1b according to a third embodiment will be described with reference to FIG. 14.

FIG. 14 is a schematic configurational diagram of the ultrasound diagnostic apparatus 1b

[0064] In the ultrasound diagnostic apparatus 1 of the first embodiment, the discharge means 4 is configured independently. In contrast, in the ultrasound diagnostic apparatus 1b of the third embodiment, discharge means 80 is incorporated into a DC bias circuit 6b.

The DC bias circuit 6b containing the discharge means 80 changes the DC bias applied between the electrodes of the ultrasound transducer 20 to thereby accelerate discharge of the charge accumulated in the diaphragm 22 of the ultrasound transducer 20.

In the ultrasound diagnostic apparatus 1b, in place of discharge pulses equal to or higher than the collapse voltage, which is employed in the first and second embodiments, a reverse-polarity DC bias equal to or less than the collapse voltage is applied by reference to the applied bias historical information 54 of the storage section 53, whereby the charge injected into the diaphragm 22 of the ultrasound transducer 20 can be discharged.

[0065] In the third embodiment, a reverse-polarity DC bias which is equal to or less than the collapse voltage is applied by means of the DC bias circuit 6b containing the discharge means 80, whereby discharge of the charge accumulated in the diaphragm 22 of the ultrasound transducer 20 can be accelerated, and the trans-

mission-reception sensitivity offset can be caused to quickly approach zero.

Notably, when a DC bias equal to or less than the collapse voltage is applied, the discharge processing continues for several hours. Accordingly, it is desired to provide, on an operation device such as a keyboard or a monitor, a switch for starting the discharge processing, and to provide reporting means for providing a display, warning, or the like which indicates that the discharge processing is being executed.

[0066] Further, in the third embodiment, pulse timing generation means, pulse waveform producing means, and a pulser are not required for the discharge processing. Therefore, the configuration of the apparatus can be simplified.

Notably, it is theoretically possible to perform the processing of discharging the accumulated charge through application of a DC bias equal to or higher than the collapse voltage. However, in a case where a reverse-polarity DC bias equal to or higher than the collapse voltage is applied, there is a possibility that the charge injection proceeds instead and the transmission-reception sensitivity drift increases. Therefore, discharge through gradual application of a reverse-polarity DC bias equal to or less than the collapse voltage is simple and reliable, and is preferable from the viewpoint of safety.

(7. Fourth Embodiment)

[0067] Next, an ultrasound diagnostic apparatus 1c according to a fourth embodiment will be described with reference to FIG. 15.

FIG. 15 is a schematic configurational diagram of the ultrasound diagnostic apparatus 1c and an external control apparatus 153.

[0068] In the first embodiment, the ultrasound diagnostic apparatus 1 itself calculates the timing for the discharge processing and the discharge pulse waveform information so as to execute the discharge processing. In contrast, in the fourth embodiment, the external control apparatus 153 calculates the timing for the discharge processing and the discharge pulse waveform information.

[0069] The ultrasound diagnostic apparatus 1c is connected via a network 152 to the external control apparatus 153 belonging to a remote maintenance center 154. The ultrasound diagnostic apparatus 1c is similar to the ultrasound diagnostic apparatus 1 of FIG. 1. The external control apparatus 153 includes historical information acquisition means 156, parameter update means 157, discharge-processing instruction means 158, and a storage section 160. The external control apparatus 153 is a terminal apparatus such as a personal computer. The network 152 is a radio communication line such as personal handy-phone system, or a network line such as the internet. The ultrasound diagnostic apparatus 1c and the external control apparatus 153 include a communication section 151 and a communication section 155, respec-

tively, and can exchange information via the communication section 151, the network 152, and the communication section 155.

[0070] The historical information 159 is transmitted and received between the ultrasound diagnostic apparatus 1c and the external control apparatus 153 at constant intervals or intermittently in accordance with the state of use. The historical information 159 includes history regarding application of a DC bias between the electrodes and history regarding application of discharge pulses between the electrodes. The transmission and reception of the historical information 159 is effected in such a manner that the ultrasound diagnostic apparatus 1c actively transmits the historical information 159 to the external control apparatus 153 or in such a manner that the external control apparatus 153 actively acquires the historical information 159 from the ultrasound diagnostic apparatus 1c.

[0071] The external control apparatus 153 acquires the historical information 159 held in the storage section 53 of the ultrasound diagnostic apparatus 1c by means of the historical information acquisition means 156, and stores the information in the storage section 160. The external control apparatus 153 checks the charging-discharging state of the charge in the insulating portion of the ultrasound diagnostic apparatus 1c by reference to the historical information 159.

Also, the external control apparatus 153 causes the parameter update means 157 to check various pieces of information such as parameters for determining the peak value of the discharge pulse waveform, the predetermined time, and the number of times, etc., and parameters for determining the condition of state transition. Further, when necessary, the external control apparatus 153 causes the parameter update means 157 to transmit update values of these parameters to the ultrasound diagnostic apparatus 1c for update.

Moreover, the external control apparatus 153 causes the discharge-processing instruction means 158 to instruct the ultrasound diagnostic apparatus 1c to execute the discharge processing.

[0072] On the basis of discharge processing instruction and the parameters associated with the discharge processing and transmitted from the external control apparatus 153, the ultrasound diagnostic apparatus 1c updates these parameters and executes the discharge processing. The ultrasound diagnostic apparatus 1c transmits to the external control apparatus 153 the results of the update of the parameters and the results of the execution of the discharge processing.

[0073] As described above, in the fourth embodiment, the external control apparatus 153 can control update of the parameters associated with the discharge processing in the ultrasound diagnostic apparatus 1c and the timing of the discharge processing. Further, when a plurality of ultrasound diagnostic apparatus 1c are connected to the network 152, it becomes possible to enable the single external control apparatus 153 to centrally manage the

discharge processing in the plurality of ultrasound diagnostic apparatus 1c.

(8. Others)

[0074] In the above-described embodiment, the charge injected into a diaphragm of an ultrasound transducer due to application of a DC bias over a long time is quickly discharged by utilizing the collapse phenomenon and the snapback phenomenon. However, the charge within the diaphragm can be discharged by a shunt switch, such as a mechanical relay, which is formed in the ultrasound transducer by utilizing an MEMS (Micro Electro Mechanical System). In this case, the charge can be discharged by controlling the open/close state of the shunt switch.

[0075] Further, the control means 5 may be configured to monitor the state transition between the ultrasound-wave transmission-reception processing state and the discharge processing state, and report the processing state to an operator of the ultrasound diagnostic apparatus 1. For example, the control means 5 may report the fact of the discharge processing currently being executed to the operator of the ultrasound diagnostic apparatus 1 by providing an alert display on the display means 10, sound information, or the like.

[0076] Preferred embodiments of the ultrasound diagnostic apparatus according to the present invention have been described with reference to the accompanying drawings. However, the present invention is not limited to the above-described embodiments. It is clear that a person with ordinary skill in the art can easily conceive various modifications and changes within the technical idea disclosed herein, and it is contemplated that such modifications and changes naturally fall within the technical scope of the present invention.

Claims

1. An ultrasound diagnostic apparatus comprising an ultrasound probe equipped with an ultrasound transducer including a pair of electrodes and an insulating portion and a sacrificial layer provided between the electrodes; DC-bias application means for applying a DC bias between the electrodes; transmission means for causing a portion of the insulating portion to vibrate through application of an ultrasound transmission pulse between the electrode in addition to the DC bias, to thereby transmit an ultrasound wave toward a subject; reception means for receiving an ultrasound echo from the subject; and image-processing means for producing an ultrasound image on the basis of a signal output from the reception means,
the ultrasound diagnostic apparatus being **characterized by** further comprising discharge means for discharging a charge accumulated in the insulating

portion between the electrodes.

2. An ultrasound diagnostic apparatus according to claim 1, wherein the discharge means repeatedly applies a discharge voltage between the electrodes at least one time.
3. An ultrasound diagnostic apparatus according to claim 2, wherein the discharge means applies the discharge voltage between the electrodes while inverting the polarity of the discharge voltage alternately.
4. An ultrasound diagnostic apparatus according to claim 3, wherein the discharge means applies the discharge voltage between the electrodes while maintaining the absolute value of the discharge voltage constant.
5. An ultrasound diagnostic apparatus according to claim 2, wherein the discharge means repeatedly applies the discharge voltage between the electrodes such that the discharge voltage becomes equal to or higher than a collapse voltage at least one time.
6. An ultrasound diagnostic apparatus according to claim 3, wherein the discharge means gradually shortens an application time of the discharge voltage repeatedly applied between the electrodes.
7. An ultrasound diagnostic apparatus according to claim 2, further comprising monitor means for monitoring an amount of charge accumulated in the insulating portion, wherein the discharge means applies the discharge voltage between the electrodes on the basis of the amount of charge accumulated in the insulating portion.
8. An ultrasound diagnostic apparatus according to claim 7, wherein the monitor means includes storage means for holding applied voltage historical information including history regarding the application voltage and application time of the DC bias applied between the electrodes; and the discharge means applies the discharge voltage between the electrodes on the basis of the applied voltage historical information.
9. An ultrasound diagnostic apparatus according to claim 8, wherein the storage means holds the sum of product of the application voltage and application time of the DC bias applied between the electrodes; and the discharge means applies the discharge voltage between the electrodes when the sum of product reaches a predetermined threshold value.

10. An ultrasound diagnostic apparatus according to claim 9, further comprising display means for displaying at least one of history regarding the sum of product, a degree of reaching of the sum of product to the predetermined threshold value, and necessity and timing of the discharge processing of the discharge means determined on the basis of the degree of reaching. 5

11. An ultrasound diagnostic apparatus according to claim 1, wherein the discharge means discharges the charge at least when the ultrasound diagnostic apparatus is turned on or turned off or when the transmission and reception of ultrasound waves to and from the subject is stopped. 10 15

12. An ultrasound diagnostic apparatus according to claim 1, wherein the discharge means and the transmission means share a portion or the entirety of at least one of pulse waveform producing means for producing a pulse waveform, pulse timing generation means for determining timings at which the pulse waveform is generated, and pulse amplification means for outputting a high voltage pulse. 20 25

13. An ultrasound diagnostic apparatus according to claim 1, wherein the discharge means applies a voltage between the electrodes, the voltage being equal to or lower than a collapse voltage. 30

14. An ultrasound diagnostic apparatus according to claim 1, wherein the discharge means is DC bias application means whose polarity is opposite that of the DC bias application means. 35

15. An ultrasound diagnostic apparatus according to claim 14, further comprising reporting means for reporting that the discharge means is discharging the charge. 40

16. An ultrasound diagnostic apparatus according to claim 1, wherein the discharge means discharges a charge accumulated in a diaphragm, which is a portion of the insulating portion. 45

17. An ultrasound diagnostic apparatus according to claim 1, further comprising communication means for communicating with an external control apparatus provided at a location different from the location of the ultrasound diagnostic apparatus, wherein the discharge means discharges the charge accumulated in the insulating portion between the electrodes on the basis of information from the external control apparatus. 50

18. A method of calibrating an ultrasound diagnostic apparatus comprising an ultrasound probe equipped with an ultrasound transducer including a pair of electrodes and an insulating portion and a sacrificial layer provided between the electrodes, and DC-bias application means for applying a DC bias between the electrodes, the method being **characterized by** comprising: 55

a monitor step of monitoring an amount of charge accumulated in the insulating portion between the electrodes;

a discharge step of discharging the charge accumulated in the insulating portion between the electrodes on the basis of the amount of charge; and

a step of repeating the monitor step and the discharge step.

19. A method of calibrating an ultrasound diagnostic apparatus according to claim 18, wherein the monitor step includes:

a storage step of acquiring and holding an application voltage and an application time of the DC bias applied between the electrodes;

a calculation step of calculating and holding the sum of product of the application voltage and the application time;

a comparison step of comparing the sum of product with a predetermined threshold value; and

a start step of starting the discharge step when the sum of product reaches the predetermined threshold value.

20. A method of calibrating an ultrasound diagnostic apparatus according to claim 18, further comprising, between the monitor step and the discharge step, a communication step of transmitting and receiving information of the charge amount between the ultrasound diagnostic apparatus and an external control apparatus provided at a location different from the location of the ultrasound diagnostic apparatus, wherein the discharge step is executed on the basis of the information from the external control apparatus.

FIG.1

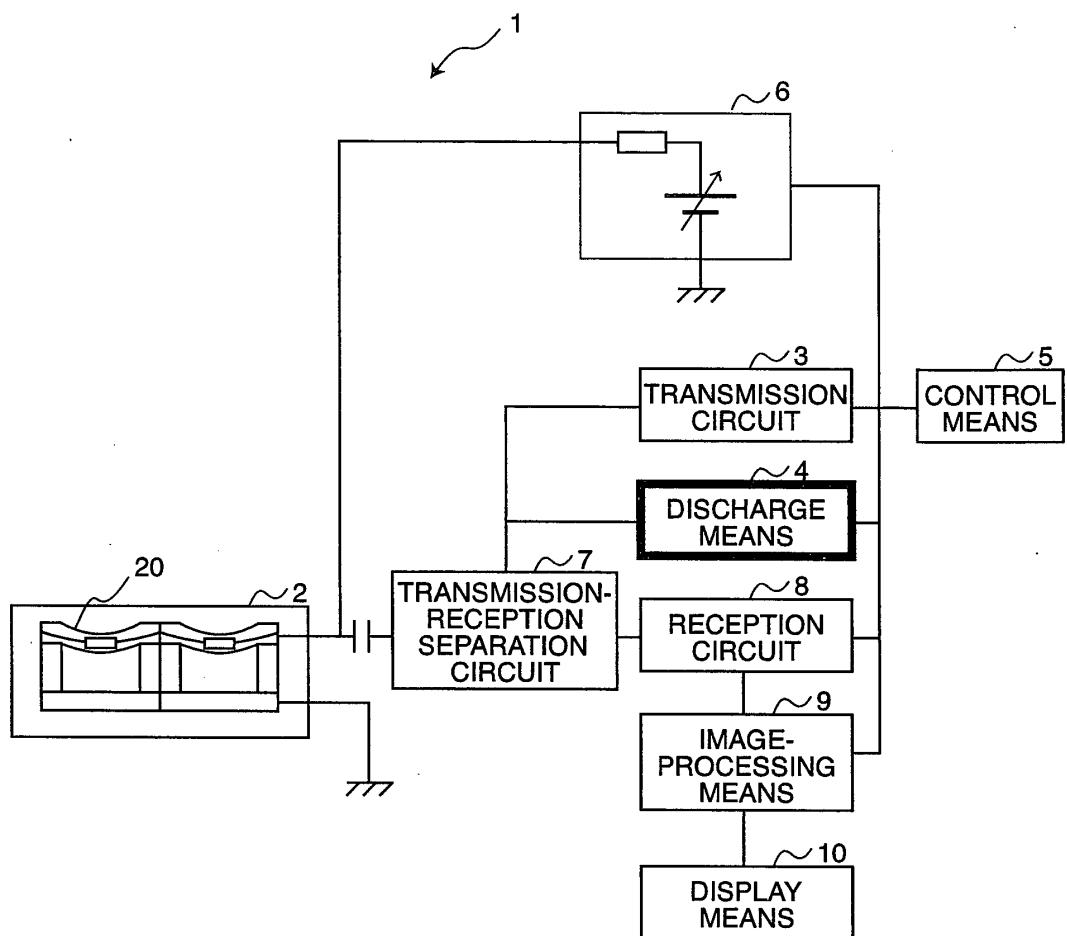


FIG.2

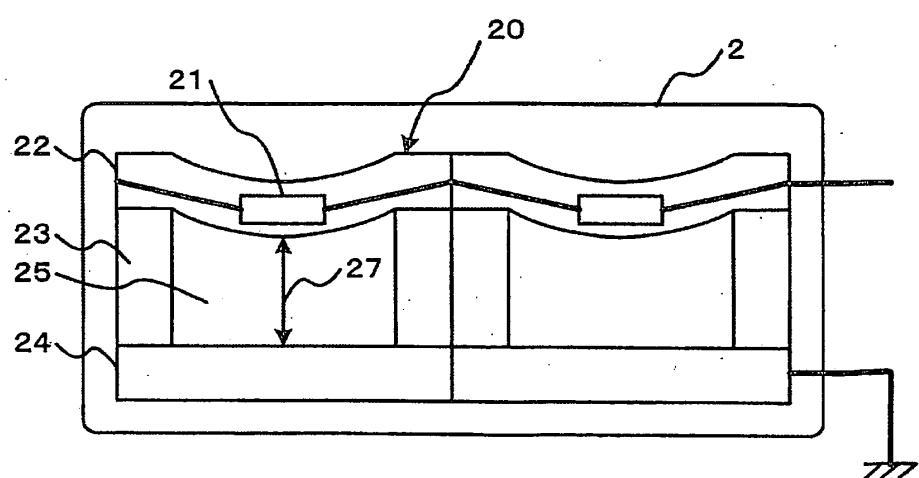


FIG.3

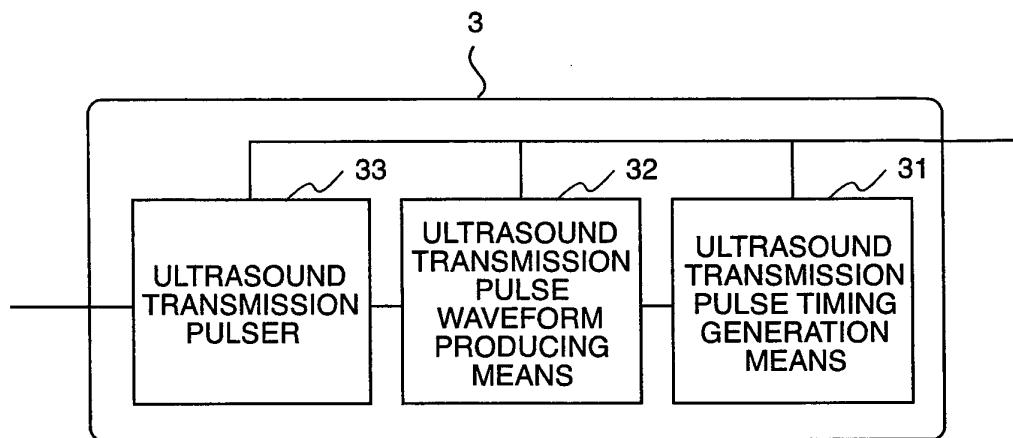


FIG.4

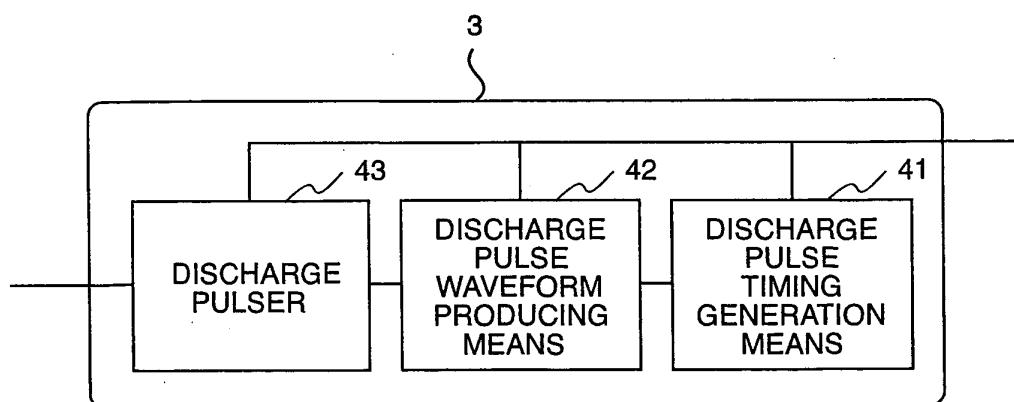


FIG.5

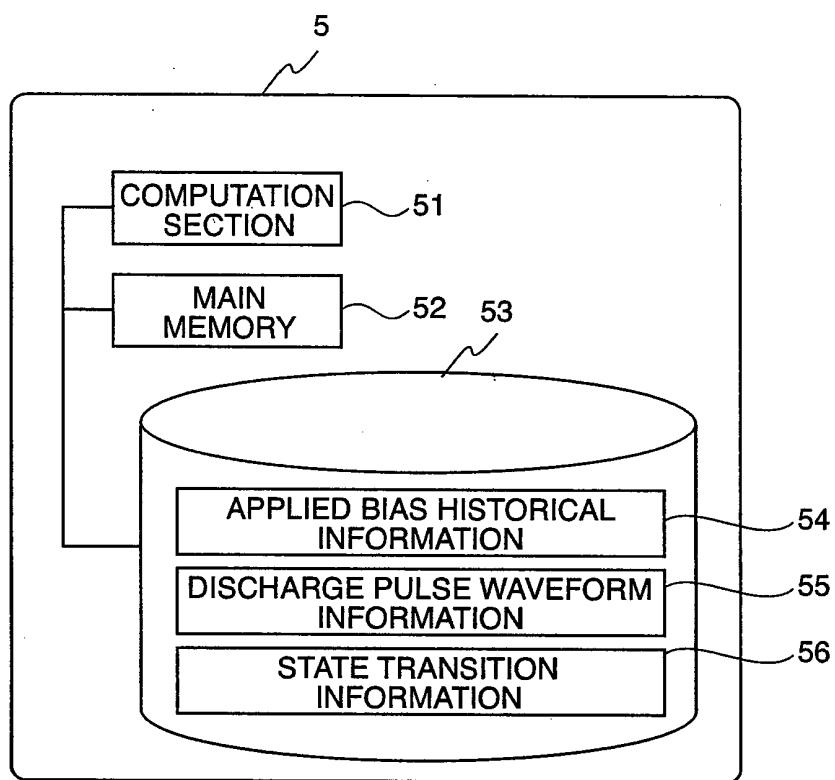


FIG.6

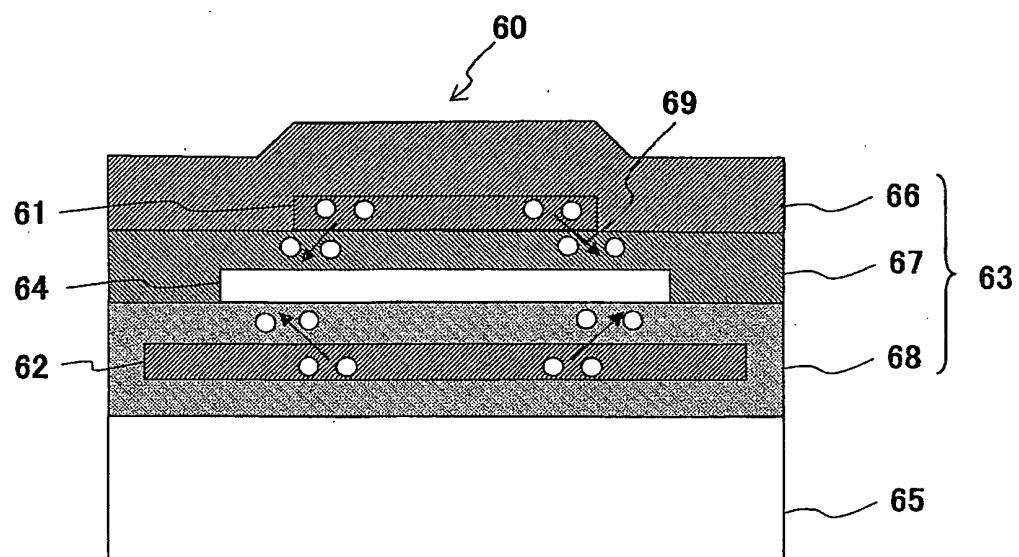


FIG.7

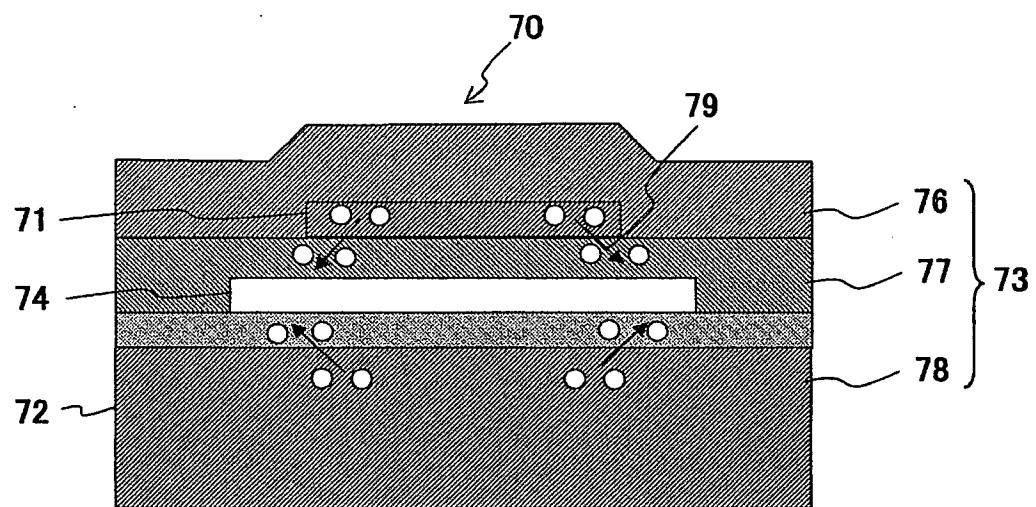


FIG.8

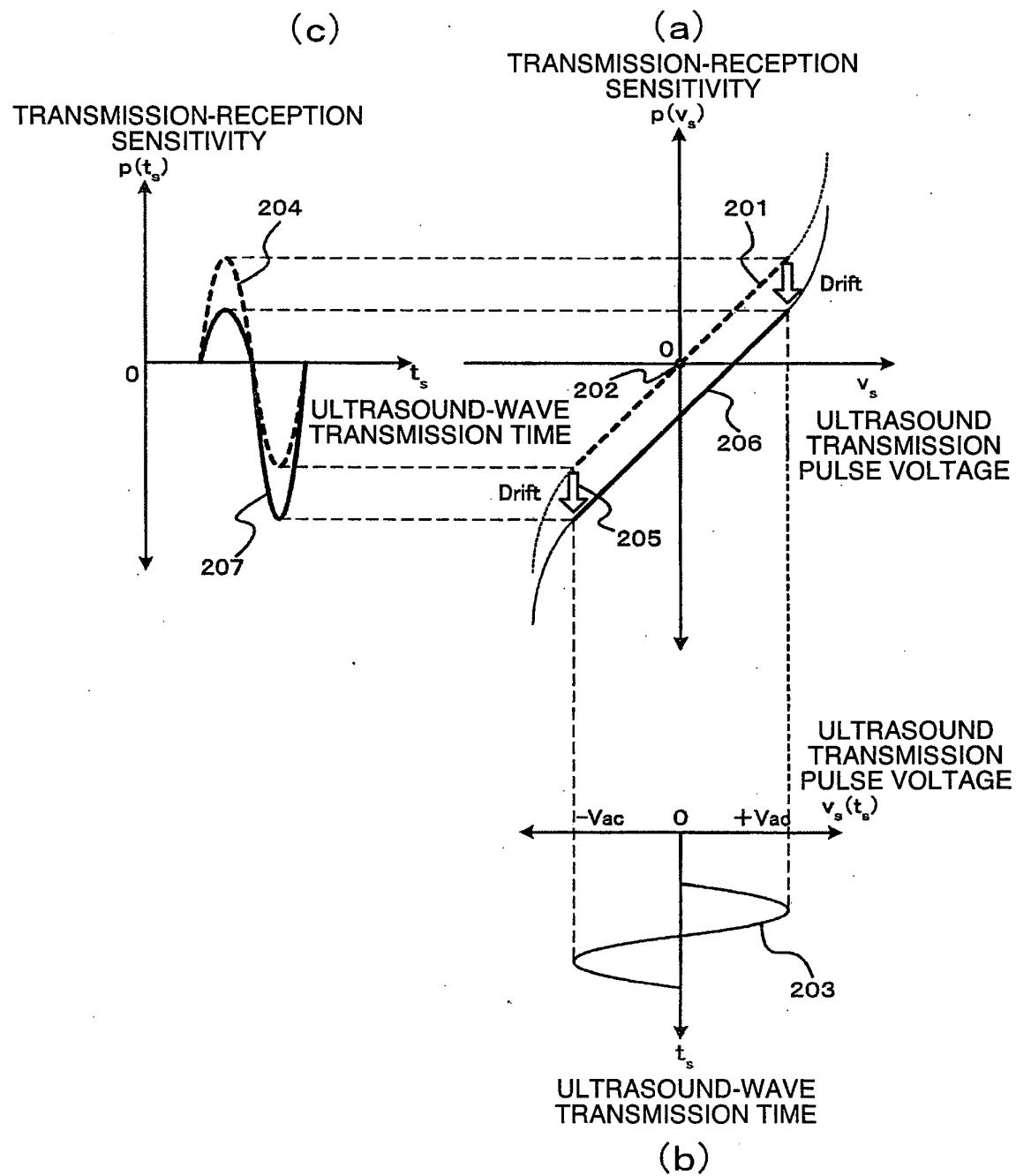


FIG.9

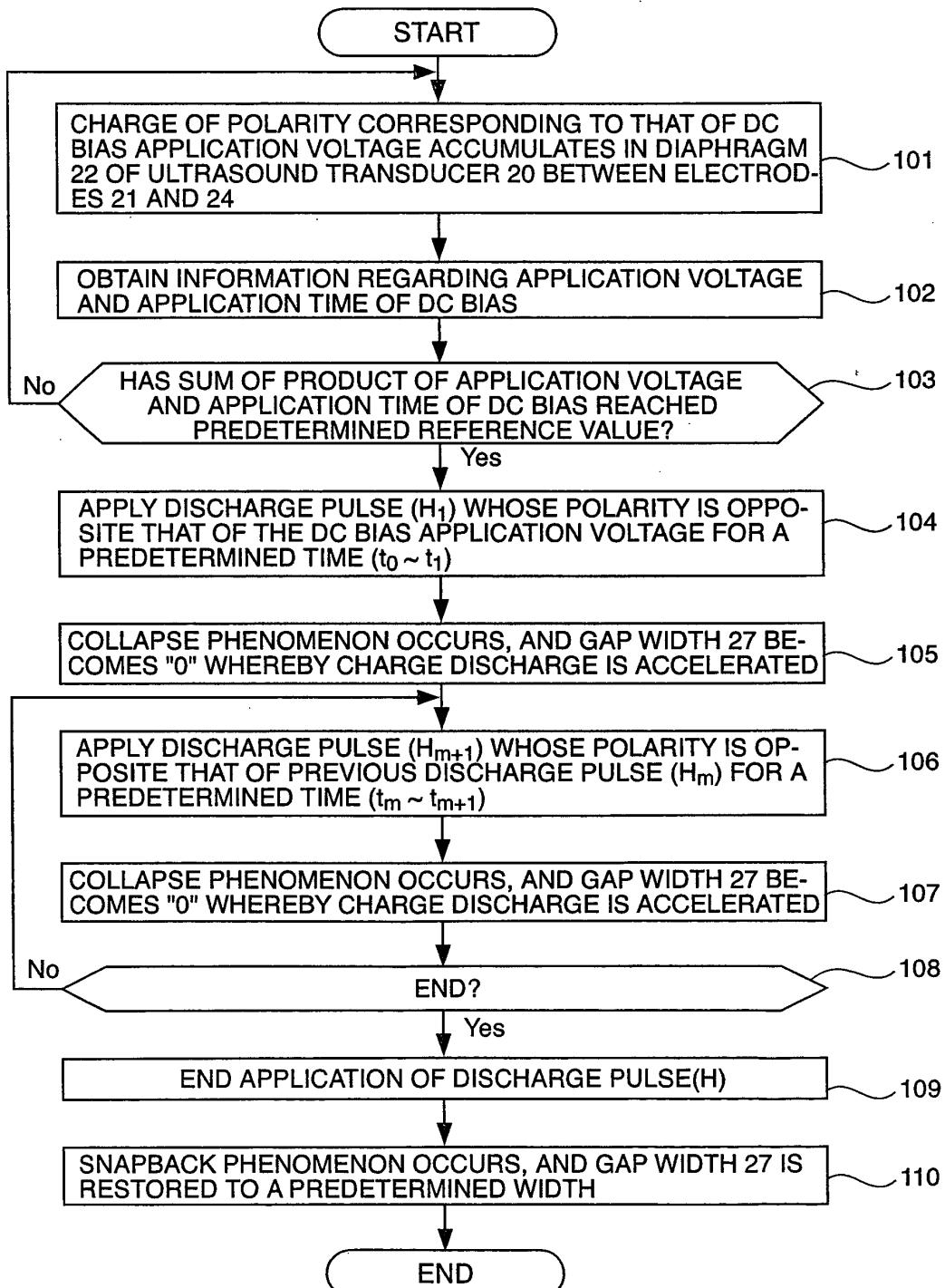


FIG.10

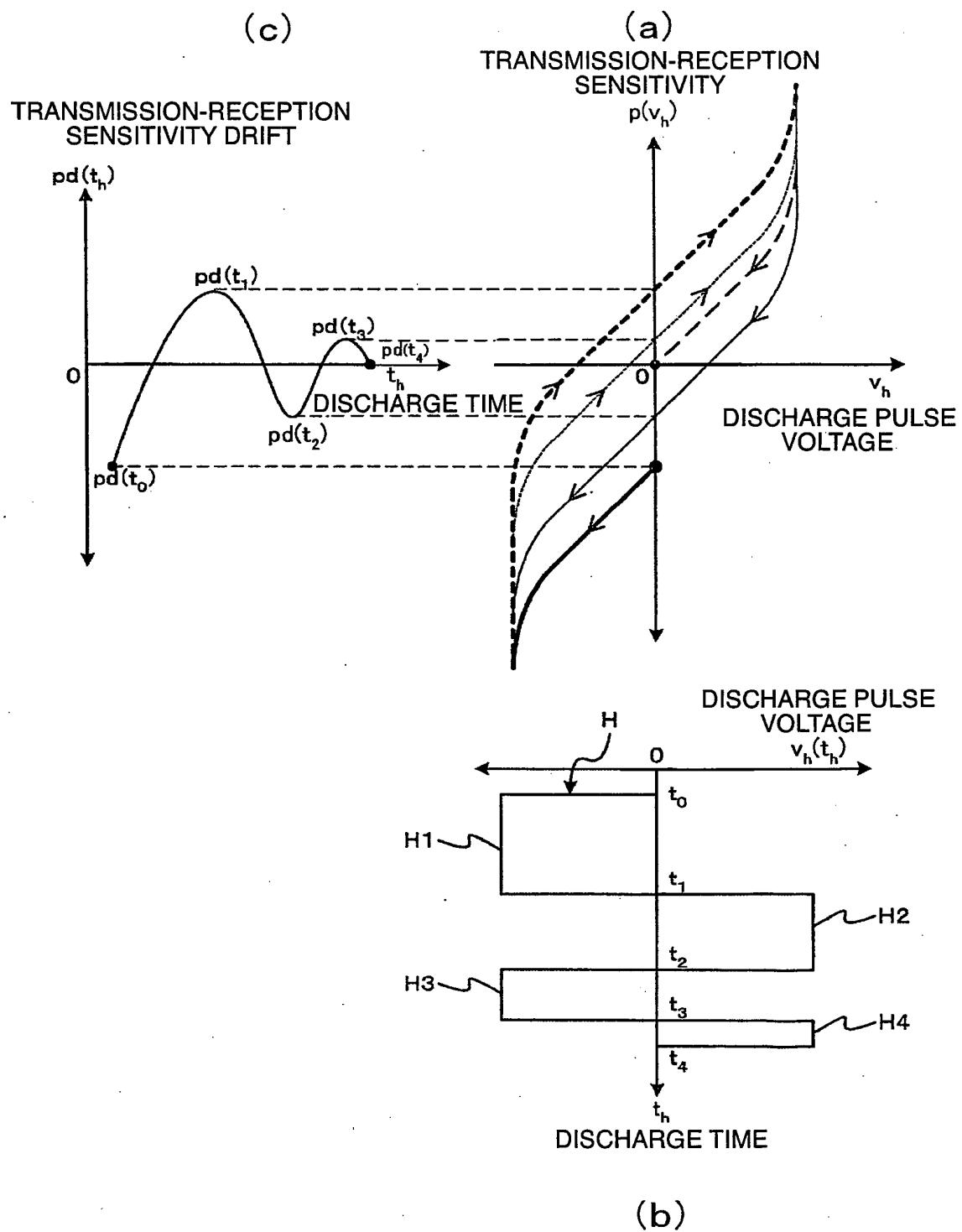


FIG.11

Diagram above the table:

110 → 54 → 111, 112, 113, 114, 115, 116 → 56

DATE	TIME	APPLICATION TIME	APPLICATION VOLTAGE	SUM OF PRODUCT	STATE
2006/9/12	10:00~12:00	2h	100V	200V·h	0.67%
2006/9/12	9:30~17:30	8h	100V	1000V·h	3.33%
⋮	⋮	⋮	⋮	⋮	⋮
2006/12/14	10:00~17:00	7h	100V	30000V·h	100%
2006/12/14	17:30	-	-	-	DISCHARGE
2006/12/15	10:00~13:00	3h	100V	300V·h	1.00%
⋮	⋮	⋮	⋮	⋮	⋮

FIG.12

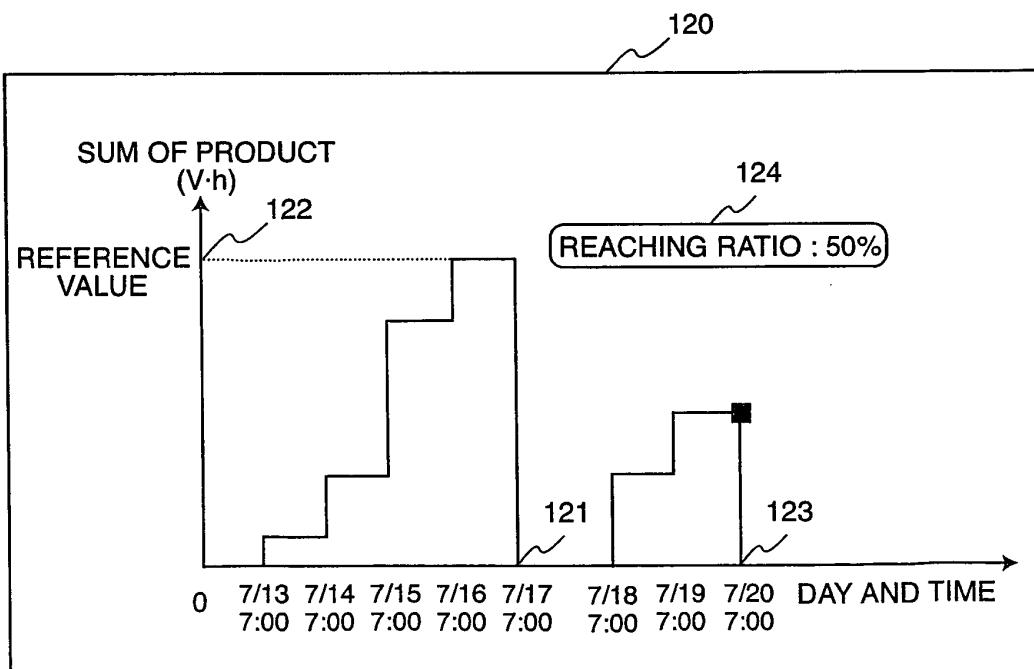


FIG.13

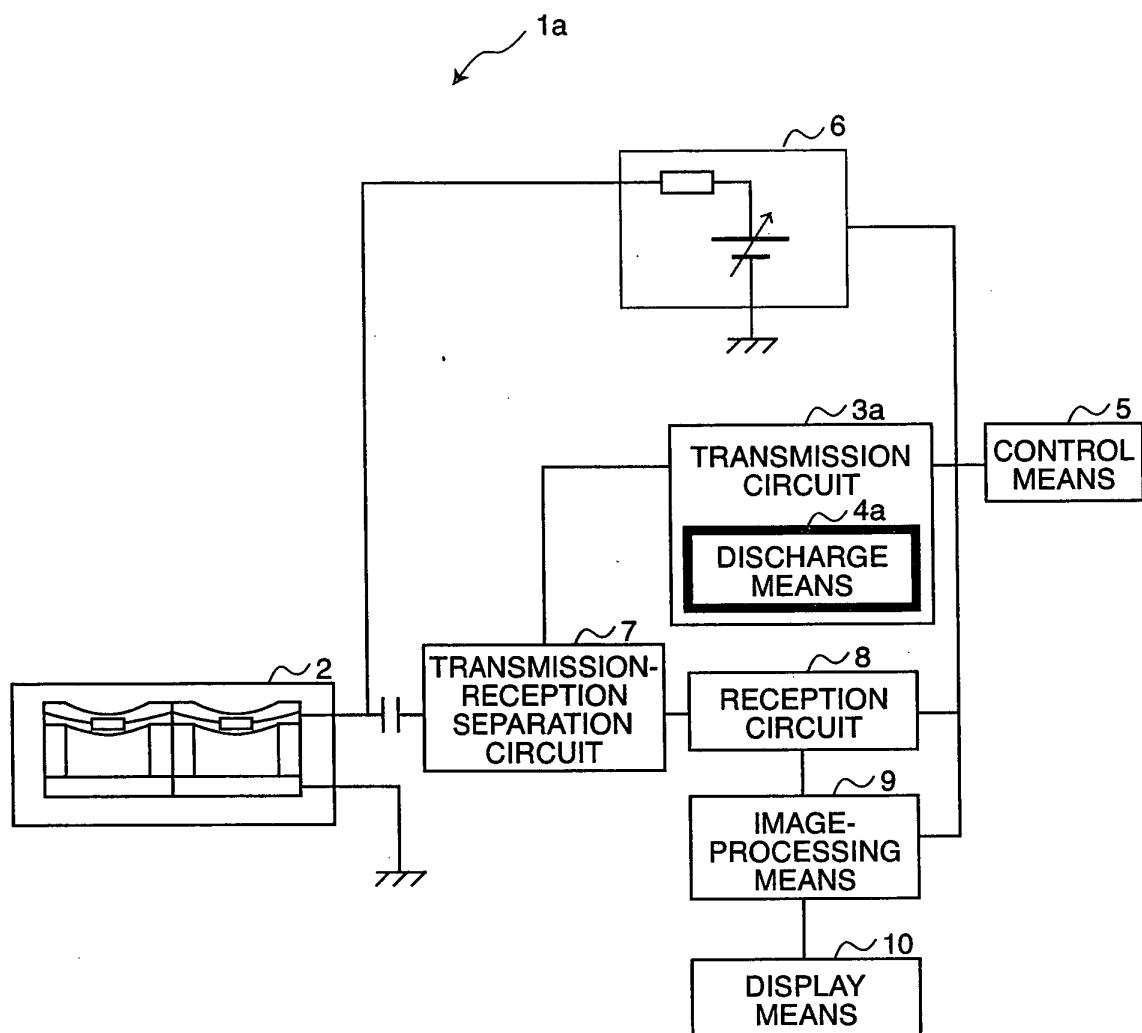


FIG.14

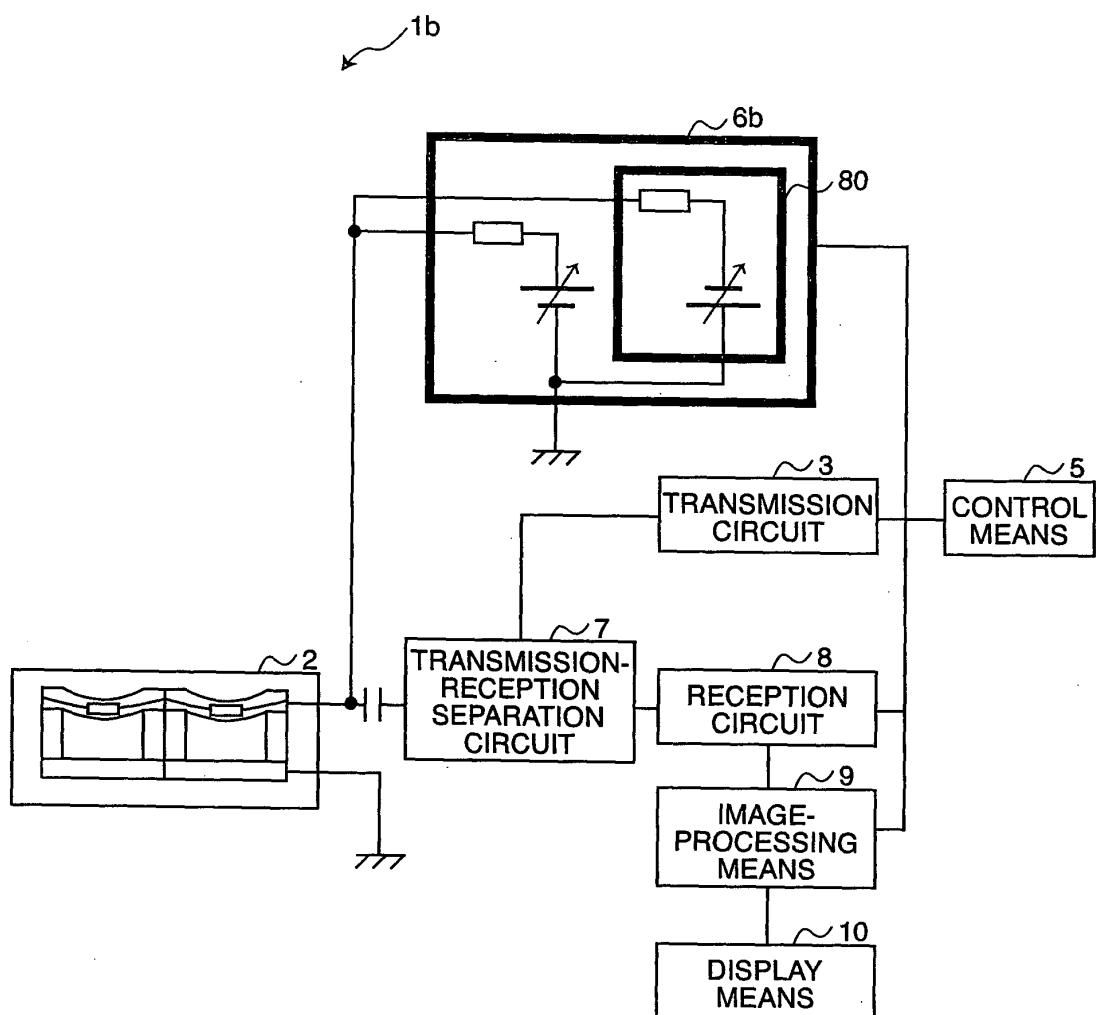
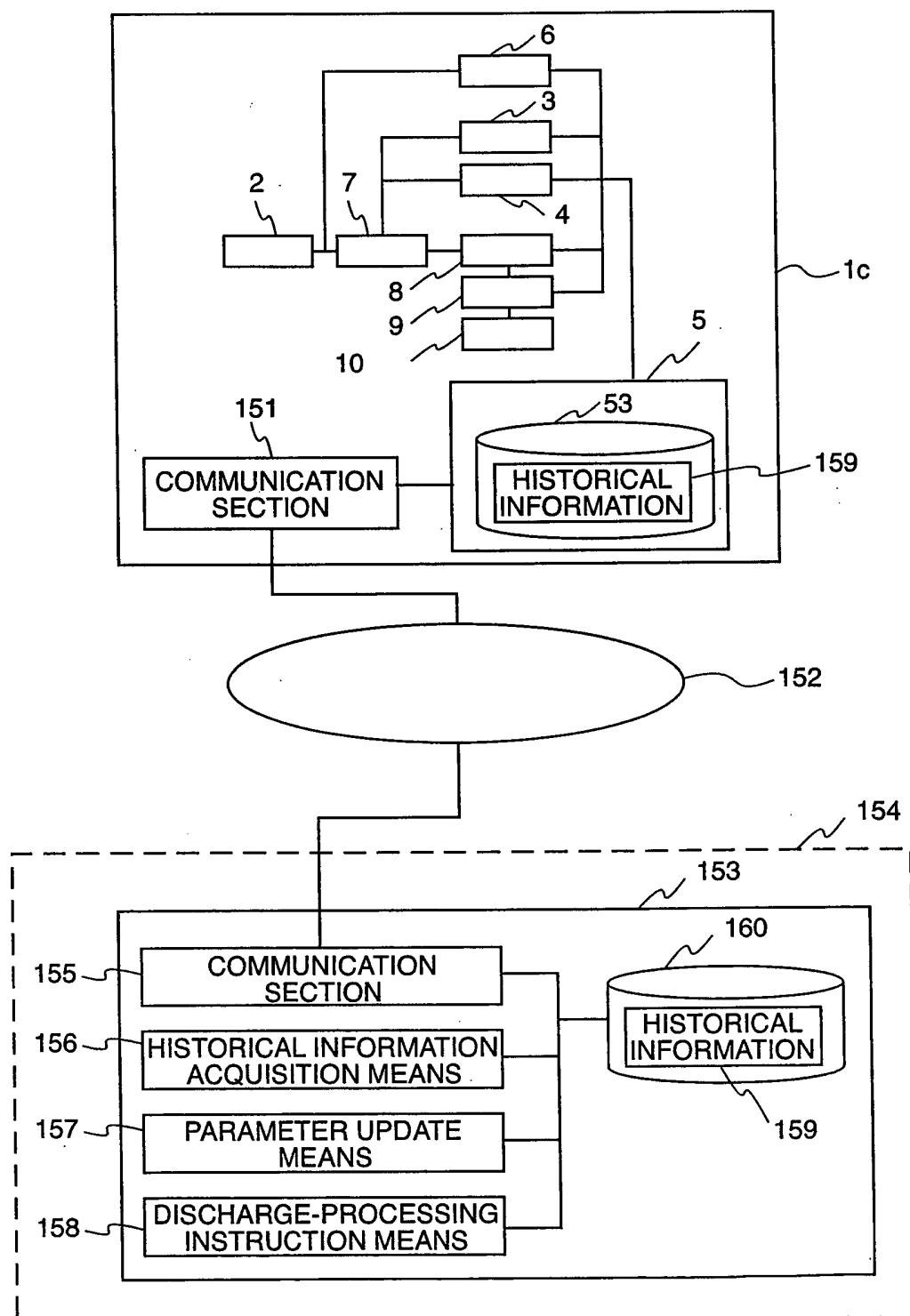


FIG.15



INTERNATIONAL SEARCH REPORT		International application No. PCT/JP2006/321228																				
<p>A. CLASSIFICATION OF SUBJECT MATTER <i>A61B8/00(2006.01)i, G01N29/24(2006.01)i, H04R3/00(2006.01)i</i></p>																						
According to International Patent Classification (IPC) or to both national classification and IPC																						
<p>B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) <i>A61B8/00, G01N29/24, H04R3/00</i></p>																						
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 33.33%; text-align: center;">Jitsuyo Shinan Koho</td> <td style="width: 33.33%; text-align: center;">1922-1996</td> <td style="width: 33.33%; text-align: center;">Jitsuyo Shinan Toroku Koho</td> <td style="width: 33.33%; text-align: center;">1996-2007</td> </tr> <tr> <td>Kokai Jitsuyo Shinan Koho</td> <td>1971-2007</td> <td>Toroku Jitsuyo Shinan Koho</td> <td>1994-2007</td> </tr> </table>			Jitsuyo Shinan Koho	1922-1996	Jitsuyo Shinan Toroku Koho	1996-2007	Kokai Jitsuyo Shinan Koho	1971-2007	Toroku Jitsuyo Shinan Koho	1994-2007												
Jitsuyo Shinan Koho	1922-1996	Jitsuyo Shinan Toroku Koho	1996-2007																			
Kokai Jitsuyo Shinan Koho	1971-2007	Toroku Jitsuyo Shinan Koho	1994-2007																			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)																						
<p>C. DOCUMENTS CONSIDERED TO BE RELEVANT</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">Category*</th> <th style="width: 80%;">Citation of document, with indication, where appropriate, of the relevant passages</th> <th style="width: 10%;">Relevant to claim No.</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">A</td> <td>JP 2004-503312 A (Koninklijke Philips Electronics N.V.), 05 February, 2004 (05.02.04), Par. Nos. [0025] to [0026]; Figs. 1, 6 & US 6328696 B1</td> <td style="text-align: center;">1-20</td> </tr> <tr> <td style="text-align: center;">A</td> <td>Baris Bayram et al, Capacitive Micromachined Ultrasonic Transducer Design for High Power Transmission, IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, vol. 52, no. 2, 2005.02, pp.326-339</td> <td style="text-align: center;">1-20</td> </tr> </tbody> </table>			Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.	A	JP 2004-503312 A (Koninklijke Philips Electronics N.V.), 05 February, 2004 (05.02.04), Par. Nos. [0025] to [0026]; Figs. 1, 6 & US 6328696 B1	1-20	A	Baris Bayram et al, Capacitive Micromachined Ultrasonic Transducer Design for High Power Transmission, IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, vol. 52, no. 2, 2005.02, pp.326-339	1-20											
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A	Baris Bayram et al, Capacitive Micromachined Ultrasonic Transducer Design for High Power Transmission, IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, vol. 52, no. 2, 2005.02, pp.326-339	1-20																				
<input type="checkbox"/> Further documents are listed in the continuation of Box C.		<input type="checkbox"/> See patent family annex.																				
<p>* Special categories of cited documents:</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%; text-align: center;">"A"</td> <td style="width: 85%;">document defining the general state of the art which is not considered to be of particular relevance</td> <td style="width: 15%; text-align: center;">"T"</td> <td>later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</td> </tr> <tr> <td style="text-align: center;">"E"</td> <td>earlier application or patent but published on or after the international filing date</td> <td style="text-align: center;">"X"</td> <td>document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</td> </tr> <tr> <td style="text-align: center;">"L"</td> <td>document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</td> <td style="text-align: center;">"Y"</td> <td>document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</td> </tr> <tr> <td style="text-align: center;">"O"</td> <td>document referring to an oral disclosure, use, exhibition or other means</td> <td style="text-align: center;">"&"</td> <td>document member of the same patent family</td> </tr> <tr> <td style="text-align: center;">"P"</td> <td>document published prior to the international filing date but later than the priority date claimed</td> <td></td> <td></td> </tr> </table>			"A"	document defining the general state of the art which is not considered to be of particular relevance	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention	"E"	earlier application or patent but published on or after the international filing date	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone	"L"	document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art	"O"	document referring to an oral disclosure, use, exhibition or other means	"&"	document member of the same patent family	"P"	document published prior to the international filing date but later than the priority date claimed		
"A"	document defining the general state of the art which is not considered to be of particular relevance	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention																			
"E"	earlier application or patent but published on or after the international filing date	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone																			
"L"	document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art																			
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"P"	document published prior to the international filing date but later than the priority date claimed																					
Date of the actual completion of the international search 10 January, 2007 (10.01.07)		Date of mailing of the international search report 16 January, 2007 (16.01.07)																				
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer																				
Facsimile No.		Telephone No.																				

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Patent documents cited in the description

- JP 2005333797 A [0001]
- JP 2004503312 W [0003]

专利名称(译)	超声诊断设备及其校准方法		
公开(公告)号	EP1952767A4	公开(公告)日	2014-06-18
申请号	EP2006822205	申请日	2006-10-25
[标]申请(专利权)人(译)	株式会社日立医药		
申请(专利权)人(译)	日立医疗器械股份有限公司		
当前申请(专利权)人(译)	日立医疗器械股份有限公司		
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其他公开文献	EP1952767B1 EP1952767A1		
外部链接	Espacenet		

摘要(译)

超声波诊断装置1使放电装置4多次在超声波换能器20的电极之间施加放电脉冲(峰值等于或大于塌陷电压)，同时每次反转放电脉冲的极性，从而加速在超声波换能器20的振动膜中累积的电荷的放电，从而可以使发送-接收灵敏度偏移快速接近零。电荷瞬间放电。也就是说，可以快速校准在向其施加DC偏压时流入电极之间的隔膜的电荷的时间累积产生的发送-接收灵敏度漂移。