



US 20180242872A1

(19) **United States**(12) **Patent Application Publication**
FIROOZABADI et al.(10) **Pub. No.: US 2018/0242872 A1**(43) **Pub. Date: Aug. 30, 2018**(54) **HIGH/LOW FREQUENCY SIGNAL QUALITY
EVALUATIONS OF ECG LEAD SIGNALS**(52) **U.S. Cl.**CPC *A61B 5/04014* (2013.01); *A61B 5/7221*
(2013.01); *A61B 5/7203* (2013.01); *A61B*
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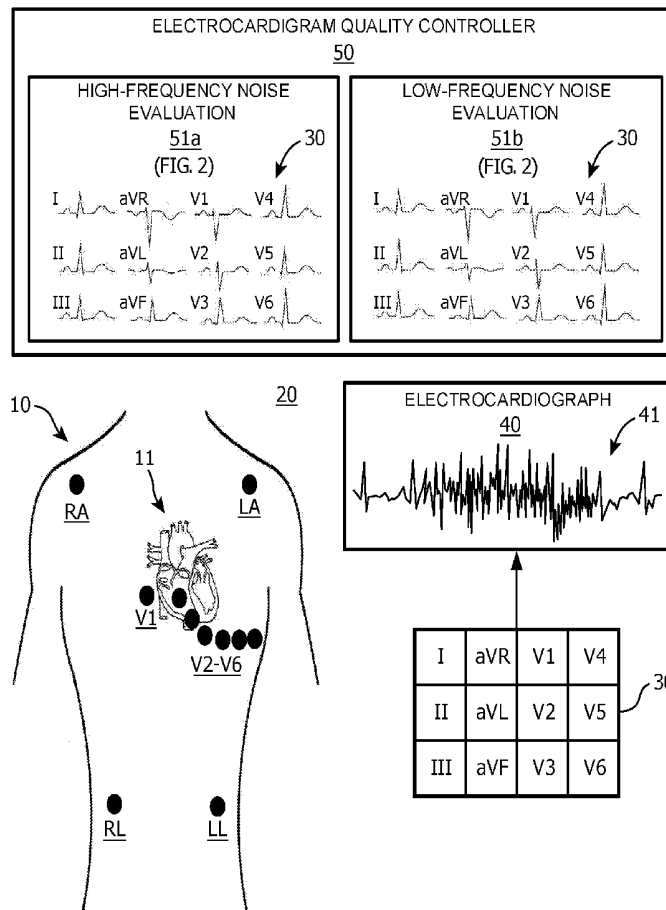
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ABSTRACT(21) Appl. No.: **15/754,675**(22) PCT Filed: **Aug. 25, 2016**(86) PCT No.: **PCT/IB2016/055061**

§ 371 (c)(1),

(2) Date: **Feb. 23, 2018****Related U.S. Application Data**(60) Provisional application No. 62/209,394, filed on Aug.
25, 2015.**Publication Classification**(51) **Int. Cl.***A61B 5/04* (2006.01)*A61B 5/044* (2006.01)*A61B 5/00* (2006.01)

A monitoring device (e.g., a ECG monitor or a Holter monitor) employing an electrocardiograph (40), and an ECG quality controller (50). In operation, the electrocardiograph (40) derives an electrocardiogram from one or more ECG leads (30) upon a connection of the electrocardiograph (40) to the ECG lead(s) 30 (e.g., electrode based or pad/paddle based). In response to the connection of the electrocardiograph (40) to the ECG lead(s) (30), the ECG quality controller (50) controls separate evaluations of a high-frequency noise level (e.g., a degree of muscle artifact and/or a degree of electrode motion artifact within the electrocardiogram) and a low-frequency noise level (e.g., a degree of baseline wander by the electrocardiogram) of an individual ECG lead (30) on an electrocardiogram segmentation basis (e.g., a continual ECG recording divided into segments of a specified duration).



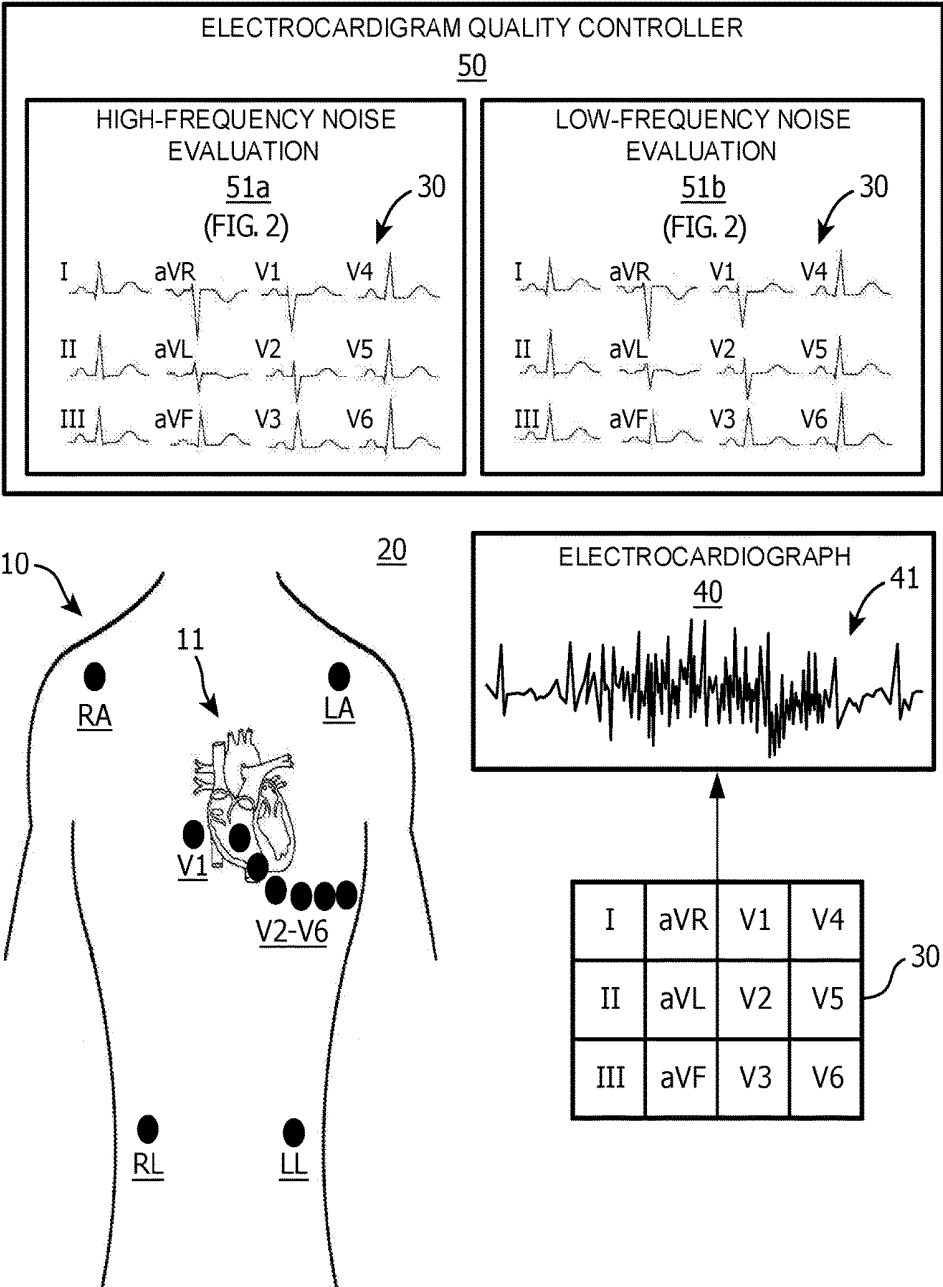


FIG. 1A

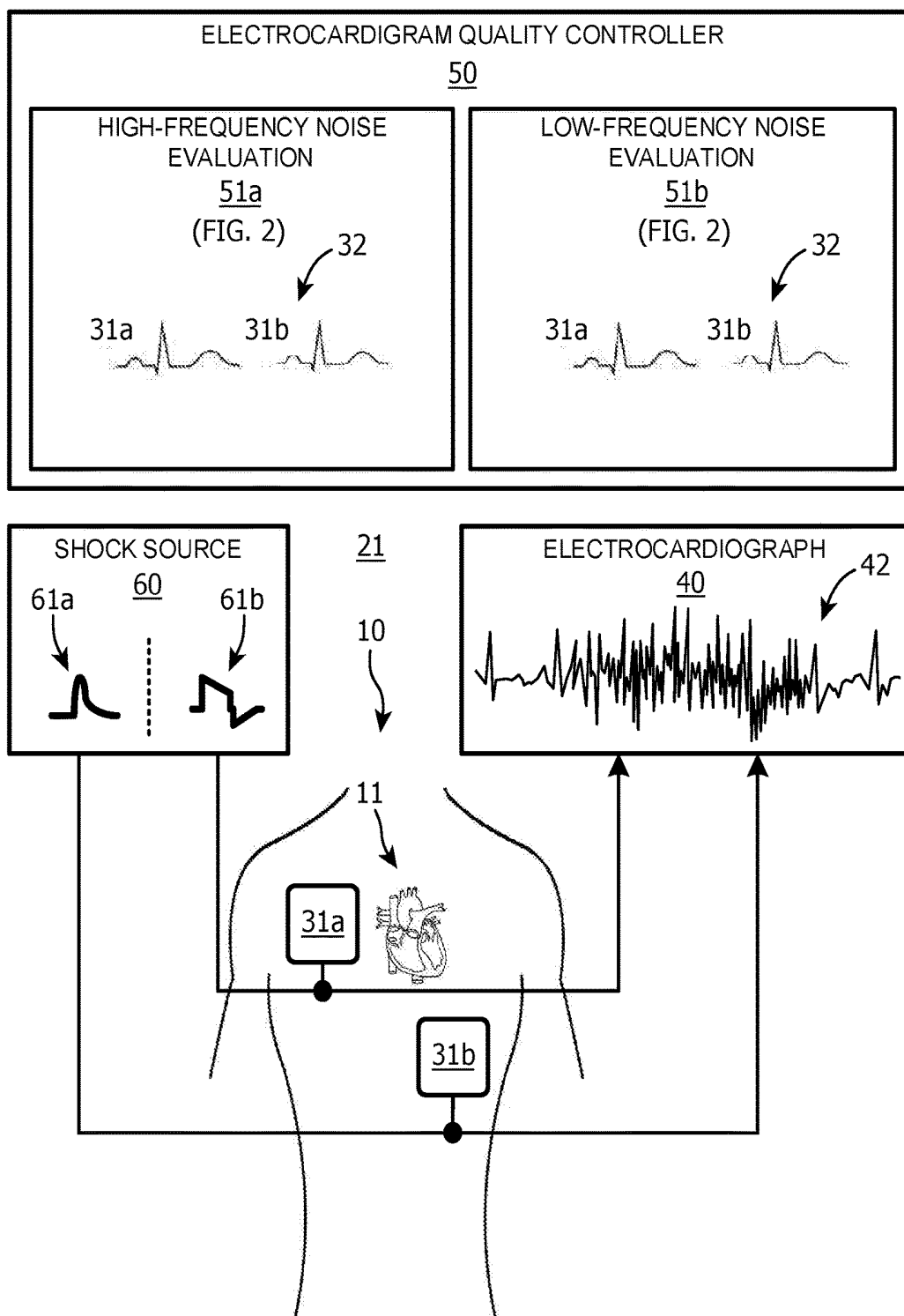


FIG. 1B

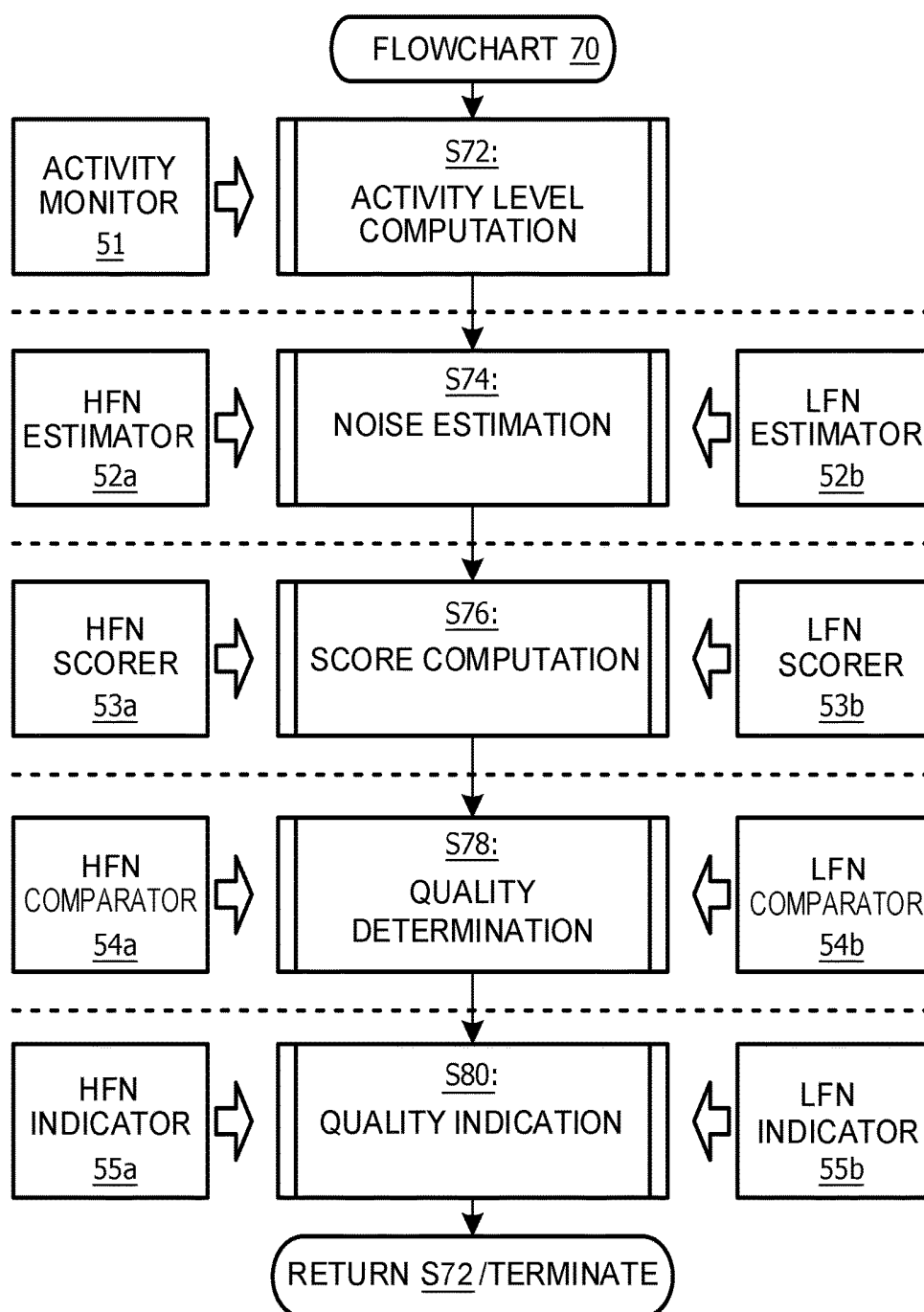


FIG. 2

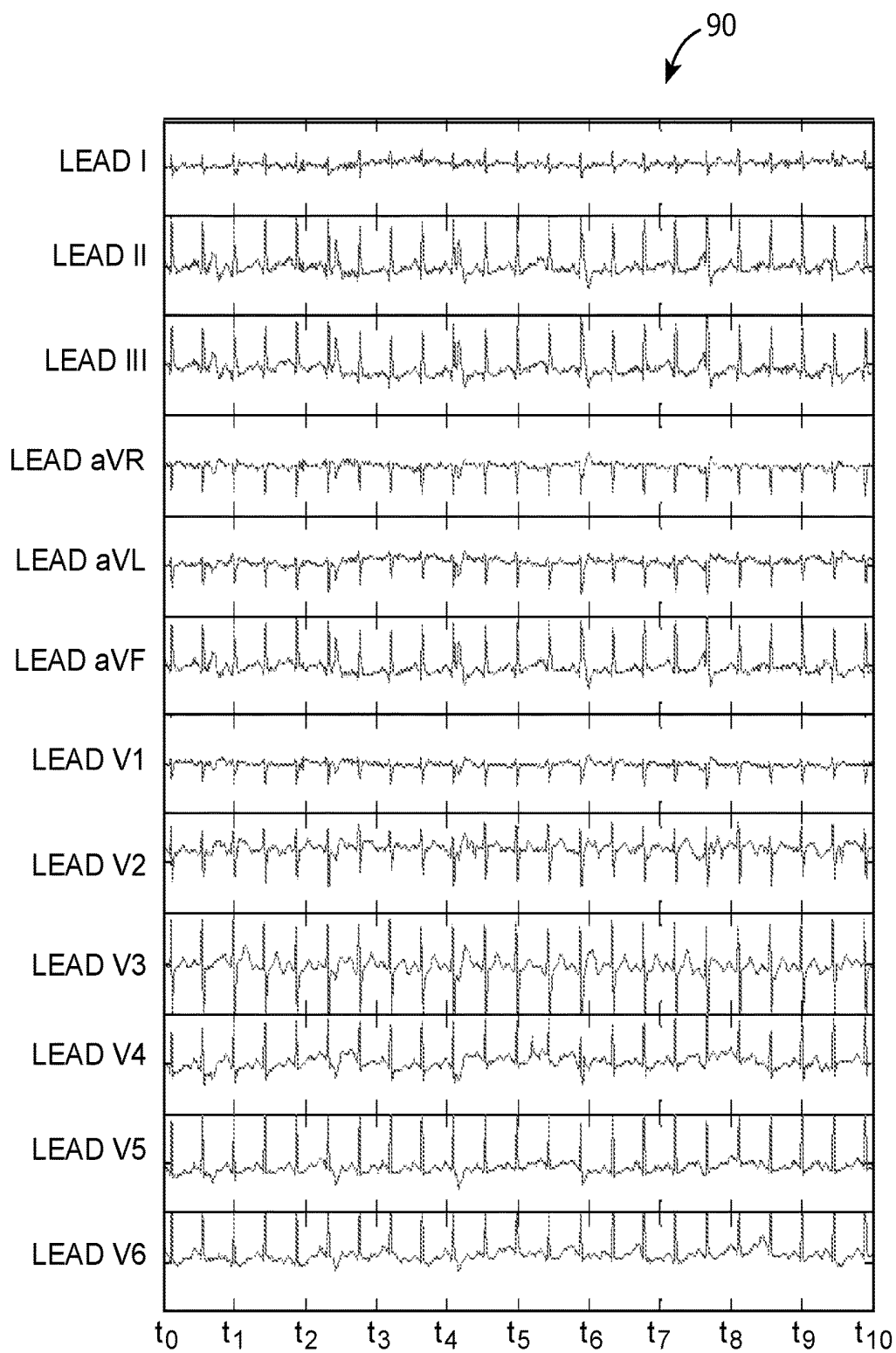


FIG. 3

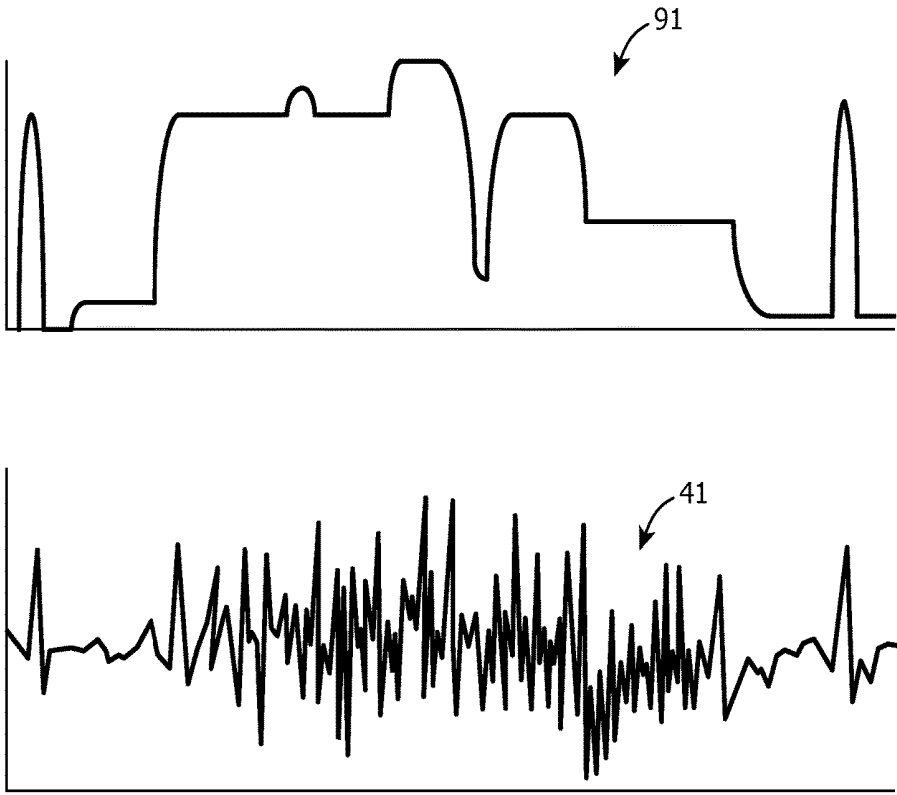


FIG. 4

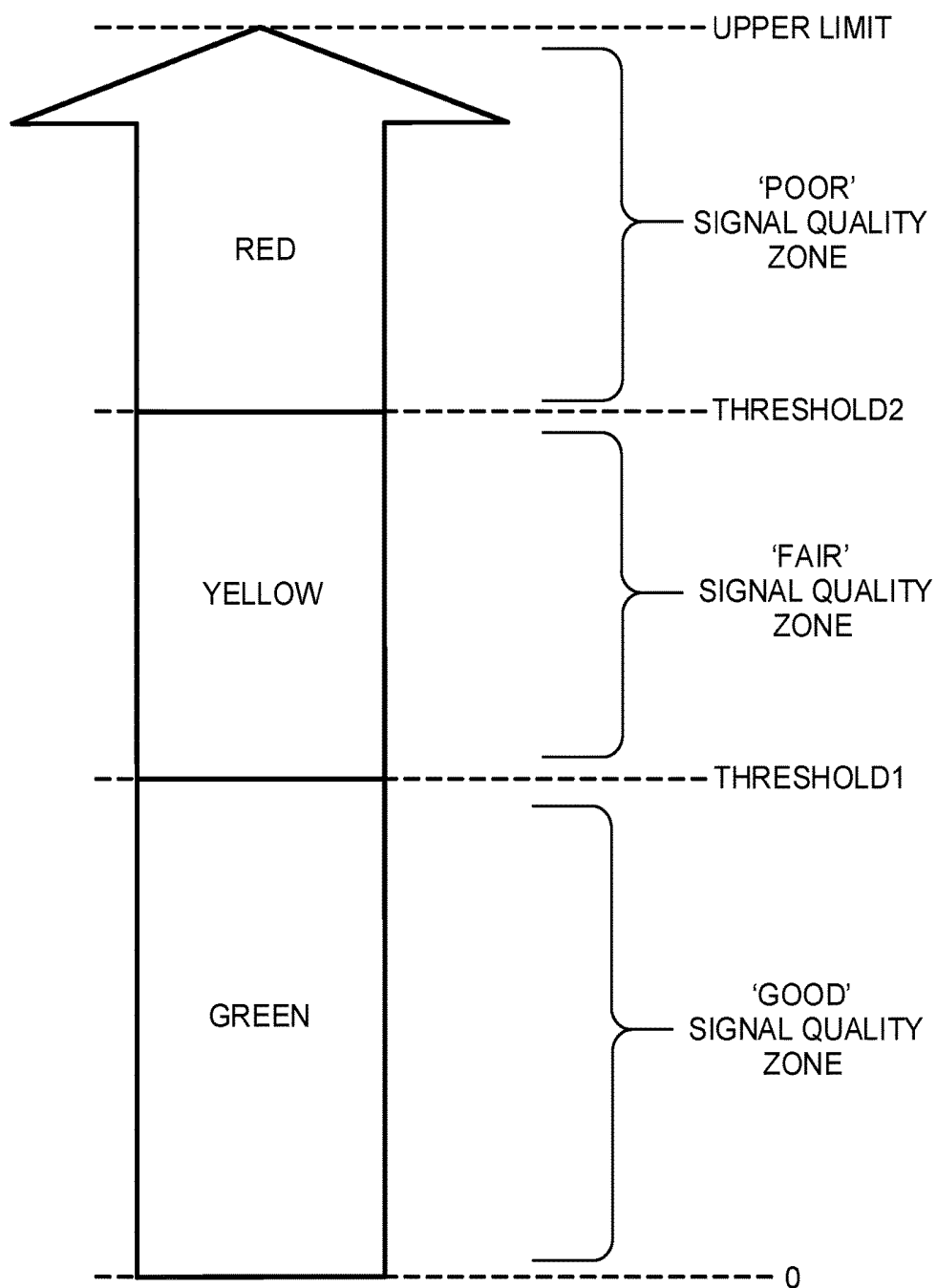


FIG. 5

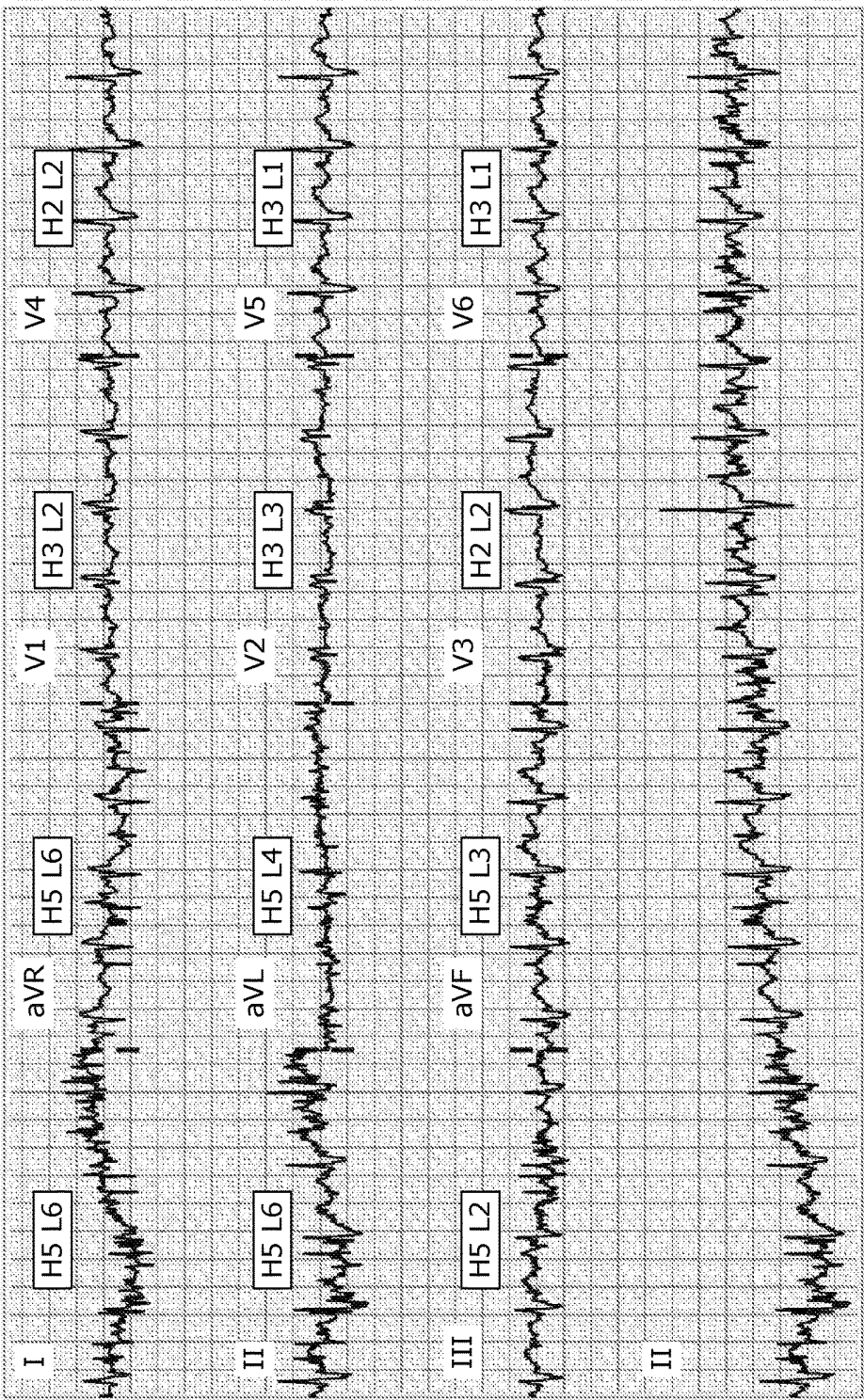


FIG. 6

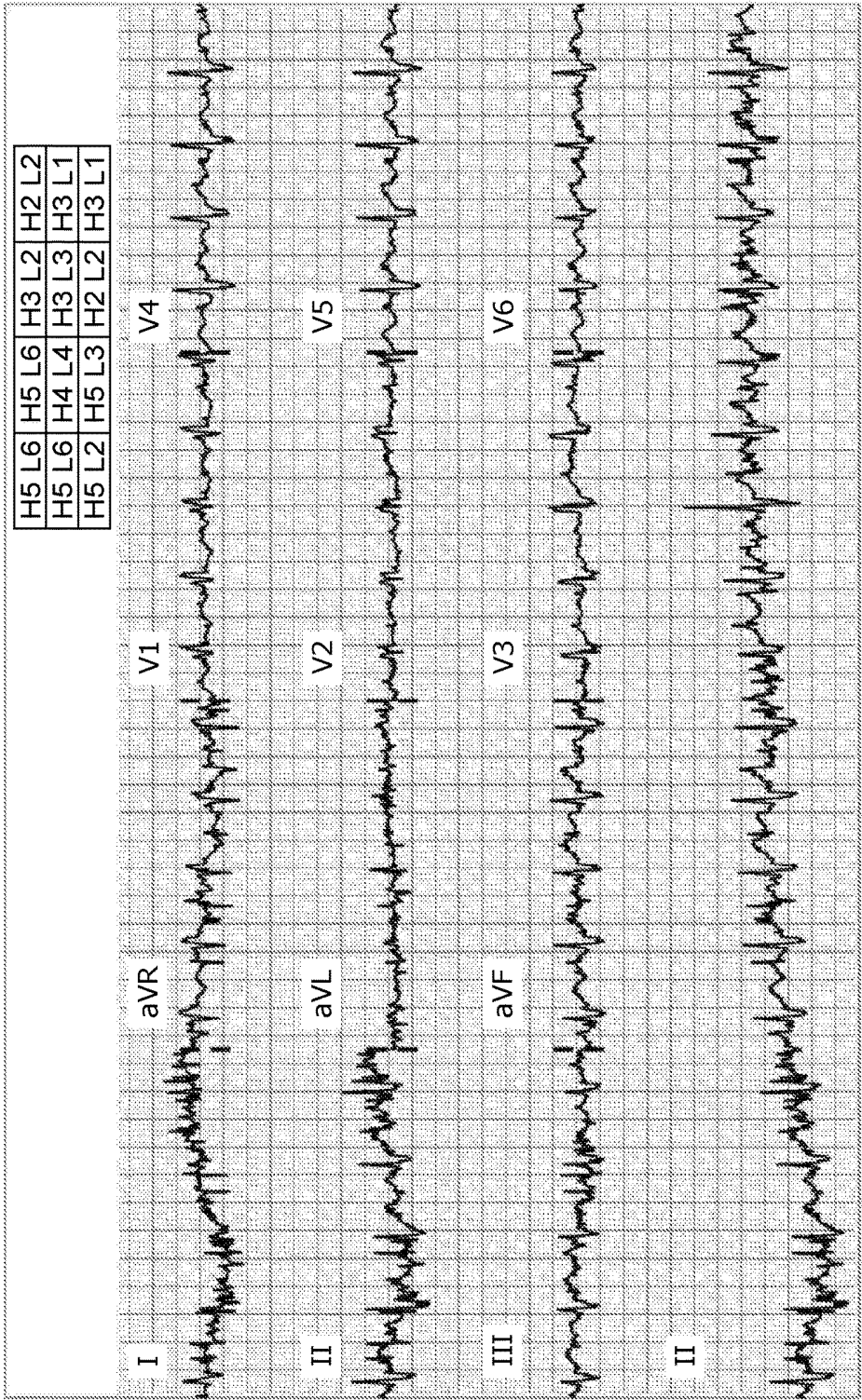


FIG. 7

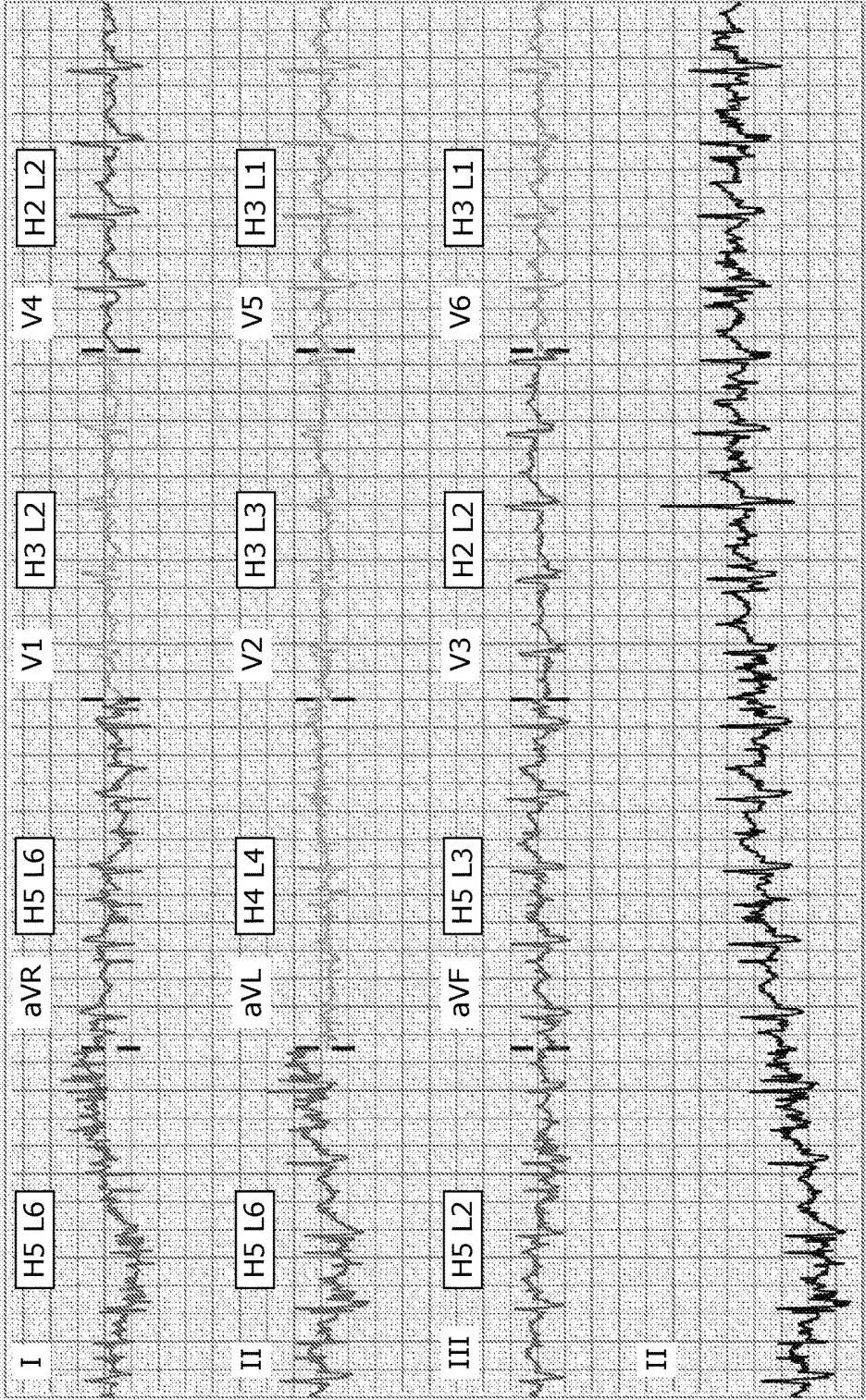


FIG. 8

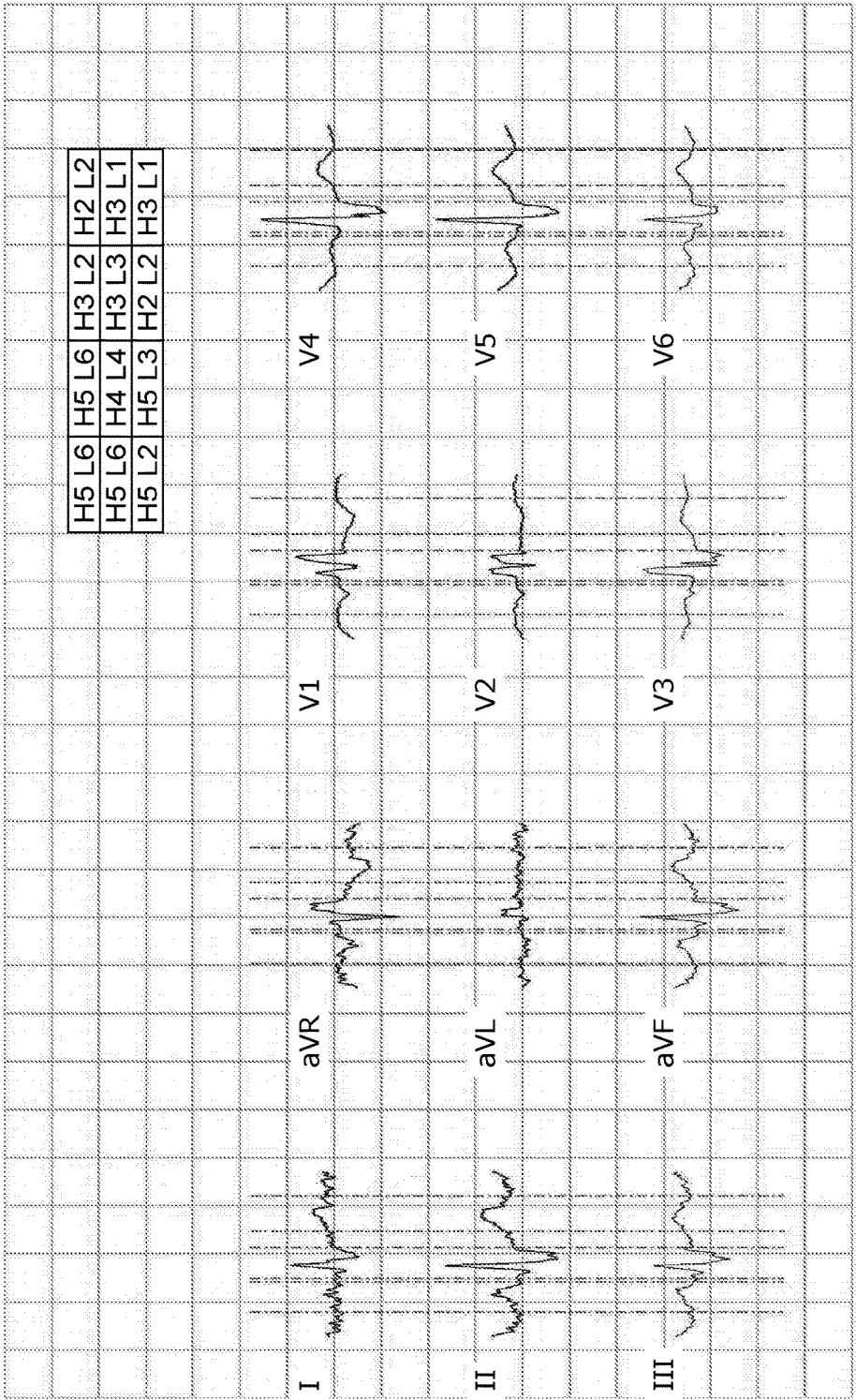


FIG. 9

HIGH/LOW FREQUENCY SIGNAL QUALITY EVALUATIONS OF ECG LEAD SIGNALS

FIELD OF THE INVENTION

[0001] The present disclosure generally relates to signal quality of each electrocardiogram (“ECG”) lead in an ECG recording during continuous ECG monitoring of a patient. The present disclosure more particularly relates an evaluation and visually display of high-frequency and low-frequency noise levels within electrocardiogram segments of each ECG lead in an ECG recording during continuous ECG monitoring of a patient.

BACKGROUND OF THE INVENTION

[0002] Electrocardiography is a non-invasive procedure for recording a multi-lead electrocardiogram (“ECG”) (e.g., a standard 12-lead ECG or a non-standard EASI lead ECG) as a representation of an electrical cardiac activity of a patient conducted to vector cardiogram or other ECG lead system electrodes on the body surface of the patient. Electrocardiography is utilized in a wide variety of clinical settings ranging from physical examinations for regular checkups and emergencies to monitoring for the preparation, operative and recovery phases of surgical/diagnostic procedures. The purpose of the electrocardiography for these setting is a detection and evaluation of cardiac problems including, but not limited to, arrhythmias (commonly known as an irregular heartbeat), tachycardia (commonly known as a fast heartbeat), bradycardia (commonly known as a slow heartbeat), and myocardial infarction (commonly known as a heart attack). Thus, a high-quality ECG in terms of accuracy is imperative to a reliable cardiac diagnosis and monitoring.

[0003] More particularly, various sources of noise induced on an ECG recording may mask diagnostic features of the ECG, which may result in an inaccurate interpretation of the ECG recordings and resulting diagnosis by a monitoring device and/or operator of the monitoring device. In an extreme case, a high level of noise on the ECG recording makes it impossible to interpret the ECG at all. Clearly, a low-quality ECG may cause a high number of false alarms generated by monitoring devices, particularly in critical care units (CCU) and intensive care units (ICU), which in turn may cause alarm in medical personnel.

[0004] One beneficial method known in the art for evaluating a signal quality of an ECG recording involves a determination of an average level of all types of noise in all leads as a single noise score for long term ECG signals and further involves a display of a compressed time-scale color-bar as the signal quality indicator. This method is very useful in rapid selection of high-quality ECG signal as representative segments to review for detailed analysis of rhythm and morphology. The present disclosure is directed to complementing the known method by determining and displaying a quality of real-time multi-lead short segments shown on a monitor screen and a reliability of each plot based on the level of noise categories including high-frequency noise and low-frequency noise.

SUMMARY OF THE INVENTION

[0005] The present disclosure provides inventions providing an evaluation and visual display of high-frequency and low-frequency noise levels within electrocardiogram seg-

ments of each ECG lead in an ECG recording during a continuous ECG monitoring of a patient.

[0006] For purposes of the present invention, the term “high-frequency” broadly encompasses high-frequency noise of a ECG lead as known in the art including, but not limited to, muscle artifact and electrode motion artifact of ECG leads, and the term “low-frequency” broadly encompasses low-frequency noise of a ECG lead as known in the art including, but not limited to, baseline wander.

[0007] One form of the inventions of the present disclosure is a monitoring device (e.g., a ECG monitor or a Holter monitor) employing an electrocardiograph, and an ECG quality controller. In operation, the electrocardiograph derives an electrocardiogram from one or more ECG leads upon a connection of the electrocardiograph to the ECG lead(s) (e.g., electrode based or pad/paddle based). In response to the connection of the electrocardiograph to the ECG lead(s), the ECG quality controller controls separate evaluations of a high-frequency noise level (e.g., a degree of muscle artifact and/or a degree of electrode motion artifact within the electrocardiogram) and a low-frequency noise level (e.g., a degree of baseline wander by the electrocardiogram) of an individual ECG lead on an electrocardiogram segmentation basis (e.g., a continual ECG recording divided into segments of a specified duration).

[0008] For purposes of the inventions of the present disclosure, terms of the art including, but not limited to, “electrocardiograph”, “electrocardiogram”, “ECG lead”, and “electrocardiogram segmentation” are to be interpreted as understood in the art of the present disclosure and as exemplary described herein.

[0009] For purposes of the inventions of the present disclosure, the term “controller” broadly encompasses all structural configurations of an application specific main board or an application specific integrated circuit housed within or linked to a monitoring device for controlling an application of various inventive principles of the present disclosure as subsequently described herein. The structural configuration of the controller may include, but is not limited to, processor(s), computer-usable/computer readable storage medium(s), an operating system, application module(s), peripheral device controller(s), slot(s) and port(s).

[0010] Examples of the monitoring device include, but are not limited to, diagnostic ECG monitoring devices (e.g., PageWriter TC cardiographs, Efficia series of cardiograph), exercise ECG monitoring devices (e.g., ST80i stress testing system), ambulatory ECG devices (Holter monitor), bedside monitoring ECG devices (e.g., IntelliVue monitors, SureSigns monitors, and Goldway monitors), telemetry ECG monitoring devices (e.g., IntelliVue MX40 monitor); advanced life support products (e.g., HeartStart MRx and HeartStart XL defibrillators, and Efficia DFM100 defibrillator/monitor), and ECG management system (e.g., IntelliSpace ECG management system).

[0011] For purposes of the inventions of the present disclosure, the descriptive labeling “ECG quality” for a controller herein serves to identify the controller as described and claimed herein without specifying or implying any additional limitation to the term “controller”.

[0012] For purposes of the inventions of the present disclosure, the term “application module” broadly encompasses a component of the controller consisting of an electronic circuit and/or an executable program (e.g., executable software and/firmware) for executing a specific application.

[0013] For purposes of the inventions of the present disclosure, any descriptive labeling of an application module herein (e.g., a “activity monitor” module, a “noise estimator” module, a “noise scorer” module, a “noise comparator” module and a “noise indicator” module) serves to identify a particular application module as described and claimed herein without specifying or implying any additional limitation to the term “application module”.

[0014] The foregoing forms and other forms of the present disclosure as well as various features and advantages of the present disclosure will become further apparent from the following detailed description of various embodiments of the present disclosure read in conjunction with the accompanying drawings. The detailed description and drawings are merely illustrative of the present disclosure rather than limiting, the scope of the present disclosure being defined by the appended claims and equivalents thereof.

BRIEF DESCRIPTION OF THE DRAWINGS

[0015] FIGS. 1A and 1B illustrate exemplary embodiments of a monitoring device in accordance with the inventive principles of the present disclosure.

[0016] FIG. 2 illustrates a flowchart representative of an exemplary embodiment of an a monitoring method in accordance with the inventive principles of the present disclosure.

[0017] FIG. 3 illustrates an exemplary ECG segment for ECG leads in accordance with the inventive principles of the present disclosure.

[0018] FIG. 4 illustrates an exemplary activity of an electrocardiogram in accordance with the inventive principles of the present disclosure.

[0019] FIG. 5 illustrates exemplary signal quality zones in accordance with the inventive principles of the present disclosure.

[0020] FIGS. 6-9 illustrates exemplary quality indications of the ECG leads in accordance with the inventive principles of the present disclosure.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0021] To facilitate an understanding of the present disclosure, the following description of FIGS. 1A and 1B, teaches basic inventive principles of evaluating and visually displaying a high-frequency noise level (e.g., muscle artifact and electrode motion artifact) and a low-frequency noise level (e.g., baseline wander) within electrocardiogram segments of each ECG lead in an ECG recording during a continuous ECG monitoring of a patient. From this description, those having ordinary skill in the art will appreciate how to apply the inventive principles of the present disclosure to a variety of monitoring devices displaying segments of a single lead or multi-lead continuous physiological waveform, which is often disturbed by noise or artifact that limits the interpretation and analysis of the physiological waveform, whereby the inventions of the present disclosure provides an evaluation and display of a level of noise from different sources in each individual lead.

[0022] Referring to FIG. 1A, a monitoring device 20 employs an electrocardiograph 40 and an ECG quality controller 50. In practice, ECG quality controller 50 may be segregated or integrated with electrocardiograph 40.

[0023] In operation, a strategic placement of ten (10) electrodes on a body surface of a patient 10 relative to a heart 11 of patient 10 as known in the art for establishing ECG leads 30 listed as follows:

- [0024]** 1. Lead I: electrode RA (−) to electrode LA (+);
- [0025]** 2. Lead II: electrode RA (−) to electrode LL (+);
- [0026]** 3. Lead III: electrode LA (−) to electrode LL (+);
- [0027]** 4. Lead aVR: electrode RA (+) to electrodes LA & LL (−);
- [0028]** 5. Lead aVL: electrode LA (+) to electrodes RA & LL (−);
- [0029]** 6. Lead aVF: electrode LL (+) to electrodes RA & LA (−);
- [0030]** 7. Lead V1: electrode V1;
- [0031]** 8. Lead V2: electrode V2;
- [0032]** 9. Lead V3: electrode V3;
- [0033]** 10. Lead V4: electrode V4;
- [0034]** 11. Lead V5: electrode V5; and
- [0035]** 12. Lead V6: electrode V6.

[0036] ECG leads 30 are connected to electrocardiograph 40 via cables (not shown) attached to the electrodes as known in the art to conduct electrical activity of heart 11 of patient 10 to electrocardiograph 40.

[0037] Electrocardiograph 40 is structurally configured as known in the art to process ECG leads 30 for measuring and recording an electrocardiogram 41 of heart 11 of patient 10. For embodiments of the inventions of the present disclosure incorporating a segregation of electrocardiograph 40 and ECG quality controller 50, electrocardiograph 40 may employ a digital signal processor (not shown) or a central processing unit (not shown) for streaming processed ECG leads 30 to ECG quality controller 50 or ECG quality controller 50 may employ known modules (not shown) for processing ECG leads 30. For embodiments of the inventions of the present disclosure incorporating an integration of electrocardiograph 40 and ECG quality controller 50, electrocardiograph 40 may employ a digital signal processor (not shown) or a central processing unit (not shown) for processing ECG leads 30 on behalf of ECG quality controller 50.

[0038] As shown in FIG. 1A, ECG quality controller 50 executes a high frequency noise evaluation 51a and a low frequency noise evaluation 51b of processed ECG leads 30 for respectively estimating a high-frequency noise level and a low-frequency noise level of each individual ECG lead 30 on an electrocardiogram segmentation basis.

[0039] For high frequency noise evaluation 51a, ECG quality controller 50 analyzes each segment of electrocardiogram 41 in sequence to estimate a high-frequency noise level of each individual ECG lead 30 including, but not limited to, any muscle artifact and any electrode motion artifact. In practice, ECG quality controller 50 may analyze any parameter(s) of each segment of electrocardiogram 41 suitable for estimating the high-frequency noise level of each individual ECG lead 30. In one embodiment, ECG quality controller 50 analyzes a standard deviation of a short segment of electrocardiogram 41 outside of the areas of high amplitude signal from the heart for estimating the high-frequency noise level of each individual ECG lead 30 as will be further described herein and illustrated in FIG. 2. Alternative embodiments for measuring high frequency noise in a narrow (in time) window include, but are not limited to, (1) a high pass filter followed by a root mean square (RMS) calculation in a sliding window, (2) a first difference or

derivative estimator followed by a RMS calculation in a sliding window, and (3) high frequency filters from a filter bank with a smoothed envelope calculation at the output of each filter.

[0040] For low frequency noise evaluation 51b, ECG quality controller 50 analyzes each segment of electrocardiogram 41 in sequence to estimate a low-frequency noise level of each individual ECG lead 30 including, but not limited to, any baseline wander of each segment of electrocardiogram 41. In practice, ECG quality controller 50 may analyze any parameter(s) of each segment of electrocardiogram 41 suitable for estimating the low-frequency noise level of each individual ECG lead 30. In one embodiment, ECG quality controller 50 analyzes each change in baseline levels of each segment of electrocardiogram 41 for estimating the low-frequency noise level of each individual ECG lead 30 as will be further described herein and illustrated in FIG. 2.

[0041] For both high/low-frequency noise evaluations 51a and 51b, ECG quality controller 50 controls a display of a signal quality indication for each segment of the electrocardiogram 41 resulting from the respective noise level analysis of each segment of electrocardiogram 41. In practice, ECG quality controller 50 may generate signal quality indication in any form suitable for communicating the resulting from the respective noise level analysis of each segment of electrocardiogram 41. In one embodiment, ECG quality controller 50 generates a textual signal quality indicator, a graphical signal quality indicator and/or a color-coded signal quality indicator for concurrent segment display with each ECG lead 30 as will be further described herein and illustrated in FIG. 2.

[0042] The result of high/low-frequency noise evaluations 51a and 51b is a capability of monitoring device 20 and/or an operator thereof to discard any low-quality segments of cardiogram 41 and to maintain good-quality leads among ECG leads 30 while resolving corrupted leads among ECG leads 30 as indicated by the evaluation of the high/low frequency signal quality of each individual ECG lead 30. This is beneficial to an accurate interpretation and diagnosis of electrocardiogram 41.

[0043] Referring to FIG. 1B, a defibrillator 21 employing electrocardiograph 40 and ECG quality controller 50 of monitoring device 20 as described in FIG. 1A, and further employs a shock source 60 structurally configured as known in the art to store electric energy for delivery of a defibrillation shock 61 via electrode pads 31 to heart 11 of patient 10. In practice, defibrillation shock 61 may have any waveform as known in the art. Examples of such waveforms include, but are not limited to, a monophasic sinusoidal waveform (positive sine wave) 61a and a biphasic truncated waveform 61b as shown in FIG. 1B.

[0044] In one embodiment, shock source 60 employs a high voltage capacitor bank (not shown) for storing a high voltage via a high voltage charger and a power supply upon a pressing of a charge button. Shock source 60 further employs a switching/isolation circuit (not shown) for selectively applying a specific waveform of an electric energy charge from the high voltage capacitor bank to electrode pads 31.

[0045] Electrode pads 31 are structurally configured as known in the art to be conductively applied to a patient 10 in an anterior-apex arrangement as shown in FIG. 1 or in an anterior-posterior arrangement (not shown). Electrode pads

31 conduct a defibrillation shock from shock source 60 to a heart 11 of patient 10, and are connected to electrocardiograph 40 via cables (not shown) attached to the electrodes as known in the art to conduct electrical activity of heart 11 of patient 10 to electrocardiograph 40 and for establishing ECG leads 32.

[0046] Electrocardiograph 40 is structurally configured as known in the art to process ECG leads 32 for measuring and recording an electrocardiogram 42 of heart 11 of patient 10. For embodiments of the inventions of the present disclosure incorporating a segregation of electrocardiograph 40 and ECG quality controller 50, electrocardiograph 40 may employ a digital signal processor (not shown) for streaming processed ECG leads 32 to ECG quality controller 50 or ECG quality controller 50 may employ known modules (not shown) for processing ECG leads 32. For embodiments of the inventions of the present disclosure incorporating an integration of electrocardiograph 40 and ECG quality controller 50, electrocardiograph 40 may employ a digital signal processor (not shown) or a central processing unit (not shown) for processing ECG leads 32 on behalf of ECG quality controller 50.

[0047] As shown in FIG. 1B, ECG quality controller 50 executes high frequency noise evaluation 51a and low frequency noise evaluation 51b as previously described herein of processed ECG leads 32 for respectively estimating a high-frequency noise level and a low-frequency noise level of each individual ECG lead 32 on an electrocardiogram segmentation basis. This is beneficial to an accurate interpretation and diagnosis of electrocardiogram 42.

[0048] To facilitate a further understanding high frequency noise evaluation 51a and low frequency noise evaluation 51b in accordance with the inventive principles of the present disclosure, a flowchart 70 representative of a monitoring method of the present disclosure as executed by application modules 51-55b of an exemplary embodiment of ECG quality controller 50 as illustrated in FIG. 2 will now be described herein in the context of ECG leads 30 and electrocardiogram 40 as illustrated in FIG. 1A. From this description, those having ordinary skill in the art will appreciate how to apply the inventive principles of the present disclosure in structurally configuring various embodiments of ECG quality controller 50 in implementing various embodiments of high frequency noise evaluation 51a and low frequency noise evaluation 51b.

[0049] Referring to FIG. 2, a stage S72 of flowchart 70 encompasses an activity monitor 51 of ECG quality controller 50 computing an activity level of electrocardiogram 40. In practice, activity monitor 51 divides electrocardiogram 40 into segments of a specified duration and for each segment, computes an activity function for each sample across all ECG leads 30.

[0050] In one embodiment, activity monitor divides electrocardiogram 50 into ten (10) second ECG recording segments, which are further divided into one (1) second sections, and computes the activity function at each sample across all leads in an ECG segment is in accordance with the following equation [1]:

$$A_i = \sum_{leads} (ECG_i - ECG_{i-1})^2 \quad [1]$$

[0051] where A_i is the activity function,

[0052] where ECG_i is the current ECG sample, and

[0053] wherein ECG_{i-1} is the previous ECG sample.

[0054] Activity monitor 51 low-pass filters the computed values activity function A_1 and detects local minima of activity function A_1 by a short-term sliding window for each section of the ECG segment.

[0055] For example, FIG. 3 illustrates a ten (10) second ECG segment 90 of ECG leads 30 subdivided into ten (10) one (1)-second sections, and FIG. 4 illustrates monitor 51 activity function 91 computed by activity monitor 41 within one of the sections of the ECG segment whereby activity monitor 41 detects a local minimum activity function A_1 as represented by the X for evaluating the high-frequency noise level and the low-frequency noise level for each ECG lead 30 in a small window around the local minimum.

[0056] Referring back to FIG. 2, a stage S74 of flowchart 70 encompasses a high-frequency noise (“HFN”) estimator 52a of ECG quality controller 50 for estimating a high-frequency noise level for a current ECG segment of each individual ECG lead 30. In practice, HFN estimator 52a computes a standard deviation of a short segment for each individual ECG lead 30 per segment section as an estimation of high-frequency noise level for each individual ECG lead 30. In one embodiment, HFN estimator 52a computes a standard deviation of each individual ECG lead 30 per segment section is in accordance with the following equation [2]:

$$\sigma_{lead} = \sqrt{E[(ECG_{lead} - E[ECG_{lead}])^2]} \quad [2]$$

[0057] where σ_{lead} is the standard deviation of a particular ECG lead 30,

[0058] where ECG_{lead} is the particular ECG lead 30, and

[0059] wherein E is an average or expected value of standard deviation σ_{lead} of the particular ECG lead 30.

[0060] Upon computation of standard deviation σ_{lead} for each ECG lead 30 over each segment section, a stage S76 of flowchart 70 encompasses a HFN scorer 53a computing a HFN score for individual ECG lead 30 for the current ECG segment. In practice, HFN scorer 53a computes the HFN score of each individual ECG lead 30 per segment section as a function of the estimated high-frequency noise levels per segment section during stage S74. In one embodiment, HFN scorer 53a computes a HFN score in accordance with the following equation [3]:

$$NS_{HF} = \alpha \cdot \text{median}(\sigma_{lead}) \quad [3]$$

[0061] where NS_{HF} is the HFN score for a particularly ECG lead 30,

[0062] where $\text{median}(\sigma_{lead})$ is the median of the computed standard deviations across sections of a time segment of a particular ECG lead 30, and

[0063] where α is a weighting factor for all ECG leads 30.

[0064] Still referring to FIG. 2, stage S74 of flowchart 70 further encompasses a low-frequency noise (“LFN”) estimator 52b of ECG quality controller 50 for estimating a low-frequency noise level for a current ECG segment of each individual ECG lead 30. In practice, LFN estimator 52b computes a baseline wander per segment section as an estimation of low-frequency noise level for each individual ECG lead 30. In one embodiment, LFN estimator 52b computes baseline wander of each individual ECG lead 30

per segment section around the local minimum of activity function A_1 , is in accordance with the following equations [4a] and [4b]:

$$BW_{1,lead} = \begin{cases} \text{Baseline}[ECG_k] - \text{Baseline}[ECG_{k-1}], k \neq 1 \\ 0, \text{ otherwise} \end{cases} \quad [4a]$$

$$BW_{2,lead} = \begin{cases} \text{Baseline}[ECG_k] - \text{Baseline}[ECG_{k-2}], k \neq 1, 2 \\ 0, \text{ otherwise} \end{cases} \quad [4b]$$

[0065] where $BW_{1,lead}$ is the baseline wander of a current segment for a particular ECG lead 30,

[0066] where $BW_{2,lead}$ is the baseline wander of the previous segment for a particular ECG lead 30,

[0067] where ECG_k is the current section of the current ECG segment (e.g., current 1-second section of the current 10-sec ECG segment), and

[0068] wherein ECG_{k-1} is previous section of the current ECG segment (e.g., previous 1-second section of the current 10-sec ECG segment).

[0069] Note equations [4a] and [4b] facilitate a location of a time point which does not include high amplitude physiological signal from the heart, but rather a quiet time for the heart, in absence of physiological signal.

[0070] Upon computation of the baseline wander for each ECG lead 30 over segments, stage S76 of flowchart 70 further encompasses a LFN scorer 53b computing a LFN score for each individual ECG lead 30 for the current ECG segment. In practice, LFN scorer 53b computes the LFN score of each individual ECG lead 30 per segment section as a function of the baseline wander computed during stage S74. In one embodiment, LFN scorer 53b computes a LFN score in accordance with the following equation [5]:

$$NS_{BW} = \beta \cdot (|BW_{1,lead}| + |BW_{2,lead}|) \quad [5]$$

[0071] where NS_{BW} is the LFN score for a particularly ECG lead 30; and

[0072] where β is a weighting factor for all ECG leads 30.

[0073] Still referring to FIG. 2, a stage S78 of flowchart 70 encompasses a HFN comparator 54a of ECG quality controller 50 comparing the HFN score of each individual ECG lead 30 to one or more signal quality threshold differentiating two or more signal quality zones, and a LFN comparator 54b of ECG quality controller 50 comparing the LFE score of each individual ECG lead 30 to the signal quality threshold(s) differentiating the signal quality zones. In practice, the signal quality threshold(s) may be selected in a validation process by training an algorithm with an annotated database and optimizing the performance of noise quality classification for classifying the HFN score in multiple signal quality zones. In one embodiment, two (2) thresholds were utilized to classify the HFN score of each individual ECG lead 30 into one of three (3) categories including (1) good, (2) fair, and (3) poor quality.

[0074] For example, the signal quality zones are summarized in the following TABLE 1 and illustrated in FIG. 5:

TABLE 1

Signal quality zones	Criteria
Good	$0 \leq \text{Noise Score} \leq \text{Threshold1}$
Fair	$\text{Threshold1} < \text{Noise Score} \leq \text{Threshold2}$
Poor	$\text{Threshold2} < \text{Noise Score} \leq \text{UpperLimit}$

[0075] Referring back to FIG. 2, upon determining the HFN and LFN signal quality of each ECG lead for the current ECG segment, a stage S80 of flowchart 70 encompasses a HFN indicator 55a and a LFN indicator 55b generating a signal quality indication representative of the signal quality. In practice, the signal quality indication may take any form suitable for communicating the HFN and LFN signal quality for each ECG lead including a textual signal quality indication and/or a graphical signal quality indication, both of which may be coded by various features including color and font. Additionally in practice, the signal quality indication may be displayed with the current ECG segment of ECG leads 30 or the representative beat of the current ECG segment.

[0076] In one embodiment, HFN indicator 55a and LFN indicator 55b normalize the respective HFN score and the LFN score to integers in on a scale of 0 to 10 where Threshold1 and Threshold2 are located at 2.5 and 4.5, respectively, and the UpperLimit is 10 as shown in in accordance with the following TABLE 2:

TABLE 2

Signal quality zones	Criteria
Good	Noise Score = 0, 1, 2
Fair	Noise Score = 3, 4
Poor	Noise Score = 5, 6, 7, 8, 9, 10

[0077] From the normalization, a color-coded textual indicator for each ECG lead may be concurrently displayed with the ECG leads 30 as exemplary shown in FIGS. 6, 7 and 9. Alternatively, the ECG lead waveforms may be color-coded as exemplary shown in FIG. 10.

[0078] Referring to FIG. 6, the following exemplary HFN scores and LFN scores are exemplary displayed adjacent an exemplary current ECG segment of ECG leads 30:

[0079] 1. Good HFN/LFN Scores (green color-coded): Leads V3 and V4;

[0080] 2. Fair HFN/LFN Scores (yellow color-coded): Leads aVL, V1, V2, V5 and V6; and

[0081] 3. Poor HFN/LFN Scores (red color-coded): Leads I, II, III, aVR and aVF.

[0082] Referring to FIG. 7, the following exemplary HFN scores and LFN scores are exemplary displayed in boxes above an exemplary current ECG segment of ECG leads 30:

[0083] 1. Good HFN/LFN Scores (green color-coded): Leads V3 and V4;

[0084] 2. Fair HFN/LFN Scores (yellow color-coded): Leads aVL, V1, V2, V5 and V6; and

[0085] 3. Poor HFN/LFN Scores (red color-coded): Leads I, II, III, aVR and aVF.

[0086] Referring to FIG. 8, the following exemplary HFN scores and LFN scores are exemplary displayed adjacent an exemplary color-coded current ECG segment of ECG leads 30:

[0087] 1. Good HFN/LFN Scores (green wave): Leads V3 and V4;

[0088] 2. Fair HFN/LFN Scores (yellow wave): Leads aVL, V1, V2, V5 and V6; and

[0089] 3. Poor HFN/LFN Scores (red wave): Leads I, II, III, aVR and aVF.

[0090] Referring to FIG. 9, the following exemplary HFN scores and LFN scores are exemplary displayed in boxes above an exemplary representative beats of the current ECG segment of ECG leads 30:

[0091] 1. Good HFN/LFN Scores (green color-coded): Leads V3 and V4;

[0092] 2. Fair HFN/LFN Scores (yellow color-coded): Leads aVL, V1, V2, V5 and V6; and

[0093] 3. Poor HFN/LFN Scores (red color-coded): Leads I, II, III, aVR and aVF.

[0094] Referring back to FIG. 2, note one of the purposes of stage S76 as described herein is to equalize the HFN score and the LFN score for comparisons to the same signal quality thresholds of stage S78. Nonetheless, in practice, the HFN score and the LFN score do not have to be equalized and may be compared to different signal quality thresholds.

[0095] Upon completion of stage S80, flowchart 70 returns to stage S72 to repeat stages S72-S80 for the next ECG segment and continues to loop until terminated.

[0096] Referring to FIGS. 1-9, those having ordinary skill in the art will appreciate numerous benefits of the present disclosure including, but not limited to, the novel and unique evaluating and visually displaying high-frequency and low-frequency noise levels within electrocardiogram segments of each ECG lead in an ECG recording during a continuous ECG monitoring of a patient. More generally, the inventions of the present disclosure applies to a variety of monitoring devices for displaying segments of a single lead or multi-lead continuous physiological waveform, which are often disturbed by noise or artifact that limits the interpretation and analysis of the physiological waveform, whereby the inventions of the present disclosure provides an evaluation and display of a level of noise from different sources in each individual lead. The benefits being a capability of the monitoring device and/or an operator thereof to discard any low-quality segments of the physiological waveform and to maintain good-quality leads while resolving corrupted leads as indicated by the evaluation of the high/low frequency signal quality of each individual lead.

[0097] Furthermore, as one having ordinary skill in the art will appreciate in view of the teachings provided herein, features, elements, components, etc. described in the present disclosure/specification and/or depicted in the FIGS. 1-9 may be implemented in various combinations of electronic components/circuitry, hardware, executable software and executable firmware and provide functions which may be combined in a single element or multiple elements. For example, the functions of the various features, elements, components, etc. shown/illustrated/depicted in the FIGS. 1-9 can be provided through the use of dedicated hardware as well as hardware capable of executing software in association with appropriate software. When provided by a processor, the functions can be provided by a single dedicated processor, by a single shared processor, or by a plurality of individual processors, some of which can be shared and/or multiplexed. Moreover, explicit use of the term "processor" should not be construed to refer exclusively to hardware capable of executing software, and can implicitly include, without limitation, digital signal processor ("DSP") hardware or other central processing unit ("CPU") memory (e.g., read only memory ("ROM") for storing software, random access memory ("RAM"), non-volatile storage, etc.) and virtually any means and/or machine (including hardware,

software, firmware, circuitry, combinations thereof, etc.) which is capable of (and/or configurable) to perform and/or control a process.

[0098] Moreover, all statements herein reciting principles, aspects, and embodiments of the invention, as well as specific examples thereof, are intended to encompass both structural and functional equivalents thereof. Additionally, it is intended that such equivalents include both currently known equivalents as well as equivalents developed in the future (e.g., any elements developed that can perform the same or substantially similar function, regardless of structure). Thus, for example, it will be appreciated by one having ordinary skill in the art in view of the teachings provided herein that any block diagrams presented herein can represent conceptual views of illustrative system components and/or circuitry embodying the principles of the invention. Similarly, one having ordinary skill in the art should appreciate in view of the teachings provided herein that any flow charts, flow diagrams and the like can represent various processes which can be substantially represented in computer readable storage media and so executed by a computer, processor or other device with processing capabilities, whether or not such computer or processor is explicitly shown.

[0099] Furthermore, exemplary embodiments of the present disclosure can take the form of a computer program product or application module accessible from a computer-usable and/or computer-readable storage medium providing program code and/or instructions for use by or in connection with, e.g., a computer or any instruction execution system. In accordance with the present disclosure, a computer-usable or computer readable storage medium can be any apparatus that can, e.g., include, store, communicate, propagate or transport the program for use by or in connection with the instruction execution system, apparatus or device. Such exemplary medium can be, e.g., an electronic, magnetic, optical, electromagnetic, infrared or semiconductor system (or apparatus or device) or a propagation medium. Examples of a computer-readable medium include, e.g., a semiconductor or solid state memory, magnetic tape, a removable computer diskette, a random access memory (RAM), a read-only memory (ROM), flash (drive), a rigid magnetic disk and an optical disk. Current examples of optical disks include compact disk read only memory (CD-ROM), compact disk read/write (CD-R/W) and DVD. Further, it should be understood that any new computer-readable medium which may hereafter be developed should also be considered as computer-readable medium as may be used or referred to in accordance with exemplary embodiments of the present disclosure and disclosure.

[0100] Having described preferred and exemplary embodiments of novel and inventive high/low-frequency noise evaluations of an electrical connection to an electrocardiograph, it is noted that modifications and variations can be made by persons having ordinary skill in the art in light of the teachings provided herein, including the FIGS. 1-9. It is therefore to be understood that changes can be made in/to the preferred and exemplary embodiments of the present disclosure which are within the scope of the embodiments disclosed herein.

[0101] Moreover, it is contemplated that corresponding and/or related systems incorporating and/or implementing the device or such as may be used/implemented in a device in accordance with the present disclosure are also contemplated and considered to be within the scope of the present disclosure.

Further, corresponding and/or related method for manufacturing and/or using a device and/or system in accordance with the present disclosure are also contemplated and considered to be within the scope of the present disclosure.

1. A monitoring device, comprising:

an electrocardiograph structurally configured to derive an electrocardiogram from at least one of ECG lead responsive to a connection of the electrocardiograph to the at least one ECG lead; and

an ECG quality controller structurally configured, in response to the connection of the electrocardiograph to the at least one ECG lead, to control separate evaluations of a high-frequency noise level and a low-frequency noise level of an individual ECG lead on an electrocardiogram segmentation basis.

2. The monitoring device of claim 1, wherein the high-frequency noise level evaluation of the individual ECG lead by the ECG quality controller further includes:

the ECG quality controller being structurally configured to ascertain the standard deviation of the individual ECG lead relative to a specified activity level of each electrocardiogram segment.

3. The monitoring device of claim 2, wherein a high-frequency noise level evaluation of the individual ECG lead by the ECG quality controller includes:

the ECG quality controller being structurally configured, during each electrocardiogram segment, to derive a high-frequency noise score for the individual ECG lead from a standard deviation of the individual ECG lead.

4. The monitoring device of claim 2, wherein a high-frequency noise level evaluation of the individual ECG lead by the ECG quality controller further includes:

the ECG quality controller being structurally configured, during each electrocardiogram segment, to scale the standard deviation of the individual ECG lead by a weighting factor to thereby derive the high-frequency noise score for the individual ECG lead.

5. The monitoring device of claim 2, wherein the high-frequency noise level evaluation of the individual ECG lead by the ECG quality controller further includes:

the ECG quality controller structurally configured, during each electrocardiogram segment, to compare the high-frequency noise score to at least one signal quality threshold differentiating a plurality of distinct signal quality zones.

6. The monitoring device of claim 1, wherein a low-frequency noise level evaluation of the individual ECG lead by the ECG quality controller includes:

the ECG quality controller being structurally configured, during each electrocardiogram segment, to derive a low-frequency noise score for the individual ECG lead from any change in a baseline level of the individual ECG lead.

7. The monitoring device of claim 6, wherein the low-frequency noise level evaluation of the individual ECG lead by the ECG quality controller further includes:

the ECG quality controller being structurally configured, during each electrocardiogram segment, to scale any change in the baseline level of the individual ECG lead by a weighting factor to thereby derive the low-frequency noise score of the individual ECG lead.

8. The monitoring device of claim 6, wherein the low-frequency noise level evaluation of the individual ECG lead by the ECG quality controller further includes:

the ECG quality controller being structurally configured to ascertain any change in the baseline level of the individual ECG lead measured at a specified activity level of each electrocardiogram segment.

9. The monitoring device of claim 6, wherein the low-frequency noise level evaluation of the individual ECG lead by the ECG quality controller further includes:

the ECG quality controller being structurally configured to compare the low-frequency noise score to at least one signal quality threshold differentiating a plurality of distinct signal quality zones.

10. The monitoring device of claim 1, wherein the ECG quality controller is further structurally configured to control a display of the separate evaluations of the high-frequency noise level and the low-frequency noise level of the individual ECG lead on an electrocardiogram segmentation basis.

11. The monitoring device of claim 10, wherein a display of the high-frequency noise level evaluation and the low-frequency noise level evaluation of the individual ECG lead by the ECG quality controller includes:

the ECG quality controller being structurally configured to display one of the individual ECG lead or a representative beat of the ECG lead concurrently with a signal quality indication of the separate evaluations of the high-frequency noise level and the low-frequency noise level of the individual ECG lead,

wherein each signal quality indication is at least one of a textual signal quality indication and a graphical signal quality indication.

12. The monitoring device of claim 1, wherein the ECG quality controller includes:

a high-frequency noise estimator structurally configured to compute a median of standard deviations of the individual ECG lead measured at specified activity levels during an electrocardiogram segment; and

a high-frequency noise scorer structurally configured, derive a high-frequency noise score from a computation by the high-frequency noise estimator of the median of standard deviations for the individual ECG lead; and

a high-frequency noise comparator structurally configured to compare a computation by the high-frequency noise scorer of the high-frequency noise score to at least one signal quality threshold differentiating a plurality of distinct signal quality zones.

13. The monitoring device of claim 12, wherein the ECG quality controller further includes:

a high-frequency noise indicator structurally configured to derive a signal quality indication of the evaluation of the high-frequency noise level of the individual ECG lead from a comparison by the high-frequency noise comparator of the high-frequency noise score to at least one signal quality threshold,

wherein each signal quality indication is at least one of a textual signal quality indication and a graphical signal quality indication.

14. The monitoring device of claim 1, wherein the ECG quality controller includes:

a low-frequency noise estimator structurally configured to compute any change in a baseline level of the indi-

vidual ECG lead at specified activity level within an electrocardiogram segment; and

a low-frequency noise scorer structurally configured to derive a low-frequency noise score from a computation by the low-frequency noise estimator of any change in the baseline level of the individual ECG lead; and

a low-frequency noise comparator structurally configured to compare a computation by the low-frequency noise scorer of the low-frequency noise score to at least one signal quality threshold differentiating a plurality of distinct signal quality zones.

15. The monitoring device of claim 14, wherein the ECG quality controller further includes:

a low-frequency noise indicator structurally configured to derive a signal quality indication of the evaluation of the low-frequency noise level of the individual ECG lead from a comparison by the low-frequency noise comparator of the low-frequency noise score to at least one signal quality threshold,

wherein each signal quality indication is at least one of a textual signal quality indication and a graphical signal quality indication.

16. A monitoring method, comprising

an electrocardiograph deriving an electrocardiogram from at least one ECG lead responsive to a connection of the electrocardiograph to the at least one ECG lead; and

an ECG quality controller controlling separate evaluations of a high-frequency noise level and a low-frequency noise level of an individual ECG lead on an electrocardiogram segmentation basis.

17. The monitoring method of claim 16, wherein the high-frequency noise level evaluation of the individual ECG lead by the ECG quality controller includes:

during each electrocardiogram segment, the ECG quality controller deriving a high-frequency noise score for the individual ECG lead from a standard deviation of the individual ECG lead; and

the ECG quality controller comparing each high-frequency noise score to at least one signal quality threshold differentiating a plurality of distinct signal quality zones.

18. The monitoring method of claim 16, wherein the low-frequency noise level evaluation of the individual ECG lead by the ECG quality controller includes:

during each electrocardiogram segment, the ECG quality controller deriving a low-frequency noise score for the individual ECG lead from any change in a baseline level of the individual ECG lead; and

the ECG quality controller comparing each low-frequency noise score to at least one signal quality threshold differentiating a plurality of distinct signal quality zones.

19. The monitoring method of claim 16, further comprising:

the ECG quality controller controlling a display of the separate evaluations of the high-frequency noise level and the low-frequency noise level of the individual ECG lead on the electrocardiogram segmentation basis.

20. The monitoring method of claim 19, wherein the display of the high-frequency noise level evaluation and the low-frequency noise level evaluation of the individual ECG lead by the ECG quality controller includes:

the ECG quality controller displaying the individual one of the individual ECG lead or a representative beat of

the ECG lead concurrently with a signal quality indication of the separate evaluations of the high-frequency noise level and the low-frequency noise level of the individual ECG lead,

wherein each signal quality indication is at least one of a textual signal quality indication and a graphical signal quality indication.

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专利名称(译)	ECG导联信号的高/低频信号质量评估		
公开(公告)号	US20180242872A1	公开(公告)日	2018-08-30
申请号	US15/754675	申请日	2016-08-25
[标]申请(专利权)人(译)	皇家飞利浦电子股份有限公司		
申请(专利权)人(译)	皇家飞利浦N.V.		
当前申请(专利权)人(译)	皇家飞利浦N.V.		
[标]发明人	FIROOZABADI REZA GREGG RICHARD EARL		
发明人	FIROOZABADI, REZA GREGG, RICHARD EARL		
IPC分类号	A61B5/04 A61B5/044 A61B5/00		
CPC分类号	A61B5/04014 A61B5/044 A61B5/7203 A61B5/7221 A61B5/0402		
优先权	62/209394 2015-08-25 US		
外部链接	USPTO		

摘要(译)

使用心电图仪 (40) 和ECG质量控制器 (50) 的监测设备 (例如 , ECG监测器或Holter监测器) 。在操作中 , 心电图仪 (40) 在连接心电图仪时 (40) 从一个或多个ECG导联 (30) 导出心电图一个或多个ECG导线 30 (例如 , 基于电极或基于垫/浆) 。响应心电图仪 (40) 与心电图导联 (30) 的连接 , 心电图质量控制器 (50) 控制高频噪声水平的单独评估 (例如 , 肌肉伪影的程度和/或心电图内的电极运动伪影的程度) 和低频噪声水平 (例如 , 心电图的基线漂移程度) 在心电图分割的基础上 (例如 , 连续的ECG记录被分成具有特定持续时间的段) 的个体ECG导联 (30) 。

