

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
10 November 2005 (10.11.2005)

PCT

(10) International Publication Number
WO 2005/107351 A1

(51) International Patent Classification⁷: H05K 5/00,
A61B 5/07

Goucher Terrace, Gaithersburg, Maryland 20877 (US).
WALTERS, Steven J. [US/US]; 2990 Pebble Beach
Drive, Ellicott City, Maryland 21042 (US).

(21) International Application Number:
PCT/US2005/011653

(74) Agents: HYNDS, Joseph A. et al.; 1425 K Street, NW,
Suite 800, Rothwell, Figg, Ernst & Manbeck, Washington,
District of Columbia 20005 (US).

(22) International Filing Date: 7 April 2005 (07.04.2005)

(25) Filing Language: English

(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,
AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA,
MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM,
PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY,
TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU,
ZA, ZM, ZW.

(26) Publication Language: English

(30) Priority Data:
10/825,648 16 April 2004 (16.04.2004) US

(71) Applicant (for all designated States except US): SEN-
SORS FOR MEDICINE AND SCIENCE, INC.
[US/US]; 12321 Middlebrook Road, Germantown, Mary-
land 20874 (US).

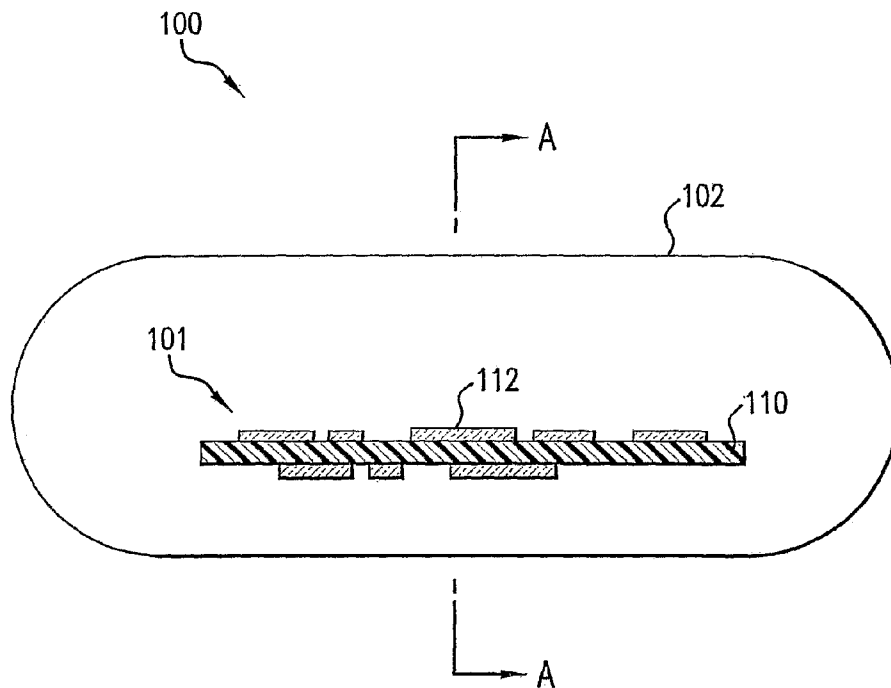
(72) Inventors; and

(84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,
FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO,

(75) Inventors/Applicants (for US only): COLVIN, JR,
Arthur E. [US/US]; 4155 Baltimore National Pike,
Mt. Airy, Maryland 21771 (US). LORENZ, Carrie R.
[US/US]; 15685 Old Frederick Road, Woodbine, Mary-
land 21797 (US). O'CONNOR, Casey J. [US/US]; 105

[Continued on next page]

(54) Title: A HOUSING FOR A CIRCUIT THAT IS TO BE IMPLANTED IN-VIVO AND PROCESS OF MAKING THE SAME



(57) Abstract: The present invention provides a biocompatible circuit assembly that includes a circuit encased within a housing. In some embodiments, the housing is a PMMA housing and before the circuit is enclosed within the housing the circuit is encased within a brick of epoxy.

WO 2005/107351 A1



SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Published:

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

A HOUSING FOR A CIRCUIT THAT IS TO BE IMPLANTED IN-VIVO AND PROCESS OF MAKING THE SAME

BACKGROUND OF THE INVENTION

1. Field of the invention

[001] The present invention relates to a circuit housing, and, more specifically, to a housing for a circuit designed to be implanted in-vivo (i.e., an implantable circuit).

2. Discussion of the Background

[002] There are several applications that require a circuit to be protected from the environment in which the circuit is intended to operate. For example, a human-implantable glucose sensor circuit must be housed within a suitable housing to both protect the sensor from the human body and to protect the human body from the sensor. U.S. Pat. No. 6,330,464, the disclosure of which is incorporated herein by this reference, discloses such a sensor.

[003] A housing encasing an implantable circuit should have at least some of the following characteristics: (1) the ability to protect the electronic circuitry of the sensor from the ambient in-vivo chemical and physical environment, (2) the ability to protect tissue adjacent to the sensor from any adverse reaction which could result as a consequence of contact (or leachables) from within the circuitry - in addition, beyond the adjacent tissue,

the encasement must not permit leachables of any detectable significance into the general body environment; (3) the ability to permit wireless electronic communication between the circuitry and an external reader for power and signal; (4) the ability to permit free passage of wavelengths of light necessary for optical functioning of the sensor; (5) the ability to support the surface chemistry required to form a chemical recognition "front-end"; (6) the housing should be high volume manufacture-able; (7) the housing must be non-toxic and "biocompatible"; and (8) provide a sufficiently high reliability to meet the specifications of a medical product.

SUMMARY OF THE INVENTION

[004] The present invention provides a housing that meets many of the criteria outlined above. In one aspect, the present invention provides a circuit encased within a completely enclosed polymer housing. Preferably, the housing is made of an organic polymer, such as PMMA. In some embodiments, the circuit is first enclosed within a glass housing which itself is then enclosed within a second housing, such as a housing made from an organic polymer. In other embodiments, the circuit is first encased within a brick of epoxy and then the epoxy brick containing the circuit is enclosed within a housing.

[005] In another aspect, the present invention provides a method for enclosing a circuit in a polymer housing. In one embodiment, the method may include

the following steps: (a) placing the circuit in a mold; (b) pouring a formulation into the mold so that the formulation completely surrounds the circuit, wherein the formulation comprises monomers; and (c) polymerizing the monomers. In step (b), all of the formulation need not be poured at once. For example, in some embodiments, the formulation is poured into the mold until the mold is half full and then after a delay additional formulation is poured into the mold. In some embodiments, the monomers may be MMA monomers. The formulation may further comprise pre-polymerized PMMA.

[006] In another embodiment, the method may include the following steps: inserting the circuit into a polymer housing; injecting an optical epoxy into the polymer housing to fill the spaces between the circuit and the inside walls of the housing (in some embodiments the injection is from the bottom up to force out trapped air); capping an open end of the housing; placing the housing containing the optical epoxy and the circuit into a pressure vessel and increasing the pressure and temperature within the vessel; allowing the optical epoxy to cure; and removing the housing from the pressure vessel.

[007] In another embodiment, the method may include the following steps: inserting the circuit into a glass housing; injecting an optical epoxy into the glass housing to fill the spaces between the circuit and the inside walls of the housing; injecting

an optical epoxy into a polymer housing; inserting into the polymer housing the glass housing containing the circuit; capping an open end of the glass housing; and capping an open end of the polymer housing.

[0008] The above and other features and advantages of the present invention, as well as the structure and operation of preferred embodiments of the present invention, are described in detail below with reference to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0009] The accompanying drawings, which are incorporated herein and form part of the specification, help illustrate various embodiments of the present invention and, together with the description, further serve to explain the principles of the invention and to enable a person skilled in the pertinent art to make and use the invention. In the drawings, like reference numbers indicate identical or functionally similar elements. Additionally, the left-most digit(s) of a reference number identifies the drawing in which the reference number first appears.

[0010] FIG. 1 illustrates one embodiment of a circuit assembly according to the present invention.

[0011] FIG. 2 is a flow chart illustrating a process, according to one embodiment, for encasing a circuit within a polymer housing.

[0012] FIG. 3 is a cross sectional view of a circuit assembly according to an embodiment of the invention.

[0013] FIG. 4 is a flow chart illustrating a process, according to another embodiment, for encasing a circuit within a polymer housing.

[0014] FIG. 5 is an exploded view of a circuit assembly according to an embodiment of the invention.

[0015] FIG. 6 is a cross sectional view of a circuit assembly according to another embodiment of the invention.

[0016] FIG. 7 illustrates a circuit assembly according to another embodiment of the present invention.

[0017] FIG. 8 is an exploded view of a circuit assembly according to another embodiment of the invention.

[0018] FIG. 9 is a flow chart illustrating a process, according to another embodiment, for encasing a circuit within a polymer housing.

[0019] FIGS. 10A and 10B illustrate a circuit covered with different amount of epoxy.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0020] FIG. 1 illustrates one embodiment of a circuit assembly 100 according to the present invention. As shown in FIG. 1, the present invention provides an assemblage including a circuit 101 housed

within a fully enclosed housing 102. Preferably, as shown in FIG. 1, housing 102 is capsule shaped, but other shapes may be used. Circuit 101 may be an electronic circuit having a printed circuit board 110 and one or more electrical and optical components 112 attached to the circuit board 110. Circuit 101 may include a conventional sensor, such as the sensor described in U.S. Patent No. 6,304,766. The housing 102 may be a housing made from PMMA, which is a polymer of methyl methacrylate (MMA) monomers, or from other organic polymers.

[0021] FIG. 2 is a flow chart illustrating a process 200, according to one embodiment, for creating circuit assembly 100. Process 200 may begin in step 202, where a polymerization initiator is added to a mold. In step 204, an encasement formulation containing monomers is poured into the mold (e.g., filling the mold halfway). In step 206, circuit 101 is placed in the mold. In step 208, more of the encasement formulation is poured into the mold so that the circuit is completely immersed in the encasement formulation. In one embodiment, the encasement formulation includes monomers. In one embodiment, the encasement formulation consists of or essentially consists of MMA monomers. In this manner, one can encase circuit 101 in a polymer housing.

[0022] In some situations, for example, situations where the formulation includes MMA monomers and circuit 101 is relatively large, circuit 101 can

become severely damaged during the polymerization process (i.e., during step 206). The cause of this damage is usually attributed to the shrinkage that occurs naturally during polymerization of MMA. In the joining of bonds between monomers contained within a neat solution of MMA, the intermolecular spacing is reduced within a polymer as the reaction progresses. This is a well-known phenomena and typical of most, if not all, polymer reactions. The net volumetric shrinkage that occurs during the polymerization of PMMA from neat monomer solution is approximately 14%.

[0023] This shrinkage can, in some circumstances, create a particular problem when using PMMA as a circuit housing because, as the encasement reaction progresses, and the viscosity increases as the shrinkage occurs simultaneously, the electrical components 112, which are mounted on the circuit board 110 typically with conductive epoxy, are pulled from the circuit board 110 during the polymerization process.

[0024] The relative strength of the conductive epoxy used to hold the components 112 in place, which conductive epoxy is formulated primarily and maximally for its electrical conductance and cure properties, does not have sufficient mechanical strength to withstand the pull and stress from PMMA shrinkage as the encasement reaction progresses. Consequently, some attempts to encase a circuit from an MMA monomer

encasement formulation result in a non-functional circuit because of un-repairable mechanical damage.

[0025] To solve this problem, one aspect of the present invention is a method by which the polymerization reaction can be conducted without damage to the encased circuit 101. Because pre-polymerized PMMA of large molecular weights (approximately up through 1 million + mw) can be dissolved in MMA monomer, and because the shrinkage is a direct result of bonds forming from discrete monomers, one possible solution is to formulate the encasement formulation to include a portion of MMA monomer and a portion of pre-polymerized PMMA dissolved within the MMA monomer.

[0026] The net shrinkage is proportional to the amount of monomer which is reacted to become polymer within the overall volume. If the overall encasement formulation volume, is portioned to include, for example, about 70% pre-polymerized PMMA, and about 30% un-reacted MMA monomer (into which the 70% PMMA has been dissolved), then the degree of shrinkage which occurs drops in direct proportion to the monomer component within the overall volume. In practice, an encasement formulation of 100% MMA monomer shrinks volumetrically about 14% overall. By dropping the formulation to only 30% MMA, shrinkage in the amount of approximately $0.3 \times 14 = 4.3\%$ would be expected. In practice, approximately 4% shrinkage is measured from making this improvement.

[0027] Accordingly, the result of altering percent solids provides an improvement in system stress during encasement by reducing shrinkage from, for example, 14% to 4% by reformulating MMA/PMMA specifically for the encasement process. Formulation ratios of 60-80% PMMA in MMA are preferred, although not required, because of a present practical limitation. Although to a point, higher ratio values would be expected to reduce shrinkage proportionately, and further reduction in shrinkage may be possible. As a practical matter, the solution viscosity becomes extremely high at these higher ratio levels making the high solids solution extremely difficult to handle, transfer, etc.

[0028] In some situations, however, even with 4% shrinkage, which is a great improvement over 14%, some percentage (about 40-50%) of circuits 101 can not withstand the 4% shrinkage of the encasement. The surviving circuits tend to have greater amounts of conductive epoxy to increase mechanical strength slightly of the surface mounted parts. However, conductive epoxy is not sufficiently strong, and to increase the amount used per connection beyond good manufacturing standards would then create other problems. Another important consideration is for wire-bonded circuits. These "frog hair" gold wires are typically 25 microns in diameter which is about 1/3 to 1/4 the diameter of a typical human hair. Small amounts of movement relative to the fixed board components can rip these wires from the attachments.

[0029] Accordingly, in some applications, it is desirable to mechanically strengthen the circuit 110 to allow it to withstand the remaining shrinkage from the PMMA encasement cure reaction.

[0030] One way to mechanically strengthen circuit 101 to allow it to withstand the remaining 4% shrinkage from the PMMA (70/30) encasement cure reaction, is to reinforce the circuit with a pre-applied epoxy layer. For example, following assembly of the electrical components to the circuit board and cleaning of the assembly, an epoxy is applied over the circuit, which epoxy both under-fills and overfills the components attached to the circuit board. Surprisingly, it was discovered that this solution works best when the applied epoxy covers the components in such a way as to result in a relatively "smooth" surface topology, but this is not a requirement. This "smooth" surface topology is illustrated in FIG. 10A. For comparison, FIG. 10B shows a "non-smooth" epoxy coating. As shown in FIG. 10A, the surface 1002 of the epoxy coating is smooth or substantially smooth.

[0031] Although the epoxy adequately strengthens circuit 101 against damage from the shrinking polymer, the resultant stress caused by the remaining 4% shrinkage then becomes manifest as de-lamination between the adjoining surfaces of epoxy and PMMA within the final encasement. As mentioned above, it was discovered that if the surface was smoothed by the

volume and application of the epoxy pre-coat, not allowing the PMMA to get a "grip" within the surface topology, then de-lamination was less likely to occur. The stress from the 4% remaining shrinkage is then absorbed as internal stress within the PMMA encasement body itself. This stress may be removed in a conventional way by annealing in a final operation.

[0032] Some or all of the epoxy used to reinforce the circuit 110 may, in some embodiments, include a light blocking pigment (such as black or wavelength specific) which prevents unwanted light propagation and scatter about the circuit, thereby increasing the optical signal to noise ratio of the system.

[0033] In some embodiments, to prolong the life of the circuitry 101, it may be desirable to prevent molecular water vapor that has seeped through the housing 102 from condensing to become liquid water. If liquid water cannot form from the water vapor, then potential ion contaminants present cannot become solvated, which can lead to circuit failure.

[0034] One way to prevent the water vapor from condensing is to prevent the formation of heat induced bubbles in the encasement polymer. MMA monomer is extremely volatile. The polymerization reaction of MMA to PMMA is also exothermic. The exothermic heat yield from a typical reaction begun at room temperature will commonly increase the temperature as the reaction progresses to a point where the remaining un-reacted monomer will boil and create bubbles of all

sizes trapped within the cured polymer. To prevent any possibility of heat induced micro-bubbles and voids within the housing where water vapor could condense, substantial overpressure may be used during the polymerization reaction. More specifically, in a preferred embodiment, a mold containing PMMA/MMA is placed within a pressure reactor that is then pressurized to a pressure that exceeds the vapor pressure of MMA monomer at the polymerization temperature. This pressurization process both prevents bubbles and provides a very close mechanical surface bond with the underlying epoxy coat which does not delaminate once formed. The housing is clear and without bubble or void defects to prevent water from condensing; and as an important byproduct, provides excellent optical clarity without bubble defect.

[0035] Referring now to FIG. 3, FIG. 3 is a cross sectional view of circuit assembly 100, according to one embodiment, along line A. As shown in FIG. 3, the circuit 101 may be fully encased within a brick of epoxy 302 (or "epoxy brick 302"), which is encased within housing 102.

[0036] FIG. 4 is a flow chart illustrating a process 400, according to another embodiment, for creating circuit assembly 100. Process 400 may begin in step 402, where a housing 500 (e.g., a sleeve 500 or tube or other housing having an open end) (see FIG. 5) is created along with a plug 504 for plugging the opening in the housing. For example, a cylindrical

sleeve 500 and plug 504 may be machined from a polymer rod, such as a rod of PMMA or other organic polymer. As shown in FIG. 5, sleeve 500 may have a notch 592 adjacent to the open end 594 of sleeve 500. If sleeve 500 and plug 504 are made from PMMA, the PMMA sleeve and plug may be annealed at approximately 80°C for about four hours (step 403).

[0037] In step 404, epoxy is applied over the circuit 101 so that the circuit is partially or fully encased within an epoxy brick 502, thereby forming an assembly 503. In step 406, assembly 503, sleeve 500 and plug 504 are cleaned. For example, assembly 503, sleeve 500 and plug 504 may be cleaned by rubbing a Q-tip with IPA on the surfaces thereof. In step 408, an optical epoxy is prepared. EPO-TEK 301-2 Epoxy from Epoxy Technology of Billerica, MA and other epoxies may be used as the optical epoxy.

[0038] In step 410, the circuit encased within the epoxy brick (i.e., assembly 503) is placed into the sleeve 500. In step 412, the prepared optical epoxy is injected (i.e., introduced) into sleeve 500. Preferably, no bubbles in the optical epoxy are formed during step 412. In step 414, the plug 504 is placed into the open end of sleeve 500, thereby sealing the open end of the sleeve.

[0039] FIG. 6 is a cross sectional view, according to one embodiment, of the circuit assembly 100 along line A after step 414 is performed. In the embodiment shown in FIG. 6, the circuit 101 is fully encased

within an epoxy brick 502. The epoxy brick 502, which houses circuit 101 is placed within sleeve 500, which may be a cylindrical sleeve. When sleeve 500 is a cylindrical sleeve and when circuit 101 is fully encased within the epoxy brick, it is preferable that the distance between the upper right hand corner and lower left corner of epoxy brick 502 be equal to or slightly less than the inner diameter of sleeve 500. That is, in some embodiments it is preferable that $w = \sqrt{(d*d)-(h*h)}$, where w is the width of assembly 503, h is the height of assembly 503, and d is the inner diameter of sleeve 500. In embodiments where the assembly 503 does not have a uniform width or has a circular shaped cross section, then the maximum width of the assembly may be equal to or slightly less than the inner diameter. As illustrated in FIG. 6, the optical epoxy (e.g., a refractive index (RI) matching epoxy) fills spaces between assembly 503 and sleeve 500.

[0040] Referring back to FIG. 4, in step 416, the new assembly (i.e., the sealed sleeve containing the epoxy and assembly 503) is placed into a pressure vessel. In step 418, the pressure within the vessel is increased to about 125psi using Nitrogen or other inert gas. In step 420, the optical epoxy is cured for an amount of time (e.g., 20 hours) at a predetermined temperature (e.g., 40°C). After the predetermined amount of time has elapsed, the assembly

is removed from the pressure vessel and then final machined (step 422).

[0041] The method described above allows the possibility of annealing a PMMA housing before encasement without putting any additional stress on the circuit 101.

[0042] FIG. 7 illustrates an alternative circuit assembly 700 of the present invention. Circuit assembly 700 is similar to circuit assembly 100 in that assembly 700 includes a circuit 101 housed within a housing 102. However, in assembly 700, the circuit 101 is also housed within a glass housing 702 (e.g., a tube or other shaped housing), which itself is housed within the housing 102. The glass housing 702, in some embodiments, is closed at one end and open at the opposite end. The open end may be plugged by a glass ball 704 or other suitable plug. FIG. 8 is an exploded view showing the components of assembly 700, according to one embodiment. Glass housing 702, in some embodiments, may be constructed from an infra-red (IR) blocking glass.

[0043] FIG. 9 is a flow chart illustrating a process 900, according to one embodiment, for making assembly 700. Process 900 may begin in step 902, where a sleeve and a plug, such as sleeve 500 and plug 504, are created.

[0044] In step 904, the sleeve and plug are annealed. The sleeve and plug may be annealed at 80°C

for about four hours. In step 906, the components (e.g., sleeve 500, plug 504, glass housing 702, glass ball 704, epoxy brick 502, etc.) are cleaned. For example, sleeve 500 and plug 504 may be cleaned in an ultrasonic bath with IPA followed by a rinse step, and glass housing 702 and glass ball 704 may also be cleaned ultrasonically with KOH/alcohol solutions and then rinsed with water.

[0045] In step 908, a bonding agent is applied to the glass housing 702 and glass ball 704. The bonding agent used may be trimethoxy [2-(7-oxabicyclo [4.1.0]hept-3-yl)ethyl] silane, which may be purchased from Sigma-Aldrich Corporation (catalog no. 413321)

[0046] In step 910, a batch of optical epoxy is prepared. In step 912, the epoxy coated circuit board is inserted into the glass housing. In step 914, some of the prepared epoxy is injected into the glass housing 702.

[0047] In step 916, some of the prepared epoxy is injected into the sleeve 500. In step 918, glass housing 702, which houses the circuit, is inserted into an open end of the sleeve. In step 920, the glass ball 704 is inserted into the open end of glass housing 702, thereby sealing the open end of the glass housing. In step 922, the plug 504 is used to seal the open end of the sleeve.

[0048] In step 924, the sealed sleeve, which houses glass housing 702, which houses the circuit 101, is

placed into a pressure vessel where the pressure is increased to about 125psi using an inert gas and the temperature is increased to about 40°C. After about 20 hours, the pressure is gradually reduced and the assembly is removed from the pressure vessel and then final machined (step 926).

[0049] Although the above described processes are illustrated as a sequence of steps, it should be understood by one skilled in the art that at least some of the steps need not be performed in the order shown, and, furthermore, some steps may be omitted and additional steps added.

[0050] While various embodiments/variations of the present invention have been described above, it should be understood that they have been presented by way of example only, and not limitation. Thus, the breadth and scope of the present invention should not be limited by any of the above-described exemplary embodiments, but should be defined only in accordance with the following claims and their equivalents.

What is claimed is:

1. A biocompatible, human implantable apparatus, comprising:
a fully enclosed housing; and
a circuit encased within a brick of epoxy,
wherein the brick of epoxy containing the circuit is housed in said housing.
2. The apparatus of claim 1, wherein the housing is a polymer housing and the polymer is an organic polymer.
3. The apparatus of claim 2, wherein the polymer housing comprises PMMA.
4. The apparatus of claim 2, wherein the polymer housing consists of PMMA.
5. The apparatus of claim 2, wherein the polymer housing consists essentially of PMMA.
6. The apparatus of claim 1, wherein at least part of the circuit is covered with an epoxy including a light blocking pigment.
7. The apparatus of claim 6, wherein the circuit comprises a substrate and a plurality of components attached to said substrate.

8. The apparatus of claim 1, further comprising a glass tube, wherein the epoxy brick containing the circuit is positioned within said glass tube and said glass tube is housed within said housing.

9. The apparatus of claim 8, wherein a glass ball is used to seal an open end of said glass tube.

10. The apparatus of claim 8, wherein an optical epoxy fills spaces between said glass tube and the inner wall of said housing.

11. The apparatus of claim 8, wherein an optical epoxy fills spaces between said circuit and the inner wall of said glass tube.

12. The apparatus of claim 1, wherein the housing is substantially cylindrical in shape and has an inner diameter.

13. The apparatus of claim 12, wherein the inner diameter of the polymer housing is equal or about equal to the square root of: $(w^2 + h^2)$, where w is the width of the epoxy brick and h is the height of the epoxy brick.

14. The apparatus of claim 1, wherein the housing does not comprise any glass.

15. A biocompatible, human implantable apparatus, comprising:

a fully enclosed housing;

a glass housing housed within said housing, the glass housing comprising a glass tube having an open end and a glass ball sealing said open end of said tube; and

a circuit housed within said glass housing.

16. The apparatus of claim 15, wherein the housing is a polymer housing and the polymer is an organic polymer.

17. The apparatus of claim 16, wherein the polymer housing comprises PMMA.

18. The apparatus of claim 16, wherein the polymer housing consists of PMMA.

19. The apparatus of claim 16, wherein the polymer housing consists essentially of PMMA.

20. The apparatus of claim 15, wherein at least part of the circuit is covered with an epoxy including a light blocking pigment.

21. The apparatus of claim 20, wherein the circuit comprises a substrate and a plurality of components attached to said substrate.

22. The apparatus of claim 15, further comprising optical epoxy, wherein the optical epoxy fills spaces between said glass housing and the inner wall of said housing.

23. The apparatus of claim 15, wherein an optical epoxy fills spaces between said circuit and the inner wall of said glass housing.

24. A method for fully encasing a circuit within a polymer housing, comprising:

placing the circuit in a mold;

injecting a formulation into the mold so that the formulation completely surrounds the circuit, wherein the formulation comprises monomers; and

polymerizing the monomers.

25. The method of claim 24, further comprising the step of covering the circuit with an epoxy prior to placing the circuit in the mold, wherein a sufficient amount of epoxy is used to cover the circuit so that the resulting surface topology is substantially smooth.

26. The method of claim 24, wherein the polymerization step is performed in a pressure vessel where the pressure is increased to at least about 125 psi using inert gas.

27. The method of claim 24, wherein the formulation comprises MMA monomers.

28. The method of claim 27, wherein the formulation further comprises pre-polymerized PMMA.

29. The method of claim 28, wherein the formulation comprises between 60% and 80% pre-polymerized PMMA by volume.

30. The method of claim 24, wherein the formulation consists essentially of MMA monomers.

31. The method of claim 24, wherein the formulation consists of MMA monomers.

32. A method for fully encasing a circuit within a housing, comprising:

inserting the circuit into the housing;

injecting an optical epoxy into the housing;

placing the housing containing the optical epoxy and the circuit into a pressure vessel;

increasing the pressure within the vessel;

increasing the temperature within the vessel;

allowing the optical epoxy to cure;

removing the housing from the pressure vessel;

and

capping an open end of the housing.

33. The method of claim 32, wherein the housing comprises PMMA.

34. The method of claim 32, wherein the housing consists essentially of PMMA.

35. The method of claim 32, further comprising the step of encasing the circuit within an epoxy brick prior to placing the circuit in the housing.

36. The method of claim 32, wherein the pressure is increased to at least about 125 psi.

37. The method of claim 32, wherein the temperature is increased to about 40 degrees centigrade.

38. The method of claim 32, wherein the circuit is inserted into the housing after the optical epoxy is injected into the housing.

39. The method of claim 32, wherein the circuit is inserted into the housing before the optical epoxy is injected into the housing.

40. A method for fully encasing a circuit within a housing, comprising:

inserting the circuit into the glass housing;

injecting an optical epoxy into a glass housing;
injecting an optical epoxy into a second housing;
inserting into the second housing the glass
housing containing the circuit;
capping an open end of the glass housing; and
capping an open end of the second housing.

41. The method of claim 40, wherein the second housing comprises PMMA.

42. The method of claim 40, wherein the second housing consists essentially of PMMA.

43. The method of claim 40, further comprising the step of encasing the circuit within an epoxy brick prior to placing the circuit in the glass housing.

44. The method of claim 40, wherein the circuit is inserted into the glass housing after the optical epoxy is injected into the glass housing.

45. The method of claim 40, wherein the circuit is inserted into the glass housing before the optical epoxy is injected into the glass housing.

46. The method of claim 40, wherein the step of capping an open end of the glass housing comprises the step of inserting a glass ball at least partially into said open end of the glass housing.

47. The method of claim 40, further comprising the step of curing the optical epoxy contained in the glass housing prior to inserting the glass housing into the second housing.

48. The method of claim 47, wherein the step of curing the optical epoxy comprises the step of placing the glass housing containing the optical epoxy and the circuit into a pressure vessel and increasing the temperature and pressure within the vessel.

49. The method of claim 40, further comprising the step of curing the optical epoxy contained in the glass housing after inserting the glass housing into the second housing.

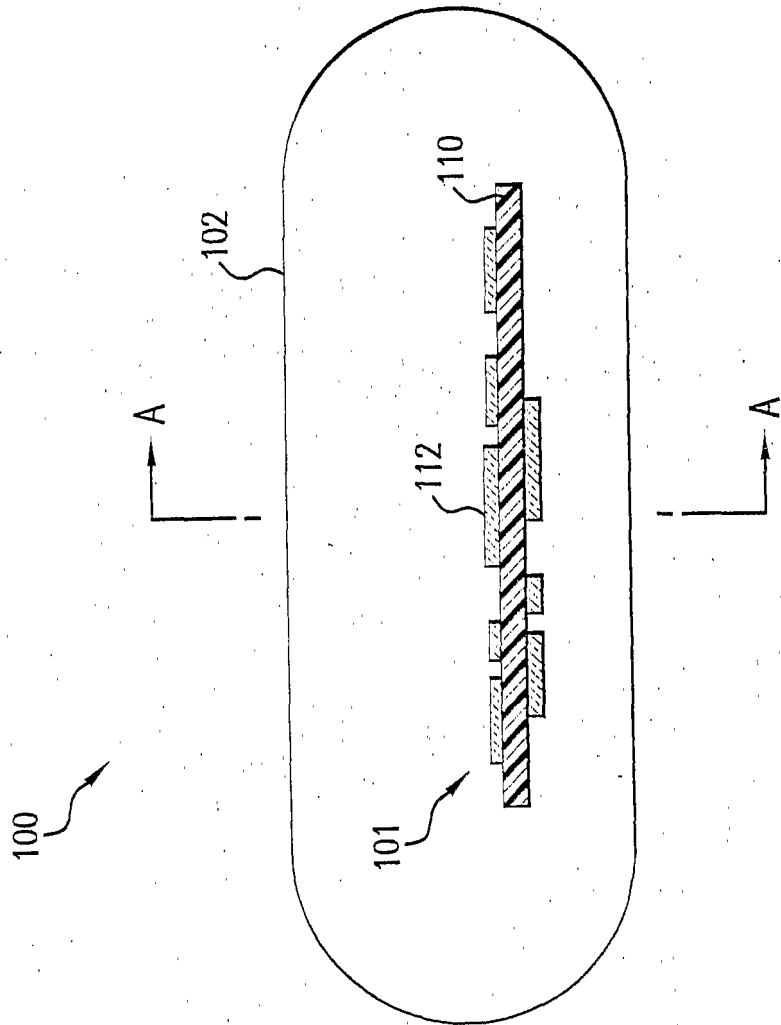


FIG. 1

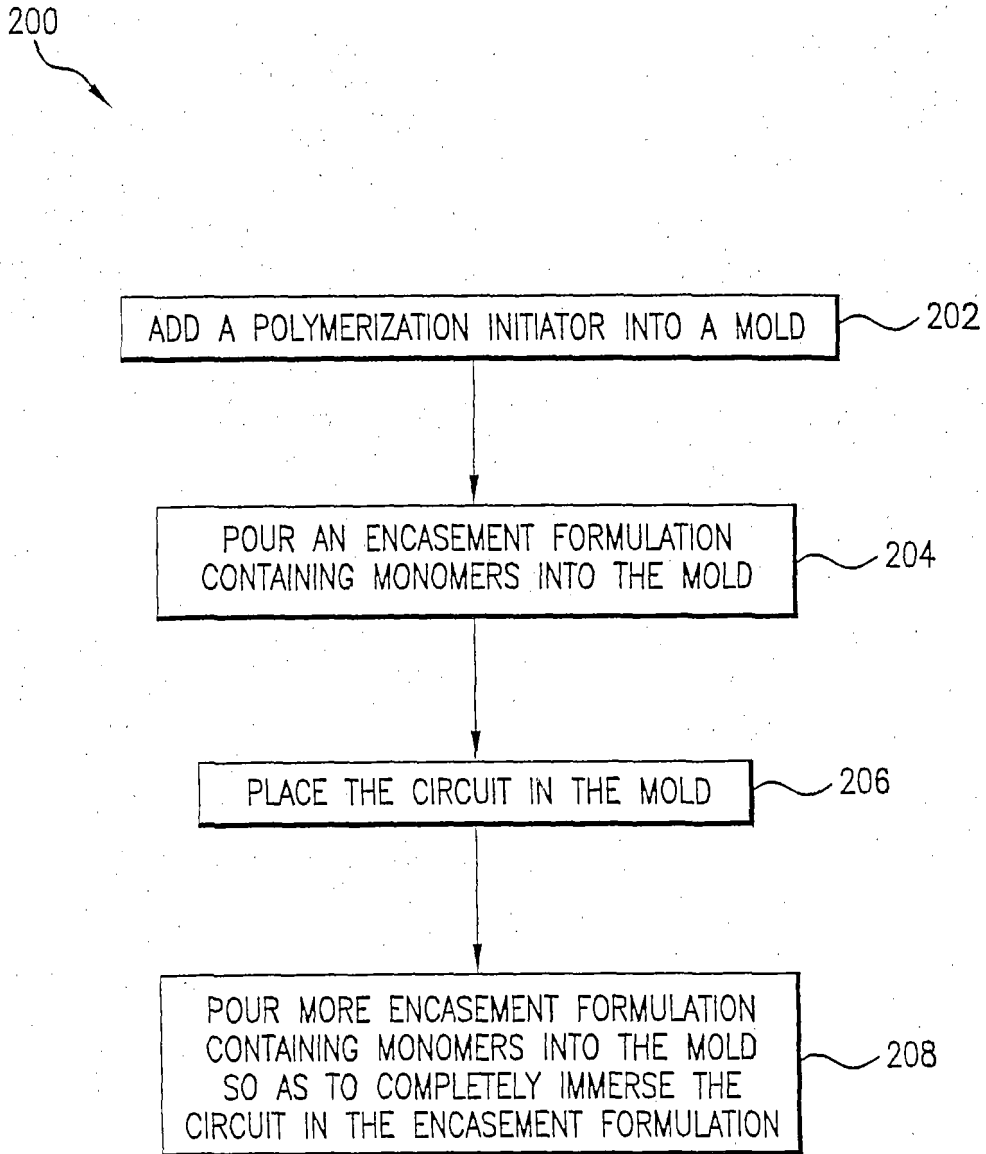


FIG.2

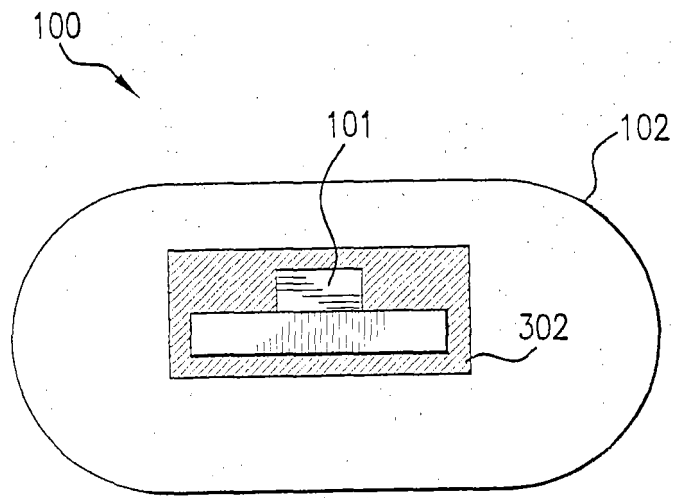


FIG. 3

400

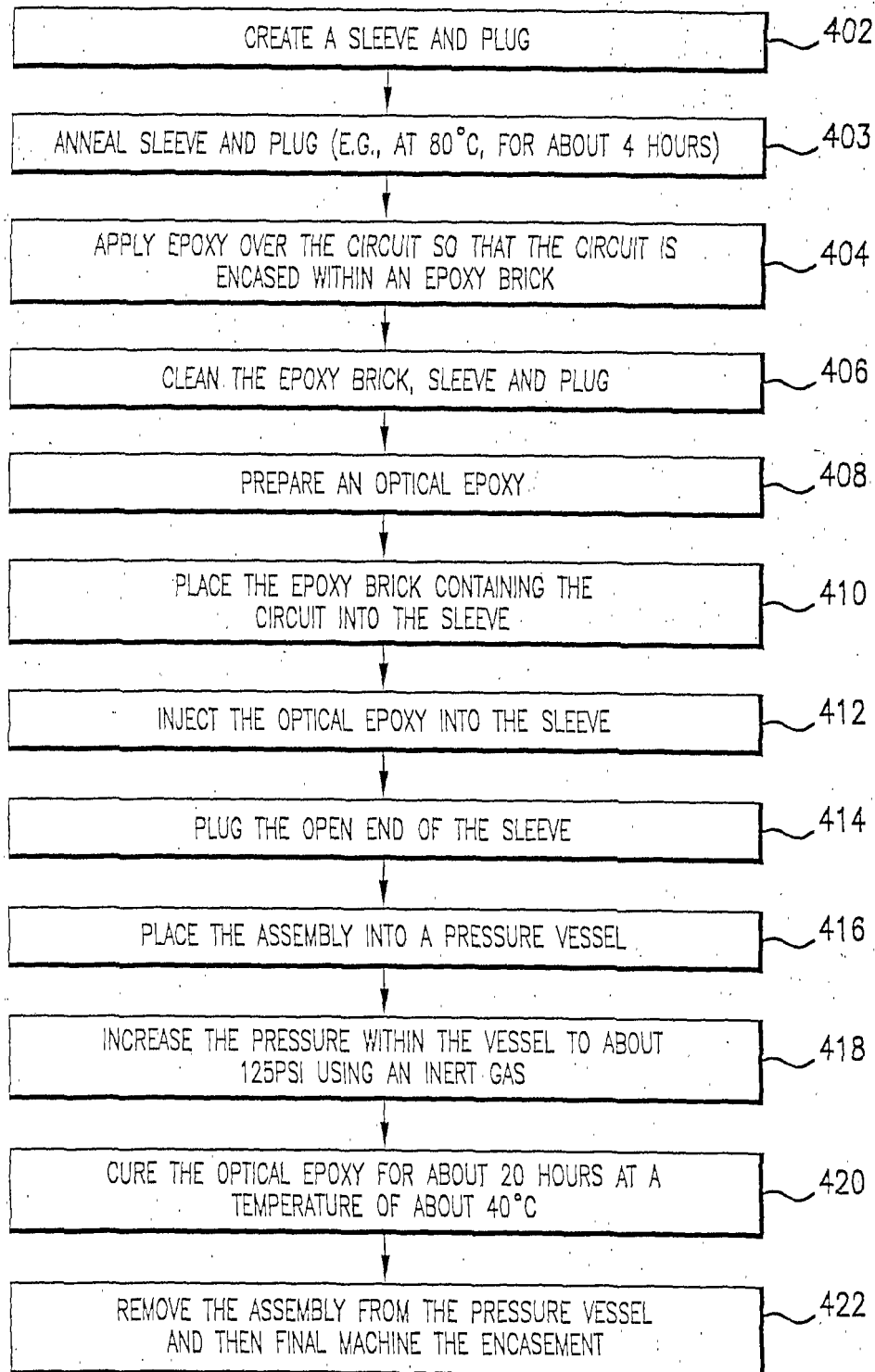


FIG. 4

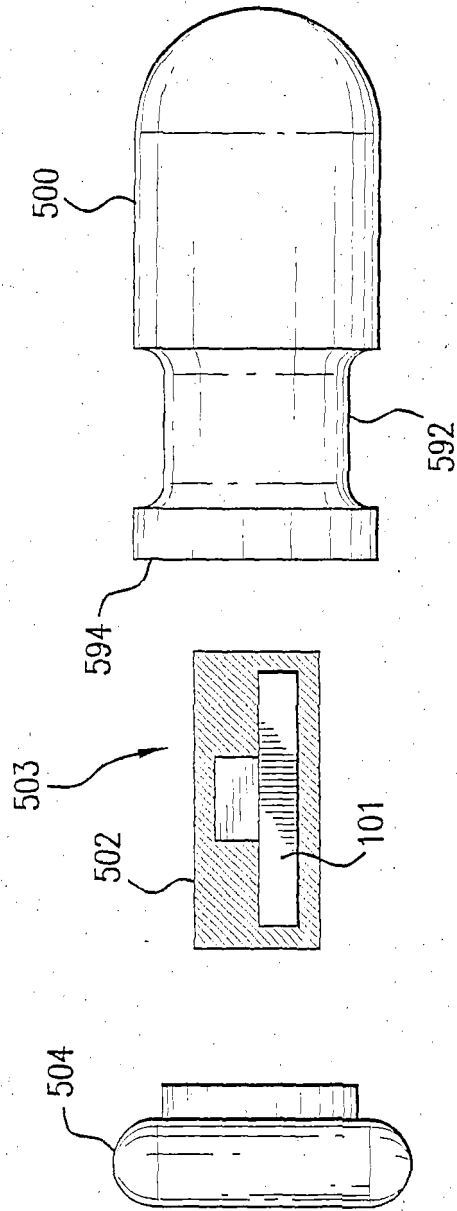


FIG. 5

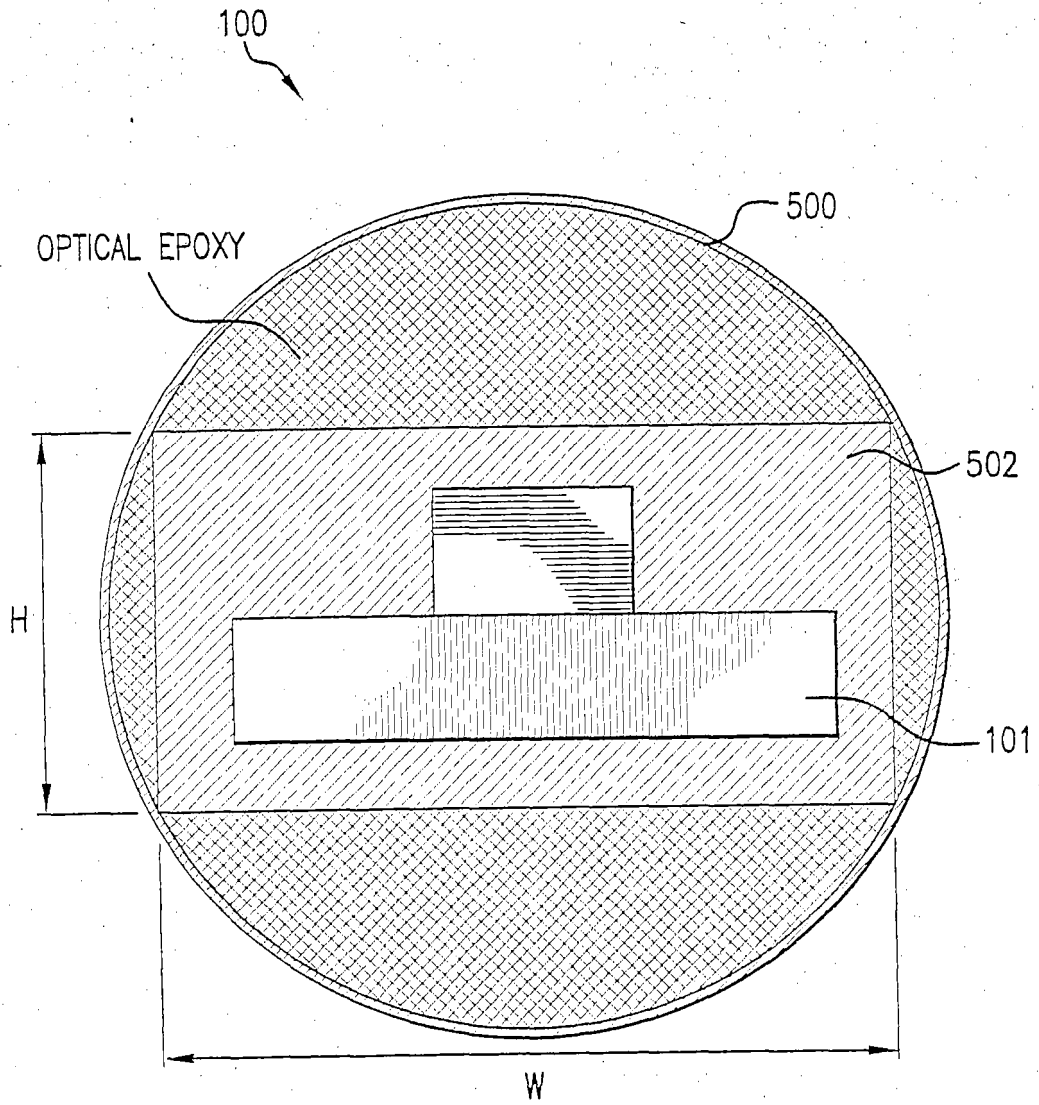


FIG.6

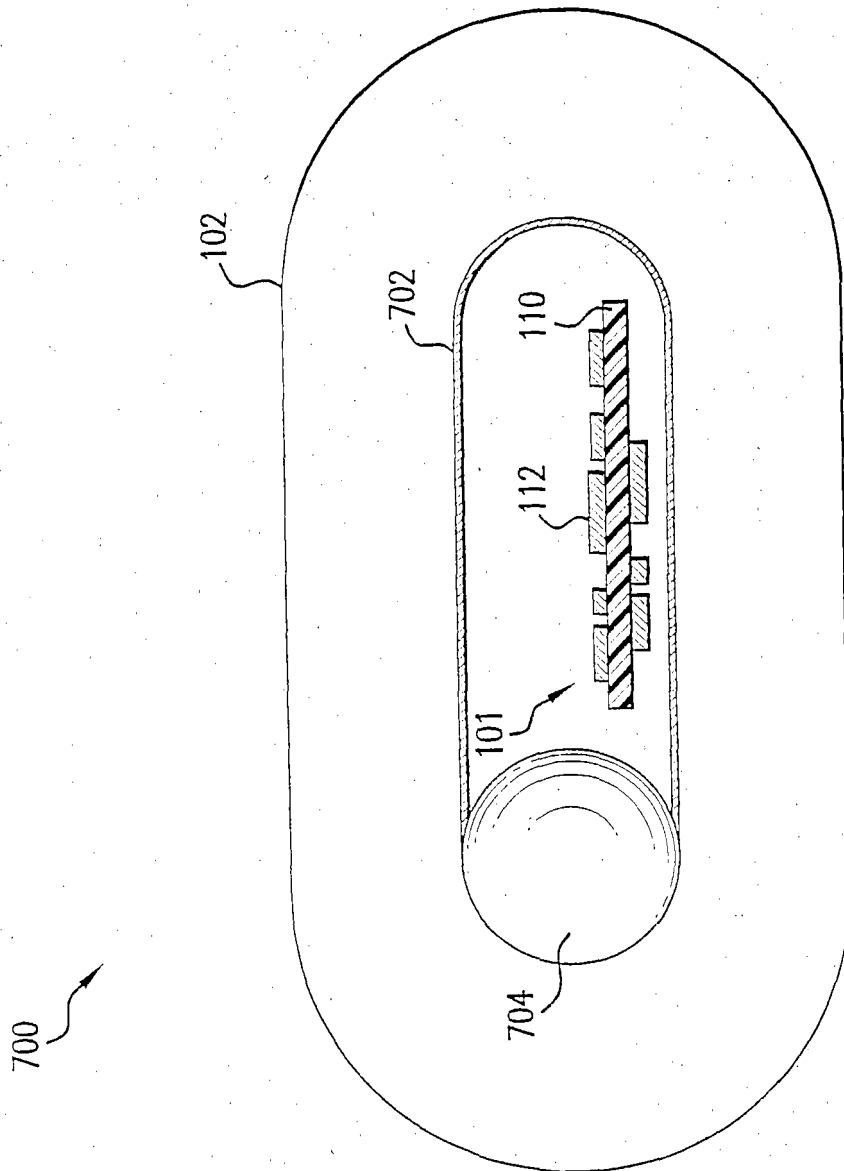


FIG. 7

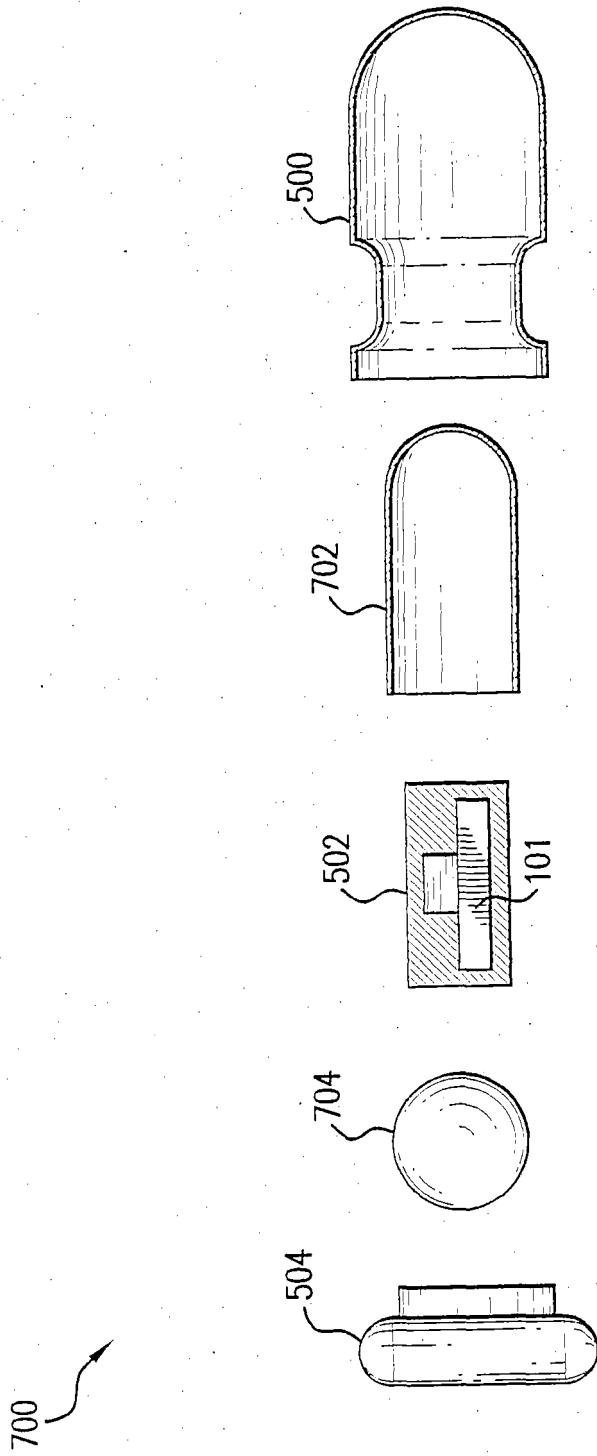


FIG.8

900

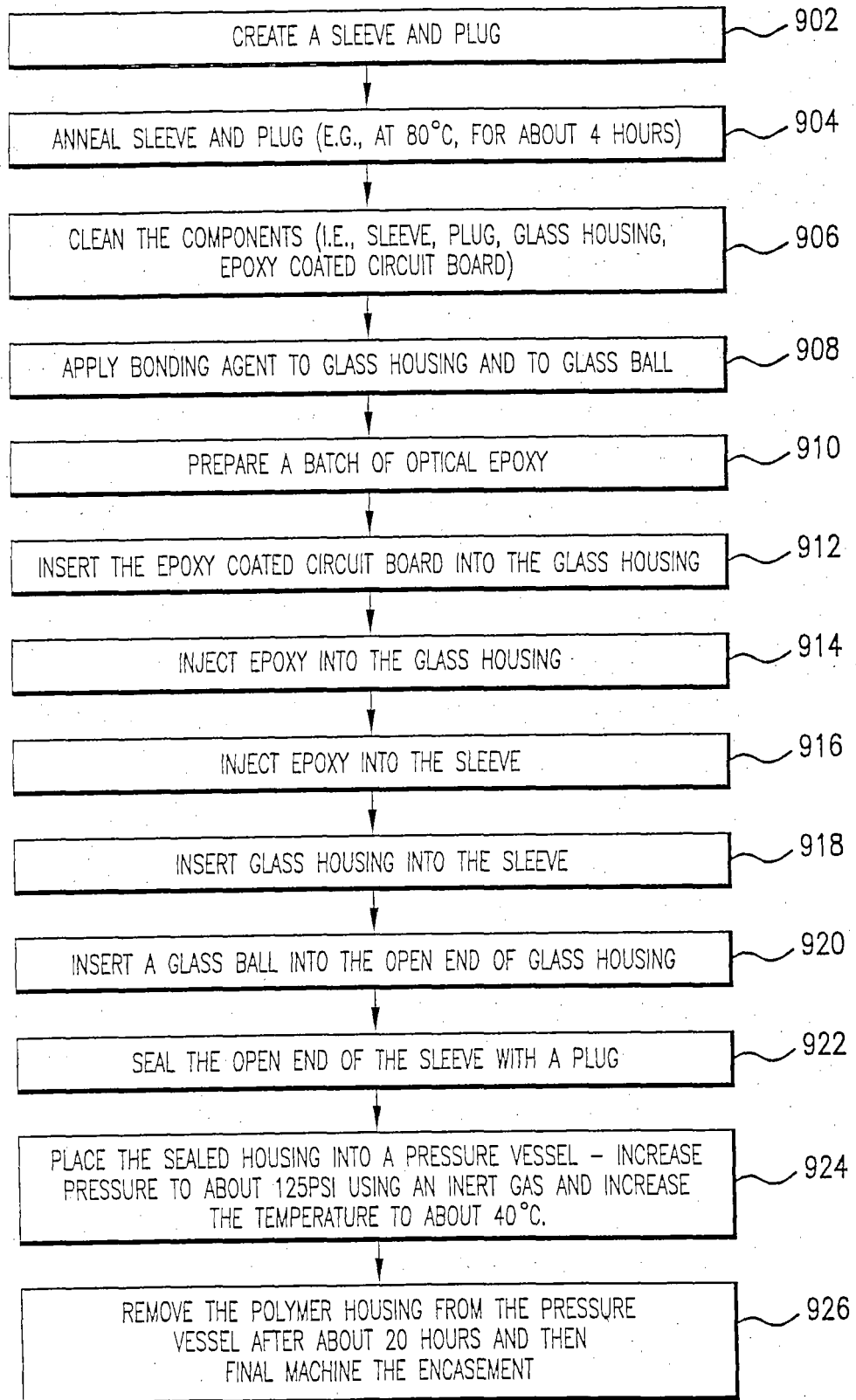


FIG.9

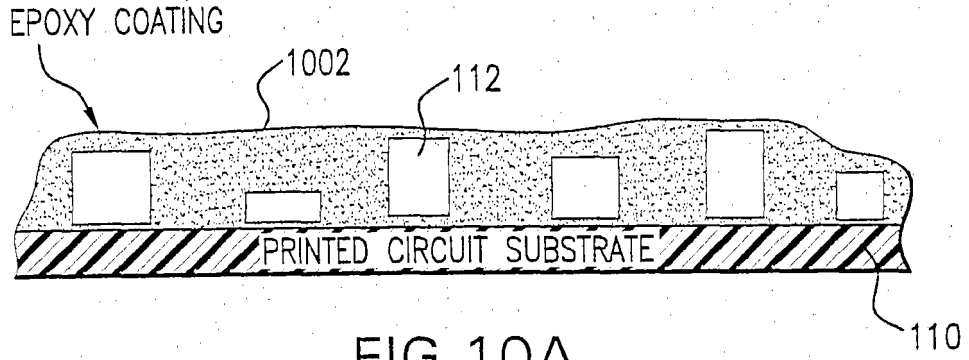


FIG. 10A

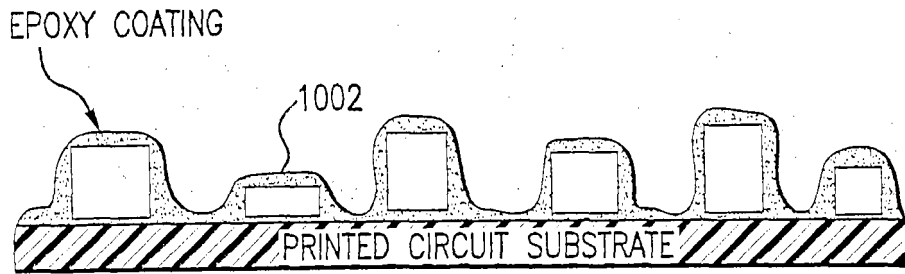


FIG. 10B

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US2005/011653

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H05K5/00 A61B5/07				
According to International Patent Classification (IPC) or to both national classification and IPC				
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC 7 H05K A61B A01K				
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched				
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, WPI Data, PAJ, IBM-TDB, COMPENDEX, INSPEC				
C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.		
X	US 2003/181794 A1 (RINI CHRISTOPHER J ET AL) 25 September 2003 (2003-09-25) paragraph [0069] - paragraph [0076]; figure 1 paragraph [0096] - paragraph [0098]; figures 19-21	1-5, 8-10, 12-19,22		
X	EP 0 940 076 A (N.V. NEDERLANDSCHE APPARATENFABRIEK NEDAP) 8 September 1999 (1999-09-08) paragraph [0019]; figures 1,2 ----- -/--	1,12		
<input checked="" type="checkbox"/> Further documents are listed in the continuation of box C.				
<input checked="" type="checkbox"/> Patent family members are listed in annex.				
° Special categories of cited documents :				
<table style="width: 100%; border: none;"> <tr> <td style="width: 50%; border: none; vertical-align: top;"> "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed </td> <td style="width: 50%; border: none; vertical-align: top;"> "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. "&" document member of the same patent family </td> </tr> </table>			"A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. "&" document member of the same patent family
"A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. "&" document member of the same patent family			
Date of the actual completion of the international search	Date of mailing of the international search report			
12 July 2005	21.09.05			
Name and mailing address of the ISA	Authorized officer			
European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Dobbs, H			

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US2005/011653

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>LOVELY D F ET AL: "Epoxy moulding system for the encapsulation of microelectronic devices suitable for implantation" MEDICAL & BIOLOGICAL ENGINEERING & COMPUTING UK, vol. 24, no. 2, March 1986 (1986-03), pages 206-208, XP001206580 ISSN: 0140-0118 page 208, column 1, paragraph 2 - column 2, paragraph 1</p>	24-31
A	<p>----- US 2002/026108 A1 (COLVIN ARTHUR E ET AL) 28 February 2002 (2002-02-28) paragraph [0163]; figure 17</p>	1-23, 40-49
A	<p>----- US 5 840 148 A (CAMPBELL ET AL) 24 November 1998 (1998-11-24) column 3, line 15 - line 67; figures 1-5</p>	1-23, 40-49

INTERNATIONAL SEARCH REPORT

international application No.
PCT/US2005/011653

Box II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. Claims Nos.:
because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically:

3. Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

1. As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims.
2. As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:
4. No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:
1-5, 8-10, 12-19, 22, 24-31, 40-49

Remark on Protest

- The additional search fees were accompanied by the applicant's protest.
 No protest accompanied the payment of additional search fees.

INTERNATIONAL SEARCH REPORT

International Application No. PCT/ US2005/ 011653

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-5, 8-10,12-19, 22, 24-31, 40-49

A biocompatible, human implantable apparatus including a polymer housing or a glass tube enclosed within the housing, and a method for encasing a circuit thereto.

2. claims: 6, 7, 11, 20, 21, 23, 32-39

A biocompatible, human implantable apparatus including an epoxy including light-blocking pigment or an optical epoxy, and a method for encasing a circuit.

INTERNATIONAL SEARCH REPORT

Information on patent family members

In International Application No
PCT/US2005/011653

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2003181794 A1	25-09-2003	NONE	
EP 0940076 A	08-09-1999	NL 1008512 C2	07-09-1999
US 2002026108 A1	28-02-2002	NONE	
US 5840148 A	24-11-1998	AU 725710 B2	19-10-2000
		AU 5624596 A	09-01-1997
		CA 2179966 A1	31-12-1996
		CH 693348 A5	30-06-2003
		DE 19626093 A1	02-01-1997
		FR 2736240 A1	03-01-1997
		GB 2303026 A	05-02-1997
		IT T0960560 A1	29-12-1997
		JP 2713878 B2	16-02-1998
		JP 9117230 A	06-05-1997
		NL 1003465 C2	23-09-1997
		NL 1003465 A1	31-12-1996
		NZ 286874 A	26-02-1998

专利名称(译)	用于在VIVO中植入的电路的外壳及其制造过程		
公开(公告)号	EP1736041A1	公开(公告)日	2006-12-27
申请号	EP2005731370	申请日	2005-04-07
[标]申请(专利权)人(译)	医药及科学传感器公司		
申请(专利权)人(译)	医学和科学传感器公司		
当前申请(专利权)人(译)	SENSEONICS注册成立		
[标]发明人	COLVIN JR ARTHUR E LORENZ CARRIE R OCONNOR CASEY J WALTERS STEVEN J		
发明人	COLVIN, JR, ARTHUR E. LORENZ, CARRIE R. O'CONNOR, CASEY J. WALTERS, STEVEN J.		
IPC分类号	H05K5/00 A61B5/07 H05K5/06 A61B5/00		
CPC分类号	A61B5/0031 H05K5/065		
优先权	10/825648 2004-04-16 US		
其他公开文献	EP1736041B1		
外部链接	Espacenet		

摘要(译)

本发明提供了一种生物相容性电路组件，其包括封装在壳体内的电路。在一些实施例中，壳体是PMMA壳体，并且在电路封闭在壳体内之前，电路被封装在环氧树脂砖内。