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**(54) Biomedical acquisition system with motion artifact reduction**

Biomedizinisches Erfassungssystem mit Bewegungsartifaktreduktion

Système d'acquisition biomédicale avec réduction d'artéfacts de mouvement

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## Description

### BACKGROUND

#### 1. Technical Field

[0001] The present invention is related to the measurement and analysis of biopotential electrical signals, such as electrocardiogram (ECG) signals and more specifically to a biomedical acquisition system with motion artifact reduction.

#### 2. Technical Background

[0002] Monitoring over a given period of time of biopotential electrical signals, such as an electrocardiogram (ECG), may be used, for example, to evaluate the heart condition of a patient. Recently, there has been a growing interest in wearable/portable biopotential monitoring systems, to be used, for example, in ambulatory monitoring of ECG signals, which require, for example, low power dissipation, high signal quality, small implementation area and/or robust operation during use. Especially the latter is emerging as a major problem since in real-life ambulatory conditions motion artifacts will disturb and potentially saturate the readout channel, which can make the interpretation of the ECG signal difficult. In addition, requirements for biomedical acquisition systems require even more functionality with minimal power dissipation.

[0003] EP 2 298 164 discloses an analogue signal processor (ASP) application-specific integrated circuit (ASIC) that can be used for remotely monitoring ECG signals. The circuit comprises a digital filter for the removal of artifacts from the ECG signals. The measured impedance and the ECG signal. are passed to the filter. Artifacts present in the ECG signal are removed using the impedance measurement in the filter. The output from the filter is then passed to a low-power beat detection algorithm, where beat detection is carried out on the filtered ECG signals.

### SUMMARY OF THE INVENTION

[0004] According to an example embodiment of the present disclosure there is provided a system for the analysis of ECG signals comprising: at least one readout channel, configured to receive an analogue ECG signal acquired from at least one electrode attached to a body; and to extract an analogue measured ECG signal and analogue electrode-skin impedance signals; at least one ADC, configured to convert those extracted analogue signals at the readout channel into digital signals; a digital adaptive filter unit, configured to calculate a digital motion artifact estimate based on said digital versions of the measured ECG signal and the electrode-skin impedance signals; at least one DAC, configured to convert said digital motion artifact estimate into an analogue signal; and a feedback loop for sending said analogue motion artifact

estimate signal back to an analogue ECG readout module within the readout channel configured to deduct said analogue motion artifact estimate signal from said analogue measured ECG signal. According to an example embodiment, the analogue electrode-skin impedance signals comprise an analogue in-phase electrode-skin impedance signal, and an analogue quadrature electrode-skin impedance signal.

[0005] According to still another example embodiment, the digital adaptive filter unit comprises a digital adaptive filter that uses electrode-skin impedance signals as a reference to reduce motion artifacts from the measured ECG signal. According to still another example embodiment, the digital adaptive filter comprises a LMS filter.

[0006] According to an example embodiment, the readout channel further comprises a current source configured to inject current into the received analogue ECG signal leads to facilitate the extraction of the analogue electrode-skin impedance signals.

[0007] According to an example embodiment, the analogue ECG readout module comprises an analogue gain amplifier at an analogue ECG readout module configured to deduct the analogue motion artifact estimate signal from the analogue measured ECG signal. According to an example embodiment, the analogue gain amplifier is a programmable gain amplifier which uses a differential difference amplifier architecture to accomplish subtraction of the analogue motion artifact estimate signal from the analogue measured ECG signal. According to another example embodiment, the analogue gain amplifier is a programmable gain amplifier which has a reference input to determine the input DC signal level and configured to apply the analogue motion artifact estimate signal to said reference input to accomplish deduction of the analogue motion artifact estimate signal from the analogue measured ECG signal.

[0008] According to an example embodiment, analogue analysis and treatment of ECG signals, such as deducting said analogue motion artifact estimate signal from said analogue measured ECG signal, is performed in an analogue application-specific integrated circuit (ASIC), and digital analysis and treatment of ECG signals, such as calculating a digital motion artifact estimate, is performed in a microprocessor unit, said ASIC and said microprocessor unit configured to communicate with each other. According to another example embodiment, the system comprises a digital interface comprising decimation filters for the at least one ADC and configured to multiplex digitized outputs onto a single serial peripheral interface (SPI) output, the digital interface further comprising a secondary single SPI input for driving the at least one DAC.

[0009] According to still an example embodiment, the ADC is a successive approximation ADC.

[0010] These as well as other aspects, advantages, and alternatives, will become apparent to those of ordinary skill in the art by reading the following detailed description, with reference where appropriate to the accom-

panying drawings.

## BRIEF DESCRIPTION OF THE DRAWINGS

[0011] The disclosure will be further elucidated by means of the following description and the appended figures. Various exemplary embodiments are described herein with reference to the following figures, wherein like numerals denote like entities. The figures described are schematic and are non-limiting. Further, any reference signs in the claims shall not be construed as limiting the scope of the present disclosure. Still further, in the different figures, the same reference signs refer to the same or analogous elements.

**Figure 1** shows a first exemplary block diagram of a biomedical acquisition system according to an example of the present invention.

**Figure 2** shows a second exemplary block diagram of a biomedical acquisition system according to an example of the present invention.

**Figure 3** shows a third exemplary block diagram of a biomedical acquisition system according to an example of the present invention.

## DETAILED DESCRIPTION

[0012] **Figure 1** shows a first example embodiment of a multi-channel biomedical acquisition system **100** according to the invention, capable of actual real-time motion artifact suppression before the ADC. The system **100** comprises three readout channels **110**, **111**, **112** configured for receiving ECG signals **101**, **102**, **103** from corresponding electrodes attached to a body. Each readout channel **110**, **111**, **112** includes an ECG readout module **115** for extracting a measured ECG signal **120** and an impedance readout module **116** for extracting an electro-tissue impedance (ETI) signal, both an in-phase electrode-skin impedance signal **121**, and a quadrature electrode-skin impedance signal **122**. A current source **125** is also integrated in the readout channels to inject current into the ECG leads to facilitate the measurement of the ETI. The outputs (plus any extra sensors) of each readout module of the biomedical acquisition system are digitized by five SAR ADCs **130**, which can be selectively operated at an oversampling mode (8kSps/chn) or standard mode (500Sps/chn). The outputs of the ADCs **130** are time-multiplexed on a master SPI output line. According to an example embodiment of the invention, the outputs of the impedance readout module **116** are post-processed and sent through three DACs **131** and corresponding feedback lines **140**, back to the ECG readout module **115** in order to accomplish motion artifact suppression.

[0013] According to an example embodiment of the invention, the SAR-ADCs **130** convert ten analog signals, three ECG signals, and six ETI signals and one extra analog input. The outputs of the SAR-ADCs are mainly used to aid motion artifact estimation. The system **100**

also includes three sigma-Delta ADCs (not shown). Once the analog waveforms of a motion artifact estimate are generated (one for each readout channel), the motion artifact estimate is fed back, with the use of three DACs **131**, negatively to the ECG readout modules and is deducted or subtracted from the measured ECG signal, for example at a programmable gain amplifier (PGA) **150**, in order to reduce the measured ECG signal amplitude, for example at the output of the PGA **150**. In this way a lower resolution ADC is needed for processing the measured ECG signal, which leads to lower power consumption and smaller area overhead.

[0014] According to an example embodiment of the invention, a digital interface is incorporated for implementing the decimation filters for the ADCs and for multiplexing all the digitized outputs, from each readout channel, onto a single SPI output. A secondary single SPI input is used to deliver digital filter output to the DACs. The output SPI operates in master mode, while the input SPI operates in slave mode. The generation of the various clocks for each readout channel is derived from a single input clock (~32 kHz), wherein said input clock is generated externally. A chopper clock generator derives the various clock signals (2 kHz chopper, 4 kHz bias and 8 kHz ripple filter) from the externally generated input clock. The band-gap reference generates a stable reference voltage. The bias generator derives the various bias currents and voltages from the band-gap reference voltage. The sample clock of the SAR ADC is generated by a first on-chip oscillator; the oscillator in this case generates a clock signal of 1 MHz. A second ring oscillator is also incorporated to generate the SPI output clock, in this case also 1MHz. This oscillator consumes less power and can be turned on and off quickly to save power when no data needs to be pushed on the SPI bus.

[0015] According to an example embodiment of the invention, to store the various configuration modes of the biomedical acquisition system **100** a number of configuration registers are implemented in the form of serially programmable shift register.

[0016] According to an example embodiment of the invention, the biomedical acquisition system **100** may be implemented, for example, as an ASIC or as a bio-potential acquisition ASIC (BASIC) **160** in combination with a microcontroller ( $\mu$ C) or microprocessor **170** configured to digitally process signals received from the BASIC **160**, as shown in **Figure 2**. According to an example embodiment, the BASIC **160** communicates with the microcontroller **170**, to send and receive data. The microcontroller **170** may also, for example, supply a clock (e.g. 32 kHz) and/or a control signal (e.g. serial\_data/serial\_clk) to program the BASIC **160** in the desired mode of operation. According to another example embodiment, the microcontroller **170** may, for example, receive SPI data packets (16 bit), representing the digitized data as well as a time reference packet. According to an example embodiment, the microcontroller may comprise a digital adaptive filter unit **180** configured to calculate a motion artifact

estimate **181** based on input digitized versions of the measured ECG signal **120** and the in-phase electrode-skin impedance signal **121**, and the quadrature electrode-skin impedance signal **122**, and send the motion artifact estimate **181**, for example through an SPI data bus, back to the BASIC **160**.

**[0017]** According to an example embodiment of the invention, the digital adaptive filter unit **180** may be implemented in hardware and/or software. According to another embodiment of the invention the digital adaptive filter unit **180** may implement or run, for example, a Least Mean Square (LMS) filter. The LMS filter estimates the motion artifact on the readout channel **110** based on the ETI signals. The motion artifact estimate **181** is fed back to the BASIC **160** through a DAC **131** and a feedback line **140**, and the DAC output is deducted or subtracted from the measured ECG before final amplification by the PGA **150** of the ECG readout module **115**. According to another example embodiment of the invention, the PGA **150** uses a Differential Difference Amplifier (DDA) architecture to accomplish the subtraction.

**[0018]** According to another example embodiment, the digital adaptive filter unit **180** may implement or run any other type of filter which uses electrode skin impedance signals as a reference (that have maximum correlation with the motion artifact signal and minimal correlation with the ECG signal) to reduce the motion artifact from the ECG signals. According to another example embodiment, statistical analysis algorithms, such as ICA (Independent Component Analysis) or PCA (Principal Component Analysis) algorithms can be used to remove motion artifacts in digital domain.

**[0019]** According to an example embodiment of the invention, the biomedical acquisition system **100** achieves reduction or suppression of the motion artifact signals present in the measured ECG signal **120** by reducing said motion artifact signals in the analogue domain, prior to final amplification and prior to analogue to digital conversion of the measured ECG signal **120**. According to an example embodiment of the invention, said suppression of the motion artifact signals is performed with the aid of adaptive filtering in the digital domain. According to another example embodiment, the biomedical acquisition system is capable of actual real-time motion artifact suppression before the ADC **130**.

**[0020]** **Figure 3** shows another schematic view of an example biomedical acquisition system **100** according to the invention, comprising a current source **125**, two transconductance amplifiers **190, 191**, three transimpedance amplifiers **192, 193, 194**, three programmable gain amplifier **150, 151, 152**, an analogue to digital converter (ADC) **130**, a digital adaptive filter unit **180**, and a digital to analogue converter (DAC) **131**.

**[0021]** According to an example embodiment of the invention, the digital adaptive filter unit **180** extracts the estimated motion artifact from the reference signals (the in-phase electrode-skin impedance (IMPI) signal **121** and the quadrature-phase electrode-skin impedance

(IMPQ) signal **122**) and the measured ECG signal **120**. Then, the extracted motion artifact estimate is converted to an analog signal through the DAC **131** and sent back, through feedback loop **140**, to the input of the PGA **150** in the ECG readout module in order to reduce the motion artifact in the analog domain.

**[0022]** According to an example embodiment of the invention, at a first instance, the measured ECG signal **120** comprises the received ECG signal with a motion artifact signal (ECG+MA). The electrode-skin impedance signals **121, 122** and the ECG signal with motion artifact signal (ECG+MA) are measured simultaneously. These measured analog signals are then converted to digital signals and processed using the digital adaptive filter unit **180**, which then generates a digital motion artifact estimate, and said digital motion artifact estimate is converted to an analogue signal and feedback to the input of the PGA **150**. The PGA **150** then performs a first coarse motion artifact reduction by deducting or subtracting the motion artifact estimate signal from ECG signal with motion artifact signal (ECG+MA). At a second instance, the measured ECG signal **120** at the output of the PGA **150** is processed again at the digital adaptive filter unit **180** to calculate a more accurate digital motion artifact estimate which later on enables a finer motion artifact reduction. Therefore, a coarse and a fine motion artifact reduction is advantageously possible with one DAC.

**[0023]** According to an example embodiment of the invention, the output signal of the PGA **150** only includes the ECG signal without the motion artifact signal, which significantly reduces the dynamic range requirements of the ADC leading to power and area reduction of the system.

**[0024]** According to another example embodiment of the invention, the motion artifact is reduced in the analog domain before the ADC, which advantageously prevents ECG channel saturation in analog domain and ECG signal loss. Therefore, the measured ECG signal can be preserved without information loss due to a large motion artifact signal. Furthermore, the motion artifact signal can be reduced in front of the ADC which reduces the required resolution for the ADC, thus leading to reduction of the power consumption and ADC area.

**[0025]** According to another example embodiment of the invention, the PGA has a reference input to determine the input DC level, the estimated motion artifact signal can be applied to this reference input of the PGA and therefore eliminating the need of an analog subtraction block for the feedback signal.

**[0026]** According to another example embodiment of the invention, the PGA has a gain higher than one, and the feedback system doesn't require additional gain stage, which eliminates the need for extra power consumption and area.

**[0027]** According to another example embodiment of the invention, real time motion artifact reduction is achieved in the analog domain without extra sensors. To reduce the motion artifact, digital adaptive filter requires

reference signal which has high correlation with motion artifact and low correlation with target ECG signal. By measuring electrode-skin impedance and use it as a reference signal for the digital adaptive filter, the system doesn't need any extra sensors for the digitally assisted analog motion artifact reduction.

**[0028]** According to another example embodiment of the invention, the BASIC is implemented in CMOS technology.

**[0029]** Further example embodiments and advantages will be described below.

**[0030]** According to an example embodiment of the invention, the present disclosure describes a biomedical acquisition system for the evaluation and detection of dynamically changing biopotential electrical signals, such as ECG signals.

**[0031]** According to still another embodiment of the present disclosure a biomedical acquisition system is provided, configured for monitoring dynamically changing biopotential electrical signals, such as the ambulatory monitoring of Electrocardiogram (ECG). The system may comprise the following features: at least one readout channel, each configured for receiving the ECG signal acquired by at least one electrode attached to the biological subject body; at least one successive approximation register (SAR) analog to digital converter (ADC), configured for converting two analog signals and can operate in standard mode or oversampled mode; at least one sigma-delta ADC configured for converting the ECG signal received by the electrodes; at least one Digital-to-Analogue Converters (DAC) for generating the analog waveforms of the motion artifact estimate, said motion artifact estimate being fed back negatively to the ECG readout channels; a digital interface configured for implementing decimation filters for the ADCs and for multiplexing all the digitized outputs onto a single serial peripheral interface (SPI) output, said digital interface may also include a secondary single SPI input for driving the plurality of DACs; a chopper clock generator for generating the clock signals for each readout channel; a band-gap and bias generation circuit for generating the biasing currents and voltages; a first oscillator for generating a sample clock signal, wherein said clock signal is connected to the SAR-ADC; a second oscillator for generating the SPI output clock signal; at least one configuration register configured for storing the various configuration settings of the biopotential acquisition system.

**[0032]** According to another example embodiment of the present disclosure, an ASIC communicates via an SPI output and SPI input with a micro-controller to send and receive data. The microcontroller supplies the clock (32 kHz) and serial\_data/serial\_clk to program the ASIC in the desired mode of operation. It receives 16 bit SPI data packets, representing the digitized data as well as a time reference packet. The microcontroller will run a motion artifact estimation algorithm and sends the motion artifact estimates on a separate SPI data bus back to the ASIC.

**[0033]** According to another example embodiment of the present disclosure, each readout channel of the biopotential system further comprises: an ECG readout channel for extracting the ECG signal; an Impedance readout channel for extracting the electro-tissue impedance (ETI); a current source for injecting current into the ECG leads to measure the ETI.

**[0034]** According to still another example embodiment, the channel outputs (plus any extra sensor inputs) are digitized by the plurality of SAR ADCs, which can be selectively operated at an oversampling mode or standard mode. The outputs of the ADCs are time-multiplexed on a master SPI output line. The outputs of the ETI channels are post-processed and fed back to the ECG channels through the plurality of DACs to accomplish Motion artifact suppression.

**[0035]** According to still another example embodiment, to enhance the signal quality the Biomedical acquisition system includes an Instrumentation Amplifier (IA) with a fully integrated low frequency High Pass Filter (HPF), which is capable of achieving a very high-mode rejection ration (CMRR) while also being able to reject large DC electrode offset (DEO). The HPF may be implemented with fully-integrated DC-blocking capacitors, which allow rail-to-rail DEO rejection. As a result the proposed integrated HPF allows for the compensation of parasitic mismatch and maximizes the CMRR to 120dB. The resistance of the HPF can be realized by the switched-cap resistors, to provide the DC-bias and the differential input impedance.

**[0036]** According to still another example embodiment, the biomedical acquisition system also includes a smart saturation detection block and fast recovery circuit to facilitate quick start-up and immediate recovery from channel saturation. According to another example embodiment, the BASIC comprises a smart saturation detection block and fast recovery circuit to facilitate quick start-up and immediate recovery from channel saturation. A dynamic clocked comparator checks the channel output. If it is continuously saturated for at least 160ms, the IA inputs are briefly shorted to the IA DC-bias voltage, temporarily altering the HPF characteristics. Similarly, the time constants of the offset compensation loops are also reduced. The output is recovered after only 200 ms despite the 200 mHz HPF. The system is not trigger if the channel is briefly saturated (i.e. strong QRS-complex). Once the system has detected saturation and has reset the channels, the whole saturation detection block is shut down for 1s. This is to avoid continuous resets.

**[0037]** According to an example embodiment of the invention, the instrumentation amplifier (IA) is the first one able to achieve a fully integrated 200 mHz High-Pass Filter (HPF) capable of rail-to-rail DC-offset rejection without compromising the CMRR (120dB). Finally, a configurable ADC resolution and support for external sensors such as accelerometers and temperature sensors further enable the use of the BASIC for multi-modal information acquisition.

**[0038]** While various aspects and embodiments have been disclosed herein, other aspects and embodiments will be apparent to those skilled in the art. The various aspects and embodiments disclosed herein are for purpose of illustration and are not intended to be limiting, with the true scope of the invention being indicated by the claims. It is also to be understood that the terminology used herein is for the purpose of describing particular embodiments only, and is not intended to be limiting.

## Claims

1. A system (100) for the analysis of ECG signals comprising:

at least one readout channel (110) configured to receive an analogue ECG signal (101) acquired from at least one electrode attached to a body and to extract an analogue measured ECG signal (120) and analogue electrode-skin impedance signals (121, 122);

at least one analogue-to-digital-converter ADC (130), configured to convert those extracted analogue signals at the readout channel into digital signals;

a digital adaptive filter unit (180) configured to calculate a digital motion artifact estimate (181) based on said digital signals from the analogue measured ECG signal and the analogue electrode-skin impedance signals;

**characterized in that** the system further comprises:

at least one digital-to-analogue-converter DAC (131) configured to convert said digital motion artifact estimate into an analogue motion artifact estimate signal; and  
a feedback loop (140) for sending said analogue motion artifact estimate signal back to an analogue ECG readout module (115) within the readout channel (101) configured to deduct said-analogue motion artifact estimate signal from said analogue measured ECG signal.

2. The system according to claim 1, wherein said analogue electrode-skin impedance signals comprise an analogue in-phase electrode-skin impedance signal (121), and an analogue quadrature electrode-skin impedance signal (122).
3. The system according to any preceding claim, wherein the digital adaptive filter unit (180) comprises a digital adaptive filter configured to electrode-skin impedance signals as a reference to reduce motion artifacts from the analogue measured ECG signal.

4. The system according to claim 3, wherein the digital adaptive filter comprises a LMS filter.

5. The system according to any preceding claim, wherein the readout channel further comprises a current source (125) configured to inject current into received analogue ECG signal leads to facilitate the extraction of the analogue electrode-skin impedance signals.

6. The system according to any preceding claim, wherein the analogue ECG readout module (115) comprises an analogue gain amplifier (150) configured to deduct the analogue motion artifact estimate signal from the analogue measured ECG signal.

7. The system according to claim 6, wherein the analogue gain amplifier (150) is a programmable gain amplifier configured to use a differential difference amplifier architecture to accomplish subtraction of the analogue motion artifact estimate signal from the analogue measured ECG signal.

8. The system according to claim 6, wherein the analogue gain amplifier (150) is a programmable gain amplifier which has a reference input to determine the input DC signal level and configured to apply the analogue motion artifact estimate signal to said reference input to accomplish deduction of the analogue motion artifact estimate signal from the analogue measured ECG signal.

9. The system according to any preceding claim, wherein the system comprises an analogue application-specific integrated circuit (ASIC) for performing analogue analysis and treatment of ECG signals, such as deducting said analogue motion artifact estimate signal from said analogue measured ECG signal, and a microprocessor unit for performing digital analysis and treatment of ECG signals, such as calculating a digital motion artifact estimate, said ASIC and said microprocessor unit being configured to communicate with each other.

10. The system according to claim 9, wherein the system comprises a digital interface comprising decimation filters for the at least one analogue-to-digital-converter ADC and configured to multiplex digitized outputs onto a single serial peripheral interface (SPI) output, the digital interface further comprising a secondary single SPI input for driving the at least one DAC.

11. The system according to any preceding claims, wherein the analogue-to-digital-converter ADC is a successive approximation analogue-to-digital-converter ADC.

12. Electronic device comprising a system (100) for the analysis of ECG signals according to any of claims 1 to 11.

### Patentansprüche

1. System (100) für die Analyse von EKG-Signalen, umfassend:

mindestens einen Auslesekanal (110), der dazu ausgestaltet ist, ein analoges EKG-Signal (101) zu empfangen, das von mindestens einer Elektrode erfasst wurde, die an einem Körper befestigt ist, und um ein gemessenes analoges EKG-Signal (120) und analoge Elektroden-Haut-Impedanzsignale (121, 122) zu extrahieren; mindestens einen Analog-Digital-Wandler ADC (130), der dazu ausgestaltet ist, jene an dem Auslesekanal extrahierten analogen Signale in digitale Signale umzuwandeln; eine adaptive digitale Filtereinheit (180), die dazu ausgestaltet ist, eine Schätzung digitaler Bewegungsartefakte (181) auf der Basis der digitalen Signale von dem gemessenen analogen EKG-Signal und den analogen Elektroden-Haut-Impedanzsignalen zu berechnen; **dadurch gekennzeichnet, dass** das System des Weiteren Folgendes umfasst:

mindestens einen Digital-Analog-Wandler DAC (131), der dazu ausgestaltet ist, die Schätzung digitaler Bewegungsartefakte in ein analoges Signal der Schätzung der Bewegungsartefakte umzuwandeln; und eine Rückkopplungsschleife (140) zum Senden des analogen Signals der Schätzung der Bewegungsartefakte zurück an ein analoges EKG-Auslesemodul (115) innerhalb des Auslesekanals (101), das dazu ausgestaltet ist, das analoge Signal der Schätzung der Bewegungsartefakte aus dem gemessenen analogen EKG-Signal abzuleiten.

2. System nach Anspruch 1, wobei die analogen Elektroden-Haut-Impedanzsignale ein phasengleiches analoges Elektroden-Haut-Impedanzsignal (121) und ein analoges Quadratur-Elektroden-Haut-Impedanzsignal (122) umfassen.
3. System nach einem vorhergehenden Anspruch, wobei die adaptive digitale Filtereinheit (180) ein adaptives digitales Filter umfasst, das dazu ausgestaltet ist, Elektroden-Haut-Impedanzsignale als eine Referenz zu verwenden, um Bewegungsartefakte aus dem gemessenen analogen EKG-Signal zu reduzieren.

4. System nach Anspruch 3, wobei das adaptive digitale Filter ein LMS-Filter umfasst.

5. System nach einem vorhergehenden Anspruch, wobei der Auslesekanal des Weiteren eine Stromquelle (125) umfasst, die dazu ausgestaltet ist, Strom in empfangene analoge EKG-Signalleitungen einzuführen, um die Extrahierung der analogen Elektroden-Haut-Impedanzsignale zu erleichtern.

6. System nach einem vorhergehenden Anspruch, wobei das analoge EKG-Auslesemodul (115) einen analogen Gain-Verstärker (150) umfasst, der dazu ausgestaltet ist, das analoge Signal der Schätzung der Bewegungsartefakte aus dem gemessenen analogen EKG-Signal auszulesen.

7. System nach Anspruch 6, wobei der analoge Gain-Verstärker (150) ein programmierbarer Gain-Verstärker ist, der dazu ausgestaltet ist, eine Differential-Differenzverstärkerarchitektur zu verwenden, um die Subtraktion des analogen Signals der Schätzung der Bewegungsartefakte aus dem gemessenen analogen EKG-Signal vorzunehmen.

8. System nach Anspruch 6, wobei der analoge Gain-Verstärker (150) ein programmierbarer Gain-Verstärker ist, der einen Referenzeingang hat, um den DC-Eingangssignalpegel zu bestimmen, und der dazu ausgestaltet ist, das analoge Signal der Schätzung der Bewegungsartefakte auf den Referenzeingang anzuwenden, um die Deduktion des analogen Signals der Schätzung der Bewegungsartefakte von dem analogen gemessenen EKG-Signal vorzunehmen.

9. System nach einem vorhergehenden Anspruch, wobei das System eine anwendungsspezifische integrierte analoge Schaltung (ASIC) zum Durchführen analoger Analyse und Verarbeitung von EKG-Signalen, wie beispielsweise Abziehen des analogen Signals der Schätzung der Bewegungsartefakte von dem gemessenen analogen EKG-Signal, und eine Mikroprozessoreinheit zum Durchführen digitaler Analyse und Verarbeitung von EKG-Signalen umfasst, wie beispielsweise Berechnen einer Schätzung von digitalen Bewegungsartefakten, wobei die ASIC und die Mikroprozessoreinheit dazu ausgestaltet sind, miteinander zu kommunizieren.

10. System nach Anspruch 9, wobei das System eine digitale Schnittstelle umfasst, die Dezimierungsfilter für den mindestens einen Analog-Digital-Wandler ADC umfasst und die dazu ausgestaltet ist, digitalisierte Ausgänge auf einem einzelnen seriellen-peripheren Schnittstellen (SPI)-Ausgang zu multiplexen, wobei die digitale Schnittstelle des Weiteren einen sekundären einzelnen SPI-Eingang zum Trei-

ben des mindestens einen DAC umfasst.

11. System nach einem vorhergehenden Anspruch, wobei der Analog-Digital-Wandler ADC ein Analog-Digital-Wandler ADC sukzessiver Annäherung ist. 5
12. Elektronische Vorrichtung, umfassend ein System (100) für die Analyse von EKG-Signalen gemäß einem der Ansprüche 1 bis 11.

### Revendications

1. Système (100) pour l'analyse de signaux d'ECG comprenant :

au moins un canal de lecture (110) configuré pour recevoir un signal d'ECG analogique (101) acquis à partir d'au moins une électrode attachée à un corps et pour extraire un signal d'ECG analogique mesuré (120) et des signaux d'impédance électrode-peau analogiques (121, 122) ;

au moins un convertisseur analogique-numérique ADC (130) configuré pour convertir les signaux analogiques extraits du canal de lecture en des signaux numériques ;

une unité de filtre adaptatif numérique (180) configurée pour calculer une estimation d'artéfact de mouvement numérique (181) sur la base desdits signaux numériques provenant du signal d'ECG analogique mesuré et des signaux d'impédance électrode-peau analogiques ;

**caractérisé en ce que** le système comprend en outre :

au moins un convertisseur numérique-analogique DAC (131) configuré pour convertir ladite estimation d'artéfact de mouvement numérique en un signal d'estimation d'artéfact de mouvement analogique ; et

une boucle de rétroaction (140) pour renvoyer ledit signal d'estimation d'artéfact de mouvement analogique à un module de lecture d'ECG analogique (115) dans le canal de lecture (101) configuré pour soustraire ledit signal d'estimation d'artéfact de mouvement analogique dudit signal d'ECG analogique mesuré.

2. Système selon la revendication 1, dans lequel lesdits signaux d'impédance électrode-peau analogiques comprennent un signal d'impédance électrode-peau en phase analogique (121), et un signal d'impédance électrode-peau en quadrature analogique (122). 55
3. Système selon l'une quelconque des revendications précédentes, dans lequel l'unité de filtre adaptatif

numérique (180) comprend un filtre adaptatif numérique configuré pour utiliser les signaux d'impédance électrode-peau en tant que référence pour réduire les artéfacts de mouvement du signal d'ECG analogique mesuré.

4. Système selon la revendication 3, dans lequel le filtre adaptatif numérique comprend un filtre LMS.
- 10 5. Système selon l'une quelconque des revendications précédentes, dans lequel le canal de lecture comprend en outre une source de courant (125) configurée pour injecter un courant dans les conducteurs de signaux d'ECG analogiques reçus pour faciliter l'extraction des signaux d'impédance électrode-peau analogiques.
- 15 6. Système selon l'une quelconque des revendications précédentes, dans lequel le module de lecture d'ECG analogique (115) comprend un amplificateur analogique à gain (150) configuré pour soustraire le signal d'estimation d'artéfact de mouvement analogique du signal d'ECG analogique mesuré.
- 20 7. Système selon la revendication 6, dans lequel l'amplificateur analogique à gain (150) est un amplificateur à gain programmable configuré pour utiliser une architecture d'amplificateur différentiel pour effectuer la soustraction du signal d'estimation d'artéfact de mouvement analogique du signal d'ECG analogique mesuré.
- 25 30 8. Système selon la revendication 6, dans lequel l'amplificateur analogique à gain (150) est un amplificateur à gain programmable qui a une entrée de référence pour déterminer le niveau de signal continu d'entrée et configuré pour appliquer le signal d'estimation d'artéfact de mouvement analogique à ladite entrée de référence pour effectuer la soustraction du signal d'estimation d'artéfact de mouvement analogique du signal d'ECG analogique mesuré.
- 35 40 9. Système selon l'une quelconque des revendications précédentes, dans lequel le système comprend un circuit intégré spécifique à une application (ASIC) pour effectuer une analyse et un traitement analogiques des signaux d'ECG, tels que la soustraction dudit signal d'estimation d'artéfact de mouvement analogique dudit signal d'ECG analogique mesuré, et une unité de microprocesseur pour effectuer une analyse et un traitement numériques des signaux d'ECG, tels que le calcul d'une estimation d'artéfact de mouvement numérique, ledit ASIC et ladite unité de microprocesseur étant configurés pour communiquer l'un avec l'autre.
- 45 50 10. Système selon la revendication 9, dans lequel le système comprend une interface numérique compre-

nant des filtres décimateurs pour ledit au moins un convertisseur analogique-numérique ADC et configurée pour multiplexer les sorties numérisées sur une sortie d'interface de périphérique série (SPI) unique, l'interface numérique comprenant en outre une entrée de SPI unique secondaire pour commander ledit au moins un convertisseur numérique-analogique. 5

11. Système selon l'une quelconque des revendications précédentes, dans lequel le convertisseur analogique-numérique ADC est un convertisseur analogique-numérique ADC à approximations successives. 10

12. Dispositif électronique comprenant un système (100) pour l'analyse de signaux d'ECG selon l'une quelconque des revendications 1 à 11. 15

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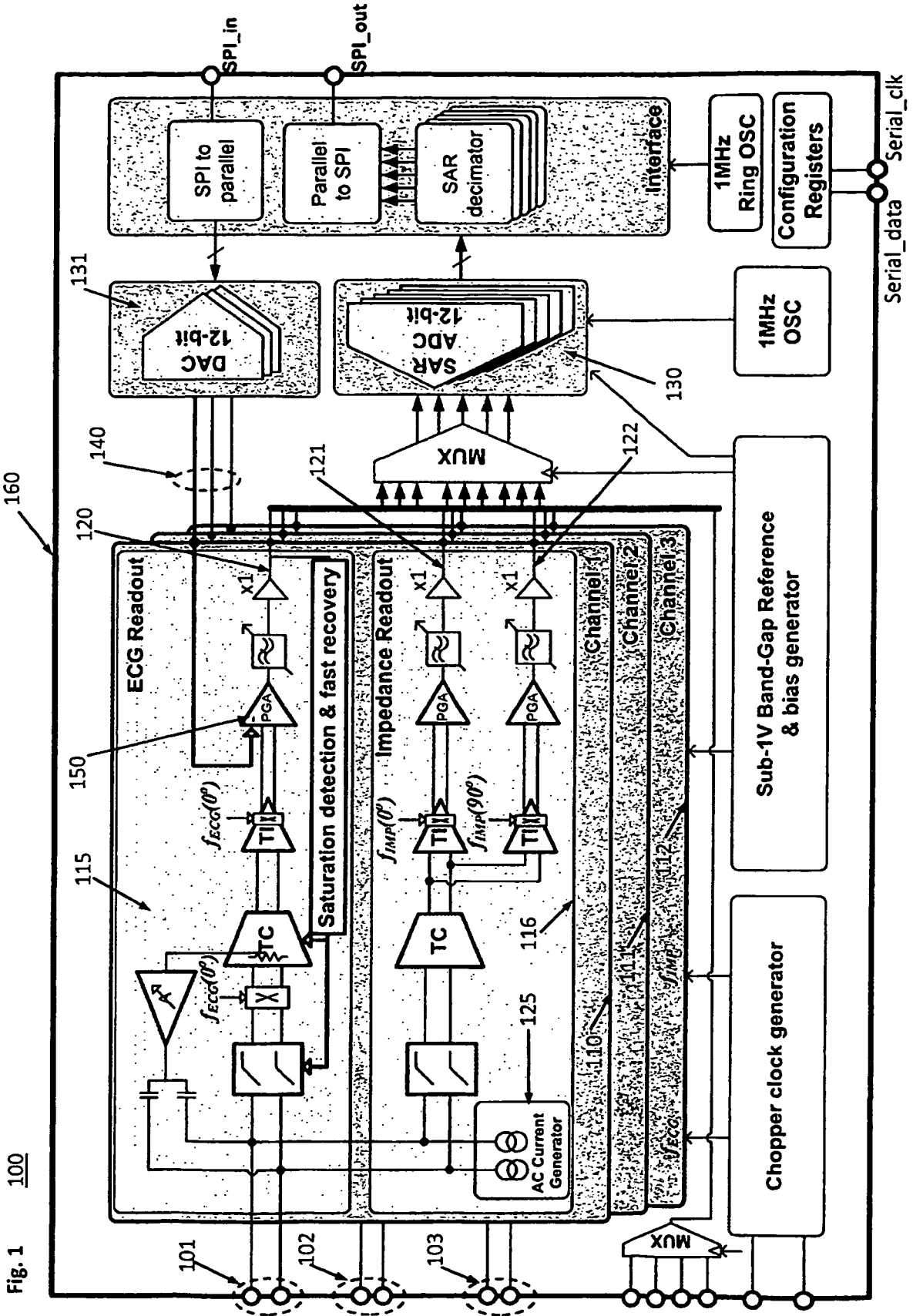
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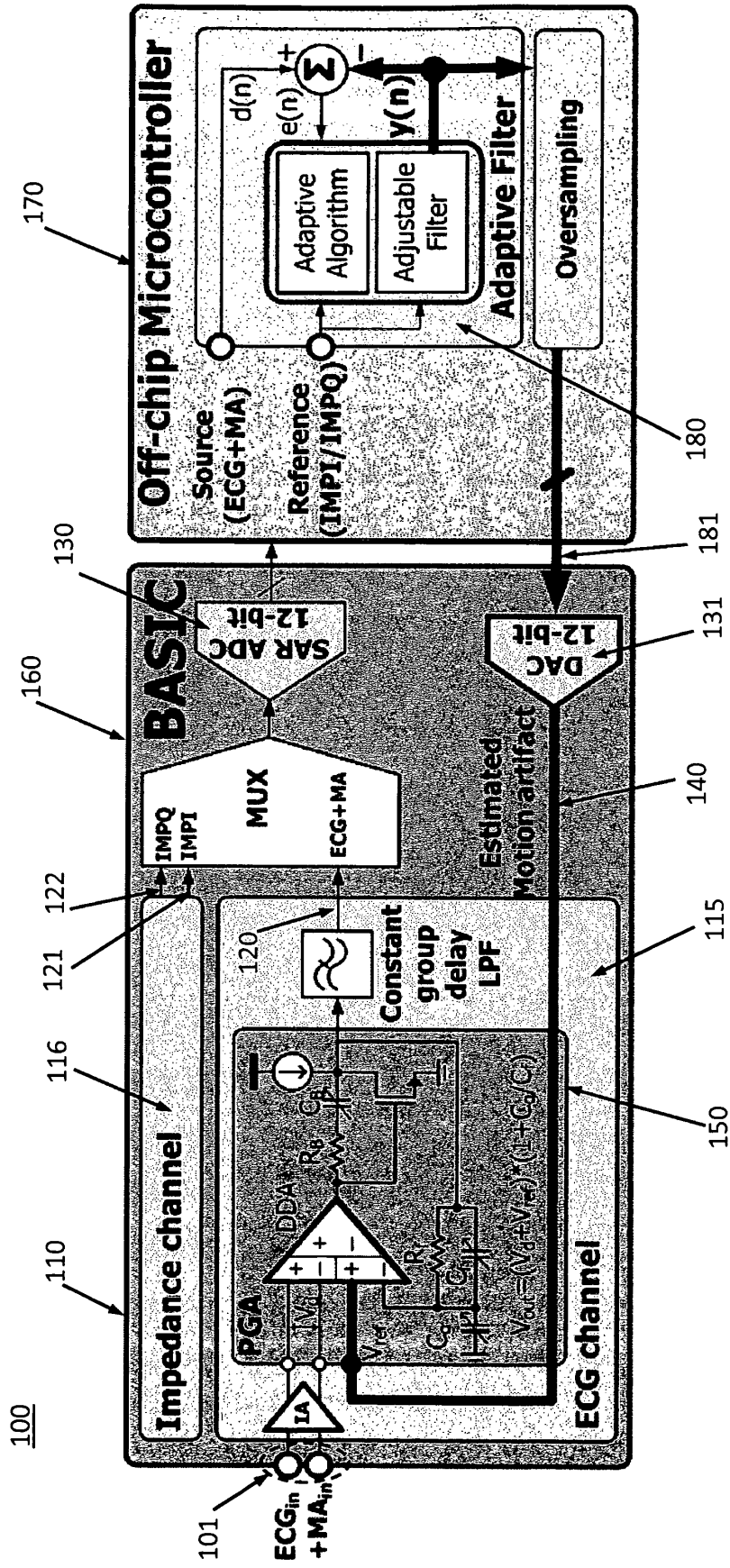


Fig. 2

100

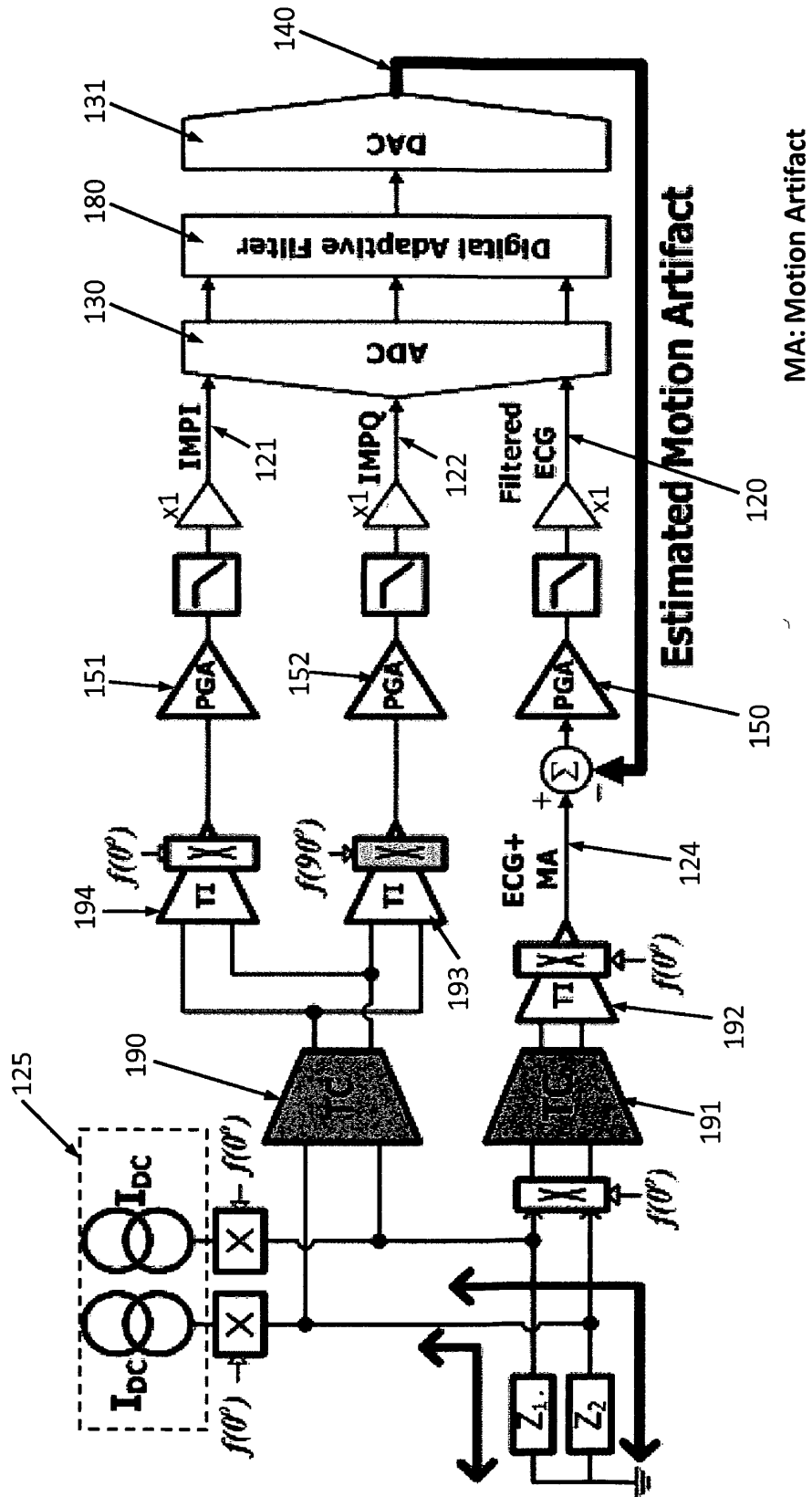


Fig. 3

**REFERENCES CITED IN THE DESCRIPTION**

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**Patent documents cited in the description**

- EP 2298164 A [0003]

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摘要(译)

一种用于分析ECG信号的系统 (100)，包括：至少一个读出通道 (110)，被配置为接收从附接到身体的至少一个电极获取的模拟ECG信号 (101)；并提取模拟测量的ECG信号 (120) 和模拟电极 - 皮肤阻抗信号 (121,122)；至少一个ADC (130)，用于将读出通道上提取的模拟信号转换成数字信号；数字自适应滤波器单元 (180)，被配置为基于所测量的ECG信号和电极 - 皮肤阻抗信号的所述数字版本来计算数字运动伪影估计 (181)；至少一个DAC (131)，用于将所述数字运动伪影估计转换为模拟信号；反馈回路 (140)，用于将所述模拟运动伪影估计信号发送回读出通道 (101)，所述读出通道 (101) 被配置为从所述模拟测量的ECG信号中减去所述模拟运动伪影估计信号。

