

(19) (KR)
(12) (B1)

(51) Int. Cl. ⁷ H05B 33/26	(45) (11) (24)	2004 04 03 10-0426031 2004 03 24
(21) 10-2001-0088537 (22) 2001 12 29	(65) (43)	10-2003-0058150 2003 07 07

(73)
20

(72) 가 307-801

1494-6

(74)
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(54)

가
가
가

, 7 8

가

가

1		
2		
3a	3g	
4		
5		1
6a	6f	
7		2
8a	8f	

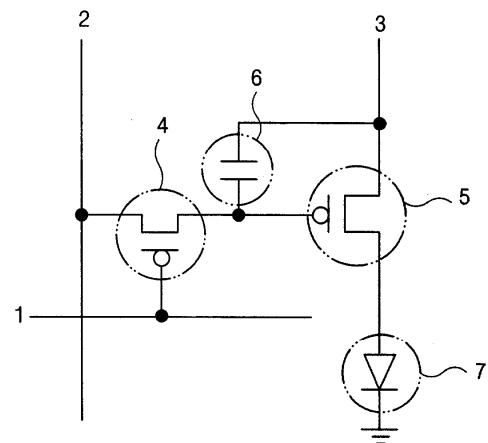
<				>
110 :	111, 114 :			
112, 116 :		113, 115 :		
120 :		121, 122 :		
130 :	1		140 :	2
151 :		152 :		
160 :	1	161, 162, 163, 164, 165 :		
171 :	1		172 :	2
173 :	2		180 :	2
181 :				

가 (cathode ray tube : CRT)
 가 (flat panel display)
 가 (liquid crystal display)
 ay) (plasma display panel), (field emission display),
 (: electroluminescence display(ELD)) 가

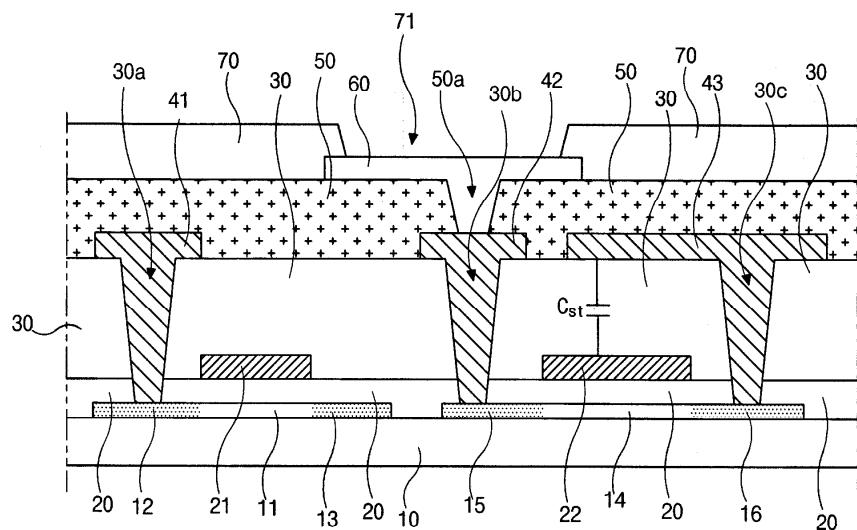
, (electroluminescence : EL)
(inorganic)

LED (Organic light emitting diode : OLED)
 D
 가
 LED : AMOLED)
 1 LED
 (switching) (4) (driving) (5),
 (ge capacitor)(6), (7) (4) (2)
 (5) p (1) (5) (2)
 (4) (4) (7) (anode) (7) (cathode)
 (5) (5) (power line)(3) (6) 가 (5) (7) (cathode)

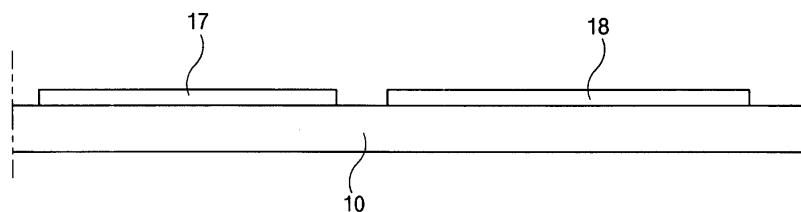
1



2

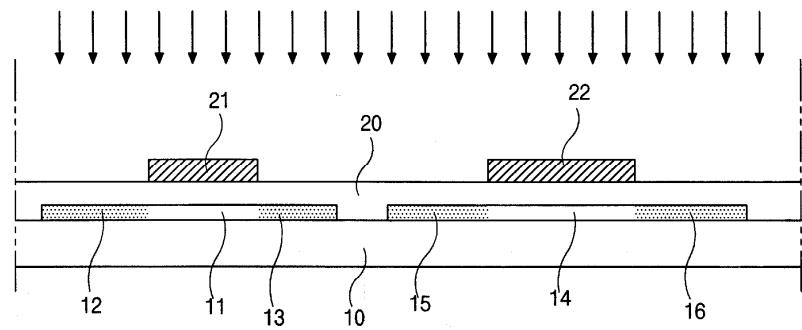


3a

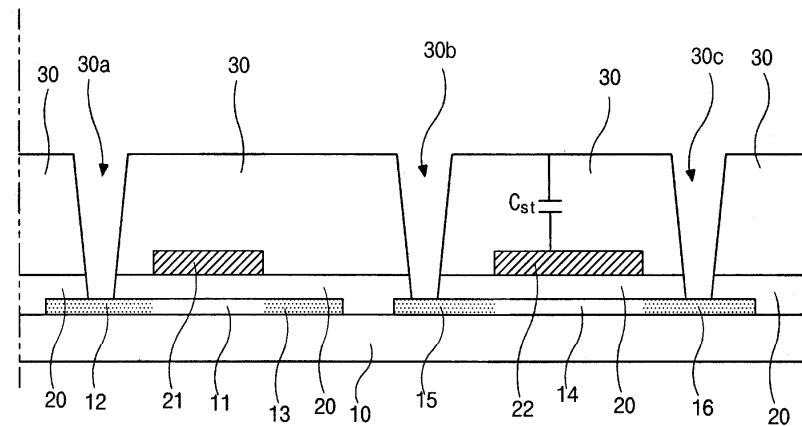


3b

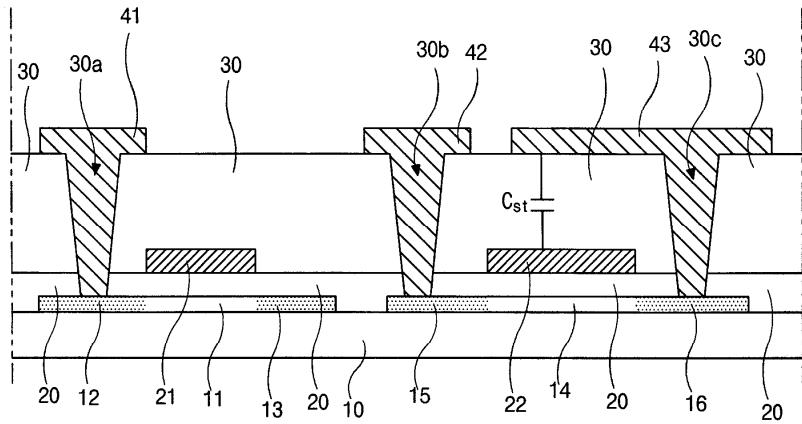
이온주입



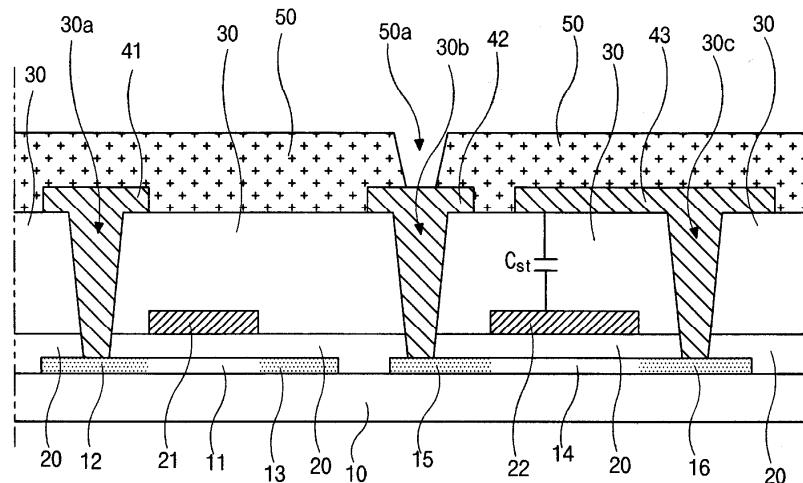
3c



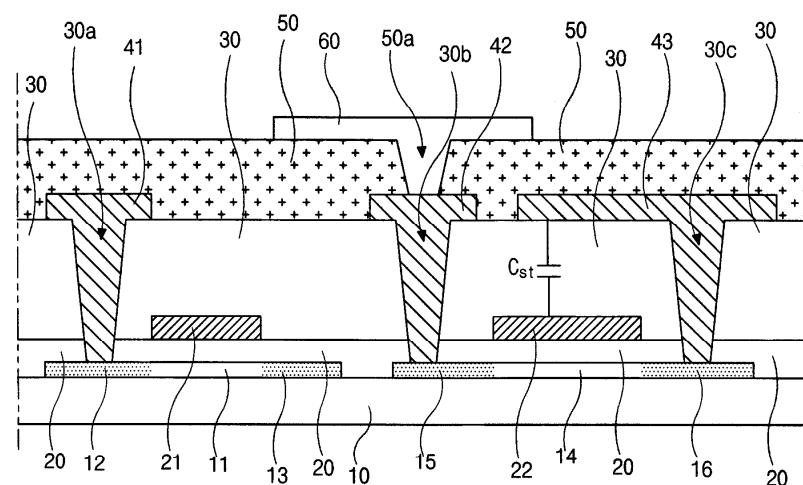
3d



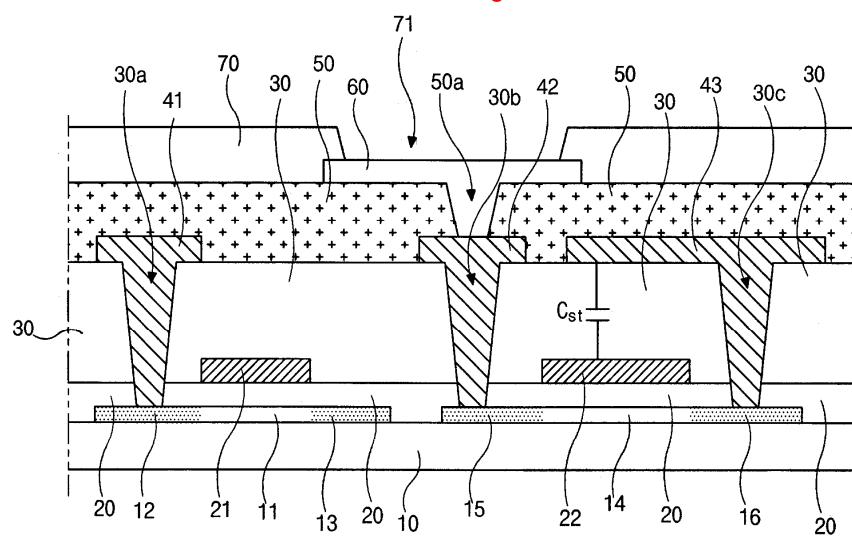
3e



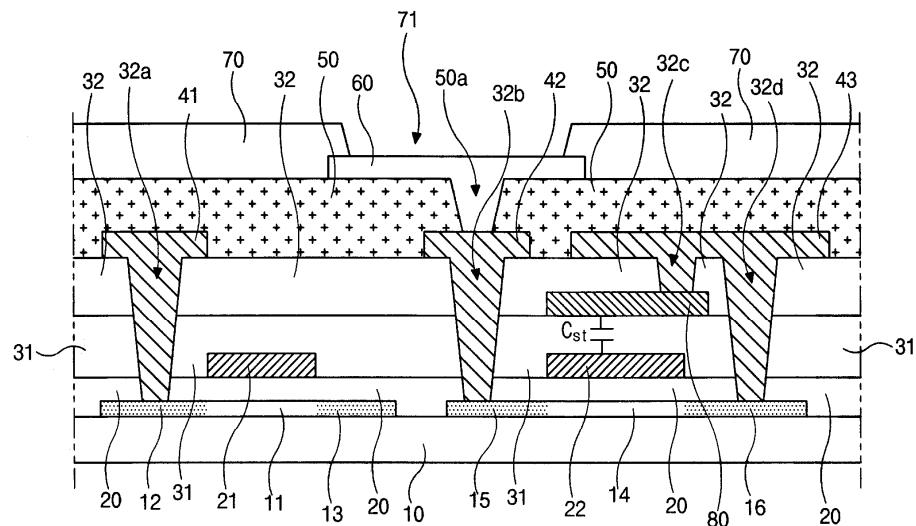
3f



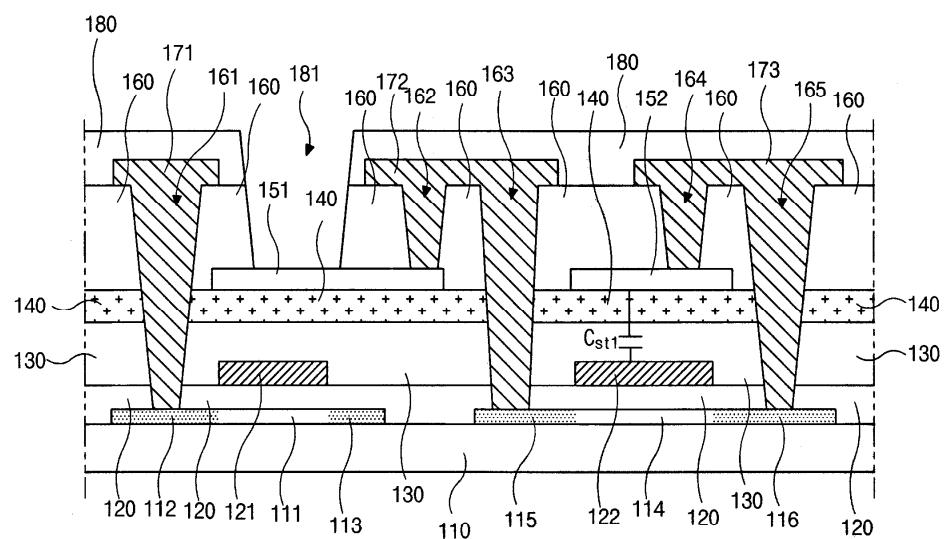
3g



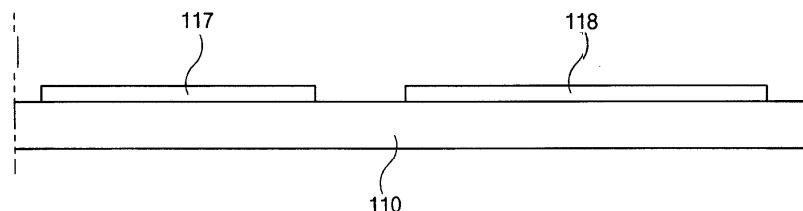
4

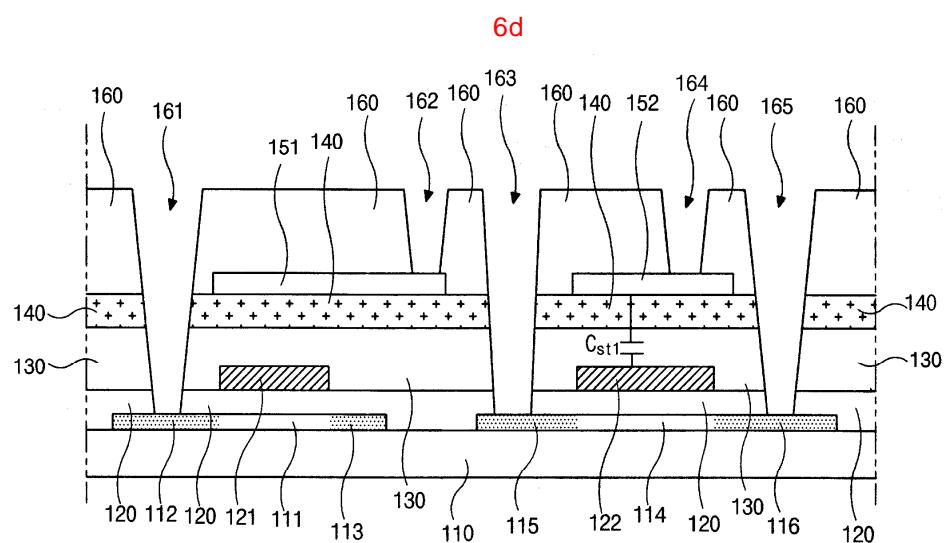
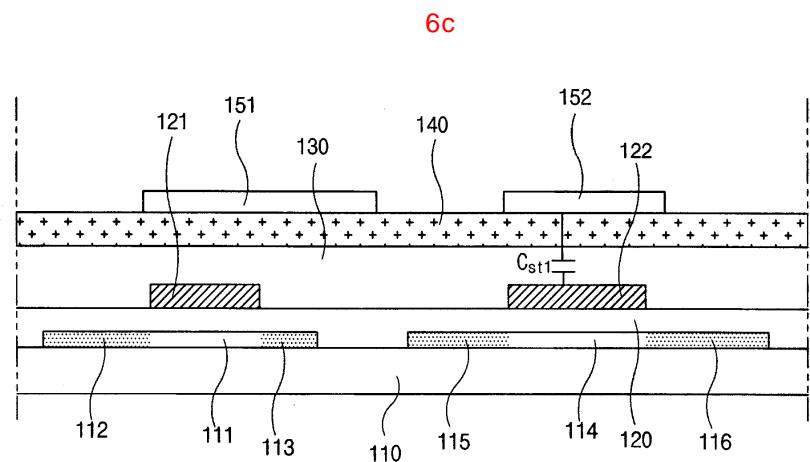
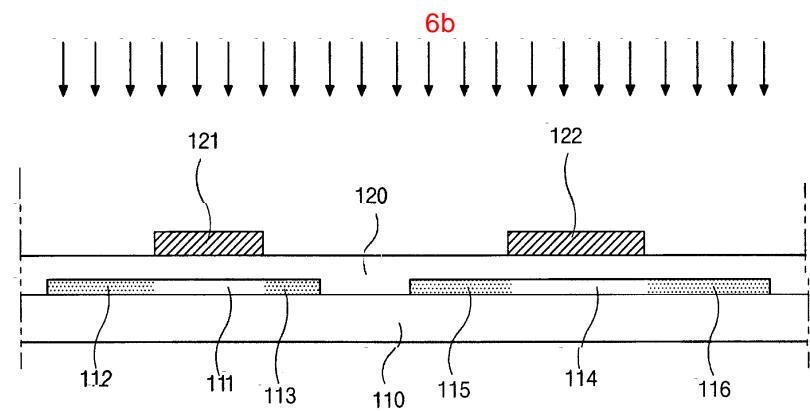


5

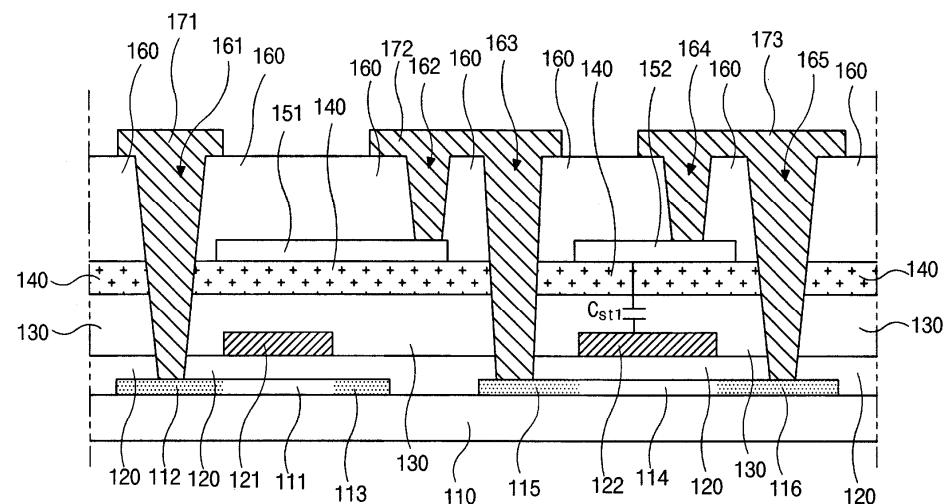


6a

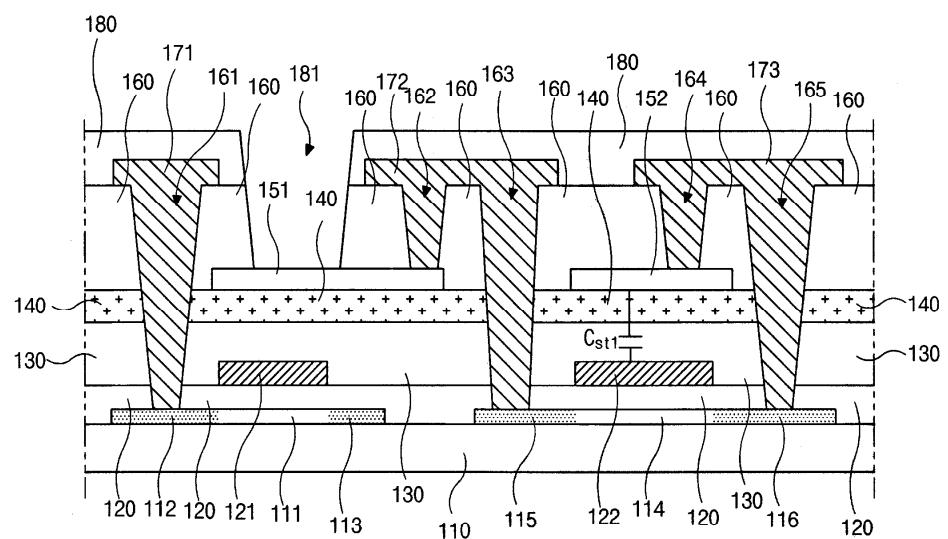




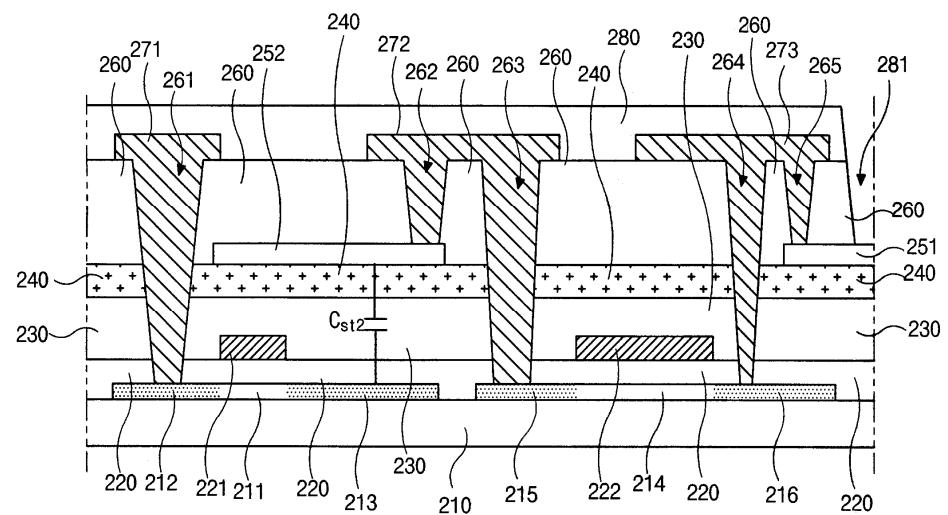
6e



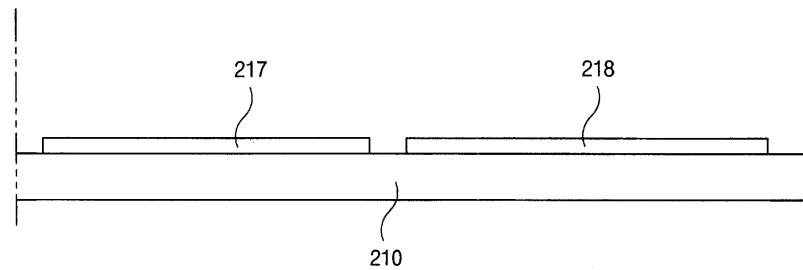
6f



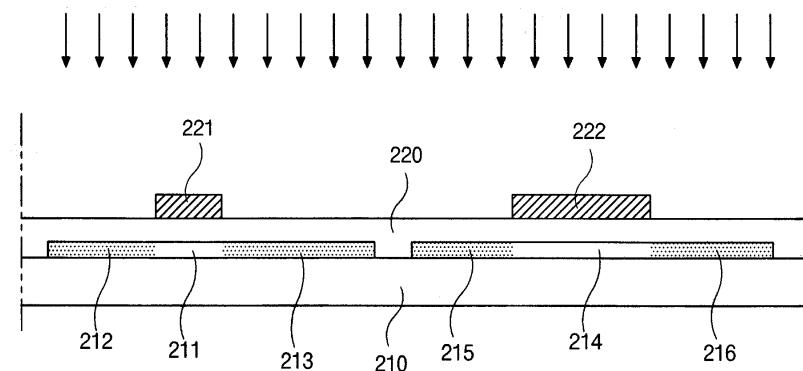
7



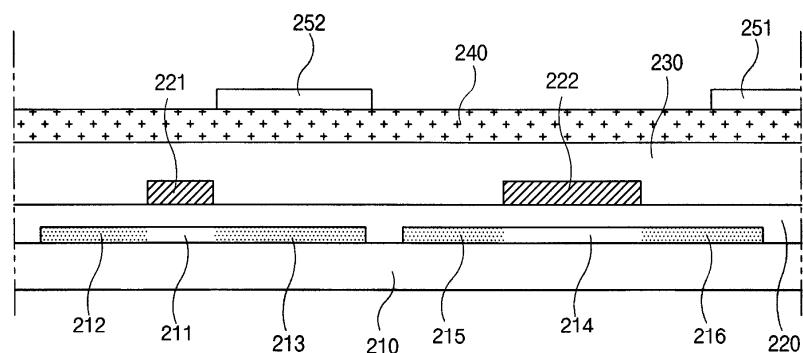
8a



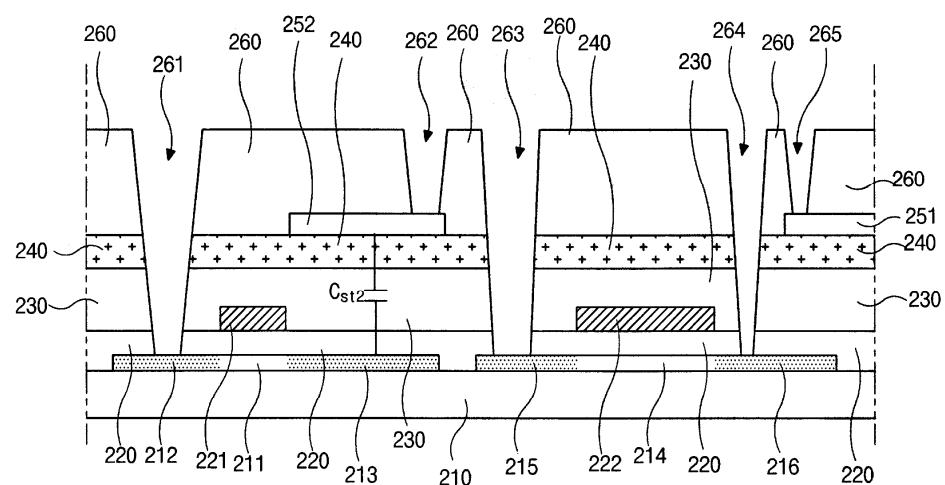
8b

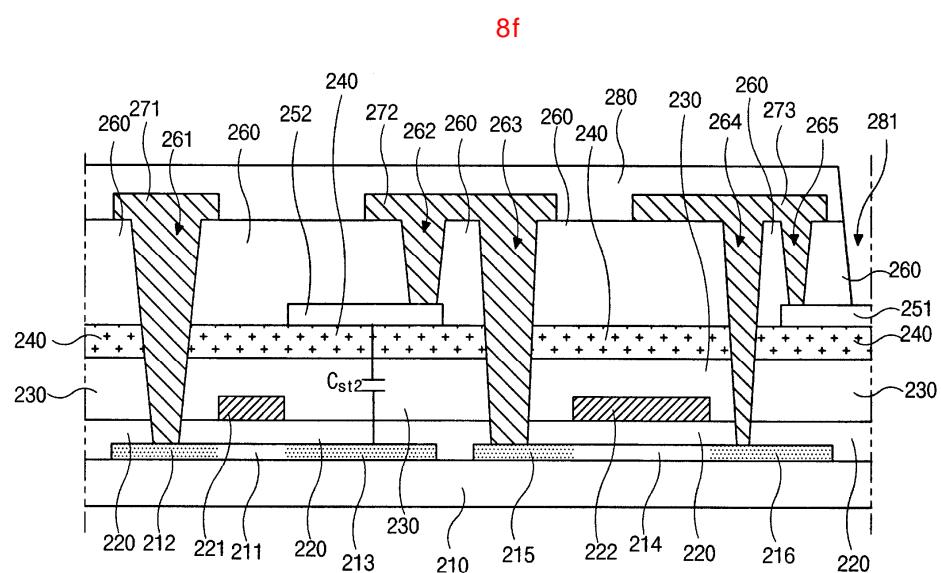
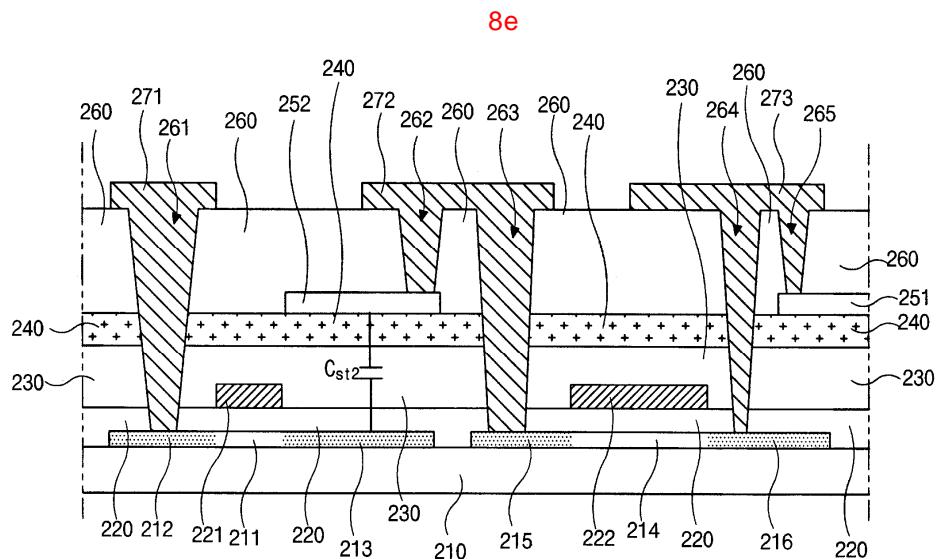


8c



8d





专利名称(译)	有源矩阵有机电致发光器件及其制造方法		
公开(公告)号	KR100426031B1	公开(公告)日	2004-04-03
申请号	KR1020010088537	申请日	2001-12-29
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG显示器有限公司		
当前申请(专利权)人(译)	LG显示器有限公司		
[标]发明人	PARK JAEYONG 박재용 YOO JUHNSUK 유준석		
发明人	박재용 유준석		
IPC分类号	H05B33/26 H01L27/32		
CPC分类号	H01L27/3265 H01L27/3258 H01L27/1255		
代理人(译)	贞媛KI		
其他公开文献	KR1020030058150A		
外部链接	Espacenet		

摘要(译)

一种有源矩阵有机电致发光显示 (ELD) 器件，包括衬底，在衬底上由多晶硅形成的第一和第二有源层，第一源区和漏区以及第二源区和漏区，与第一有源区相邻的第一源区和漏区层和与第二有源层相邻的第二源区和漏区，第一和第二有源层上的栅极绝缘层，栅极绝缘层上的第一和第二栅电极，第一和第二栅电极上的第一中间层，第一夹层上的阳极和电容器电极，阳极和电容器电极上的第一钝化层，第一钝化层上的电源线，第一钝化层上的第一源和漏电极，第一源电极是连接到第一源区和第一漏电极连接到第一漏区，第二源区和漏区第一钝化层上的电极，第二源极连接第二源极区，电源线和电容器电极以及第二漏极连接第二漏极区和阳极，第二钝化层连接第二漏极层第一源极和漏极以及第二源极和漏极，第二钝化层具有暴露阳极的堤。

