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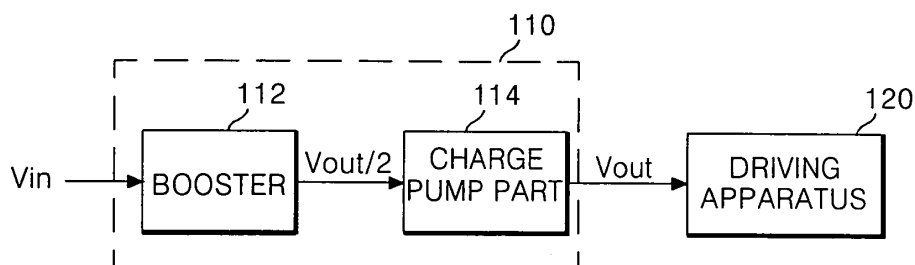
(54) **Power supply and driving method thereof and apparatus and method for driving electroluminescence display device using the same**

(57) The present invention relates to a power supply that is adaptive for improving energy conversion efficiency and a driving method thereof and an apparatus and method for driving an electroluminescence display device using the same.

A power supply that supplies a driving power to

drive a driving apparatus according to an embodiment of the present invention includes a power source to supply DC power; and a DC-DC converter to convert the DC power from the power source to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power, and then to convert the converted DC power of $1/N$ times to a level of the driving power.

FIG.2



Description

[0001] This application claims the benefit of the Korean Patent Application No. P2003-52979 filed on July 31, 2003, which is hereby incorporated by reference.

BACKGROUND OF THE INVENTION

Field of the Invention

[0002] The present invention relates to a power supply and a driving method thereof and an apparatus and method for driving an electroluminescence display device using the same, and more particularly to a power supply that is adaptive for improving energy conversion efficiency and a driving method thereof and an apparatus and method for driving an electroluminescence display device using the same.

Description of the Related Art

[0003] Recently, the application of a power supply becomes more activated in factory automation equipments, office automation equipments, information devices, communication devices and power system, wherein the power supply is stable and capable of enabling them to be made smaller and lighter.

[0004] Generally, a switching power supply has two main parts of a rectifier to convert AC input into DC and a DC-DC converter to stabilize the DC input therefrom in regard to load fluctuation and the change of input voltage. A capacity input type of rectifying circuit, which is mainly used as a DC supply for various electronic devices, needs a capacitor of big capacity in order to reduce the burden of the device by restraining the input voltage fluctuation of the DC-DC converter.

[0005] Referring to FIG. 1, a prior art power supply and a driving apparatus using the same includes a voltage boosting type of DC-DC converter 10 to receive DC power source V_{in} for boosting it to a desired output voltage and a driving apparatus 20 to receive the boosted output voltage from the DC-DC converter 10 for being driven.

[0006] The voltage boosting type of DC-DC converter 10 controls the on/off ratio of a switching device (not shown) to boost the voltage of the DC power source V_{in} , thereby outputting the boosted DC power source V_{in} . At this moment, the voltage boosting type of DC-DC converter 10 uses a pulse width modulation method in case that the output current is high and a pulse frequency modulation method in case that the output current is low, in consideration of energy conversion efficiency.

[0007] Generally, a method by the voltage boosting type of DC-DC converter 10 has a disadvantage that it is not easy to be realized in real because the on/off ratio should be controlled on a large scale whether it is the pulse width modulation method or the pulse frequency modulation method in order to boost the output voltage

more than three or four times in comparison with the input voltage. Further, the on/off ratio cannot be set to be more than 80% at maximum due to the energy conversion efficiency. Accordingly, the difference between the input voltage and the output voltage of the DC-DC converter 10 is around 5 times in design, but it is 4 times at maximum in real.

[0008] On the other hand, a DC-DC converter 10 that boosts voltage in a charge pump method where several capacitors are multitier-configured, is easy to be controlled arbitrarily in accordance with the design of the output voltage, but has many limits in output current. That is, in the charge pump type of DC-DC converter 10, the efficiency of the first tier is more than 90% in output, but the efficiency rapidly falls as it goes through several tiers.

SUMMARY OF THE INVENTION

[0009] Accordingly, it is an object of the present invention to provide a power supply that is adaptive for improving energy conversion efficiency and a driving method thereof and an apparatus and method for driving an electroluminescence display device using the same.

[0010] In order to achieve these and other objects of the invention, a power supply that supplies a driving power to drive a driving apparatus according to an aspect of the present invention includes a power source to supply a DC power; and a DC-DC converter to convert the DC power from the power source to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power.

[0011] The power supply further includes a square wave signal supplier to supply a square wave signal to the DC-DC converter; and a DC-DC controller that generates a second switching control signal for controlling the square wave signal supplier and in addition generates a first switching control signal synchronized with the second switching control signal.

[0012] In the power supply, the DC-DC converter includes a booster that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a switching device which is switched in response to the first switching control signal and outputs the converted DC power to a first output line; and a charge pump part that converts $1/N$ times the DC power from the first output line to a level of the driving power by use of the square wave signal supplied from the square wave signal supplier in response to the second switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

[0013] In the power supply, the booster includes an inductor and a first diode connected between the power source and the first output line; a first capacitor connected between a ground voltage source and a node that is between the power source and the inductor; a second capacitor connected between the first output line and

the square wave signal supplier; and a switching device connected between the ground voltage source and a node that is between the first diode and the inductor to switch an energy stored at the inductor to the first output line as well as switching an energy from the power source to the inductor in response to the first switching control signal from the DC-DC controller.

[0014] In the power supply, the square wave signal supplier includes a high voltage source to supply high voltage; and a switching device that converts the high voltage from the high voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

[0015] In the power supply, the charge pump part includes the second capacitor; a second diode connected between the first output line and the second output line; and a third capacitor connected between the ground voltage source and a node that is between the second diode and the second output line.

[0016] In the power supply, the DC-DC converter includes a charge pump part that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a square wave signal supplied from the square wave signal supplier in response to the second switching control signal and outputs the converted DC power to a first output line; and a booster that converts $1/N$ times the DC power supplied from the first output line to a level of the driving power by use of a switching device that is switched in response to the first switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

[0017] In the power supply, the charge pump part includes a first diode and a second diode connected between the power source and the first output line; a first capacitor connected between a ground voltage source and a node that is between the power source and the first diode; and a second capacitor connected between the square wave signal supplier and a node that is between the first and second diodes.

[0018] In the power supply, the square wave signal supplier includes a low voltage source to supply low voltage; and a switching device that converts the low voltage from the low voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

[0019] In the power supply, the booster includes an inductor and a third diode connected between the first output line and the second output line; a third capacitor connected between a ground voltage source and a node that is between the first output line and the inductor; a fourth capacitor connected between the ground voltage source and a node that is between the third diode and the second output line; and a switching device connected between the ground voltage source and a node that is between the inductor and the third diode to switch an

energy stored at the inductor to the second output line as well as switching an energy from the input line to the inductor in response to the first switching control signal from the DC-DC controller.

[0020] A driving apparatus of an electroluminescence display device according to another aspect of the present invention includes an electroluminescence display device to display a picture; a power source to supply DC power; and a DC-DC converter to convert the DC power from the power source to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power for driving the electroluminescence display device, and then to convert the converted DC power of $1/N$ times to a level of the driving power.

[0021] The driving apparatus further includes a square wave signal supplier to supply a square wave signal to the DC-DC converter; and a DC-DC controller that generates a second switching control signal for controlling the square wave signal supplier and in addition generates a first switching control signal synchronized with the second switching control signal.

[0022] In the driving apparatus, the DC-DC converter includes a booster that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a switching device which is switched in response to the first switching control signal and outputs the converted DC power to a first output line; and a charge pump part that converts $1/N$ times the DC power from the first output line to a level of the driving power by use of the square wave signal supplied from the square wave signal supplier in response to the second switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

[0023] In the driving apparatus, the booster includes an inductor and a first diode connected between the power source and the first output line; a first capacitor connected between a ground voltage source and a node that is between the power source and the inductor; a second capacitor connected between the first output line and the square wave signal supplier; and a switching device connected between the ground voltage source and a node that is between the first diode and the inductor to switch an energy stored at the inductor to the first output line as well as switching an energy from the power source to the inductor in response to the first switching control signal from the DC-DC controller.

[0024] In the driving apparatus, the square wave signal supplier includes a high voltage source to supply high voltage; and a switching device that converts the high voltage from the high voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

[0025] In the driving apparatus, the charge pump part includes the second capacitor; a second diode connected between the first output line and the second output line; and a third capacitor connected between the

ground voltage source and a node that is between the second diode and the second output line.

[0026] In the driving apparatus, the DC-DC converter includes a charge pump part that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a square wave signal supplied from the square wave signal supplier in response to the second switching control signal and outputs the converted DC power to a first output line; and a booster that converts $1/N$ times the DC power supplied from the first output line to a level of the driving power by use of a switching device that is switched in response to the first switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

[0027] In the driving apparatus, the charge pump part includes a first diode and a second diode connected between the power source and the first output line; a first capacitor connected between a ground voltage source and a node that is between the power source and the first diode; and a second capacitor connected between the square wave signal supplier and a node that is between the first and second diodes.

[0028] In the driving apparatus, the square wave signal supplier includes a low voltage source to supply low voltage; and a switching device that converts the low voltage from the low voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

[0029] In the driving apparatus, the booster includes an inductor and a third diode connected between the first output line and the second output line; a third capacitor connected between a ground voltage source and a node that is between the first output line and the inductor; a fourth capacitor connected between the ground voltage source and a node that is between the third diode and the second output line; and a switching device connected between the ground voltage source and a node that is between the inductor and the third diode to switch an energy stored at the inductor to the second output line as well as switching an energy from the input line to the inductor in response to the first switching control signal from the DC-DC controller.

[0030] A driving method of a power supply that supplies a driving power to drive a driving apparatus according to still another aspect of the present invention includes a first step of generating a DC power; a second step of converting the DC power into a voltage of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power and outputting the converted driving power to a first output line; and a third step of converting the converted voltage of $1/N$ times the driving power to a level of the driving power and supplying the converted voltage to the driving apparatus through a second output line.

[0031] The driving method further includes a fourth step of generating a first switching control signal to

switch a switching device; a fifth step of generating a second switching control signal synchronized with the first switching control signal; and a sixth step of converting a voltage from a voltage source into a square wave signal in accordance with the second switching control signal.

[0032] In the driving method, the second step includes the steps of: storing the DC power at an inductor and supplying the stored energy to the first output line through a first diode connected between the inductor and the first output line by switching the switching device in accordance with the first switching control signal; supplying the square wave signal generated in accordance with the second switching control signal to a first capacitor connected to the first output line, and supplying the voltage stored at the first capacitor to the first output line; and generating a voltage of $1/N$ times the driving power by use of an energy from the inductor and a voltage from the first capacitor supplied to the first output line.

[0033] In the driving method, the third step includes the steps of: storing the voltage on the first output line to a second capacitor connected between the second output line and a ground voltage source through a second diode connected between the first and second output lines; and generating the driving power by use of the voltage on the first output line supplied through the second diode and the voltage stored at the second capacitor and supplying the generated driving power to the driving apparatus through the second output line.

[0034] In the driving method, the second step includes the steps of: storing the square wave signal generated in accordance with the second switching control signal to a first capacitor connected to the first output line; supplying the DC power to the first output line through a first diode connected between an input line of the DC power and the first output line; and generating a voltage of $1/N$ times the driving power by use of a voltage stored at the first capacitor and the DC power supplied through the first diode, and supplying the generated voltage of $1/N$ times the driving power to the first output line through a second diode connected between the first output line and the first capacitor.

[0035] In the driving method, the third step includes the steps of: storing the voltage of $1/N$ times supplied from the first output line and storing the stored energy at a second capacitor connected between the second output line and a ground voltage source; and generating the driving power by use of the voltage stored at the second capacitor and the voltage from the first output line by switching the switching device in accordance with the first switching control signal and supplying the generated driving power to the driving apparatus.

[0036] A method for driving an electroluminescence display device to display a picture by use of a driving power from a power supply according to still another aspect of the present invention includes a first step of generating a DC power; a second step of converting the DC power into a voltage of $1/N$ (provided that N is a positive

integer of not less than 2) times the driving power and outputting the converted driving power to a first output line; and a third step of converting the converted voltage of $1/N$ times the driving power to a level of the driving power and supplying the converted voltage to the electroluminescence display device through a second output line.

[0037] The method further includes a fourth step of generating a first switching control signal to switch a switching device; a fifth step of generating a second switching control signal synchronized with the first switching control signal; and a sixth step of converting a voltage from a voltage source into a square wave signal in accordance with the second switching control signal.

[0038] In the method, the second step includes the steps of: storing the DC power at an inductor and supplying the stored energy to the first output line through a first diode connected between the inductor and the first output line by switching the switching device in accordance with the first switching control signal; supplying the square wave signal generated in accordance with the second switching control signal to a first capacitor connected to the first output line, and supplying the voltage stored at the first capacitor to the first output line; and generating a voltage of $1/N$ times the driving power by use of an energy from the inductor and a voltage from the first capacitor supplied to the first output line.

[0039] In the method, the third step includes the steps of: storing the voltage on the first output line to a second capacitor connected between the second output line and a ground voltage source through a second diode connected between the first and second output lines; and generating the driving power by use of the voltage on the first output line supplied through the second diode and the voltage stored at the second capacitor and supplying the generated driving power to the electroluminescence display device through the second output line.

[0040] In the method, the second step includes the steps of: storing the square wave signal generated in accordance with the second switching control signal to a first capacitor connected to the first output line; supplying the DC power to the first output line through a first diode connected between an input line of the DC power and the first output line; and generating a voltage of $1/N$ times the driving power by use of a voltage stored at the first capacitor and the DC power supplied through the first diode, and supplying the generated voltage of $1/N$ times the driving power to the first output line through a second diode connected between the first output line and the first capacitor.

[0041] In the method, the third step includes the steps of storing the voltage of $1/N$ times supplied from the first output line and storing the stored energy at a second capacitor connected between the second output line and a ground voltage source; and generating the driving power by use of the voltage stored at the second capacitor and the voltage from the first output line by switching

the switching device in accordance with the first switching control signal and supplying the generated driving power to the electroluminescence display device.

5 BRIEF DESCRIPTION OF THE DRAWINGS

[0042] These and other objects of the invention will be apparent from the following detailed description of the embodiments of the present invention with reference to the accompanying drawings, in which:

[0043] FIG. 1 is a block diagram representing a prior art power supply;

[0044] FIG. 2 is a block diagram representing a power supply in accordance with a first embodiment of the present invention;

[0045] FIG. 3 is a circuit diagram representing a power supply shown in FIG. 2;

[0046] FIG. 4 is a waveform diagram representing a square wave signal outputted from a high voltage supplier shown in FIG. 3;

[0047] FIG. 5 is a block diagram representing a power supply according to a second embodiment of the present invention;

[0048] FIG. 6 is a circuit diagram representing a power supply shown in FIG. 5;

[0049] FIG. 7 is a waveform diagram representing a square wave signal outputted from a low voltage supplier shown in FIG. 6; and

[0050] FIG. 8 is a driving apparatus of an electroluminescence display device according to another embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0051] Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

[0052] With reference to FIGS. 2 to 8, embodiments of the present invention will be explained as follows.

[0053] Referring to FIG. 2, a power supply according to the present invention includes a DC-DC converter 110 to boost a DC voltage V_{in} inputted through an input line for supplying the boosted DC voltage to a driving apparatus 120.

[0054] The driving apparatus 120 includes a flat panel display device driven by the DC voltage supplied from the DC-DC converter 110. Herein, the flat panel display device includes a liquid crystal display, a field emission display, a plasma display panel and an electroluminescence display device.

[0055] The plasma display panel is most advantageous in make large-sized screen because its structure and fabricating process is relatively simple, but has a disadvantage that its light emission efficiency and brightness is low and its power consumption is high. The liquid crystal display has difficulty in make large-sized screen because a semiconductor fabricating process is

used, but its demand is increasing because it is mainly used as the display device of notebook computers. However it has a big disadvantage that it is difficult in being made large-sized and its power consumption is high. Also, the liquid crystal display has a disadvantage that light loss is high and its viewing angle is narrow due to optical devices such as a polarizing filter, a prism sheet, a diffusion sheet and etc. In comparison to this, the electroluminescence display device has fast response speed and its light emission efficiency, brightness and viewing angle are high. The electroluminescence display device among them is a self-luminous device, which makes a fluorescent material emit light by re-combining electrons with electronic holes, and it is generally classified into an inorganic electroluminescence display device and an organic electroluminescence in accordance with its material and structure. Such an electroluminescence display device has an advantage that its response speed is as fast as a cathode ray tube in comparison with a passive light emitting device, which requires a separate light source like the liquid crystal display. Also, the electroluminescence display has low DC driving voltage and is possible to be made ultra-thin, thus it is applicable to a wall type or mobile type of display device.

[0056] A DC-DC converter 110 according to a first embodiment of the present invention, as shown in FIG. 3, includes a booster 112 to boost a DC voltage V_{in} supplied from an input line to half $V_{out}/2$ of an output voltage V_{out} by use of a switching device 1Q1, a charge pump part 114 to convert the half $V_{out}/2$ of the output voltage from the booster 112 into the output voltage V_{out} by a charge pumping method and supplied the converted output voltage V_{out} to a driving apparatus 120, a high voltage supplier 118 to supply high voltage V_H to the charge pump part 114, and a DC-DC controller 116 to control the high voltage supplier 118 as well as controlling the switching action of the switching device 1Q1.

[0057] The DC-DC controller 116 generates a first switching control signal SCS1 to switch the switching device 1Q1, and in addition, generates a second switching control signal SCS2 to switch the high voltage supplier 118, which is synchronized with the first switching control signal SCS1.

[0058] The booster 112 includes an inductor 1L and a first diode 1D1 connected in serial between the input line and a first node 1N1-the output line; a first capacitor 1C1 connected between a ground voltage source GND and a second node 1N2 that is between the input line and the inductor 1L; a switching device 1Q1 connected between the ground voltage source GND and a third node 1N3 that is between the inductor 1L and the first diode 1D1; and a second capacitor 1C2 connected between the first node 1N1 and the high voltage supplier 118.

[0059] The first capacitor 1C1 plays the role of stabilizing the DC voltage V_{in} supplied to the inductor 1L from the input line.

[0060] The switching device 1Q1 responds to the first

switching control signal SCS1 supplied from the DC-DC controller 116 to cause the energy stored at the inductor 1L to be supplied to the first node 1N1 through the first diode 1D1 as well as causing the voltage stored at the first capacitor 1C1 to be supplied to the inductor 1L. That is, the switching device 1Q1 forms a path so that the energy from the first capacitor 1C1 is supplied to the inductor 1L while it is switched on and forms a path so that the energy from the inductor 1L is supplied to the first node 1N1 through the first diode 1D1 while it is switched off.

[0061] The inductor 1L receives and stores the energy stored at the first capacitor 1C1 and supplies the stored energy to the first node 1N1 through the first diode 1D1 in accordance with the switching state of the switching device 1Q1.

[0062] The first diode 1D1 plays the role of intercepting the reverse voltage flowing toward the inductor 1L from the second capacitor 1C2.

[0063] The second capacitor 1C2 has its second terminal connected to the output terminal of the high voltage supplier 118 as well as its first terminal connected to the first node 1N1. Accordingly, the second capacitor 1C2 stores the energy supplied from the high voltage supplier 118 and supplies the stored energy to the first node 1N1.

[0064] In this way, the booster 112 boosts the DC voltage V_{in} supplied from the input line to a voltage $V_{out}/2$ corresponding to half of the output voltage V_{out} of the DC-DC converter 110.

[0065] The charge pump part 114 includes a second capacitor 1C2 of the booster part 112, a second diode 1D2 connected between the first node 1N1 and the output line V_{out} , and a third capacitor 1C3 connected between the ground voltage source GND and a fourth node 1N4 that is between the second diode 1D2 and the output line V_{out} .

[0066] The second capacitor 1C2 is commonly used in the booster 112 and the charge pump part 114.

[0067] The second diode 1D2 intercepts the voltage flowing toward the booster 112 from the third capacitor 1C3. The third capacitor 1C3 receives and stores the voltage $V_{out}/2$ corresponding to the half of the output voltage V_{out} of the DC-DC converter 110 from the booster 112, and supplies the stored voltage to the fourth node 1N4.

[0068] The charge pump part 114 supplies the voltage $V_{out}/2$ corresponding to the half of the output voltage V_{out} of the DC-DC converter 110 supplied from the booster 112 and the output voltage V_{out} of the desired DC-DC converter 110 by the voltage stored at the third capacitor 1C3, to the driving apparatus 120.

[0069] In this way, the DC-DC converter 110 according to the first embodiment of the present invention boosts the input voltage V_{in} to the voltage $V_{out}/2$ corresponding to the half of the output voltage V_{out} of the DC-DC converter 110 by use of the booster 112, and boosts the voltage $V_{out}/2$ corresponding to the half of

the output voltage of the DC-DC converter boosted at the booster 112 again to twice the voltage by use of the charge pump 114, thereby generating the output voltage V_{out} of the desired DC-DC converter 110 to supply it to the driving apparatus 120.

[0070] More specifically, the booster 112 of the DC-DC converter 110 switches the switching device 1Q1 in accordance with a first switching control signal SCS1 from a DC-DC controller 116 to store the DC voltage V_{in} from the input line at the inductor 1L, and it supplies the energy stored at the inductor 1L to the first node 1N1. At the same time, it stores a high voltage V_H of square wave from the high voltage supplier 118 at the second capacitor 1C2, and supplies the voltage stored at the second capacitor 1C2 to the first node 1N1, thereby supplying the voltage $V_{out}/2$ corresponding to the half of the output voltage V_{out} of the DC-DC converter 110 to the charge pump part 114.

[0071] And then, the charge pump part 114 stores the voltage $V_{out}/2$ corresponding to the half of the output voltage V_{out} of the DC-DC converter 110 inputted from the booster 112 at the third capacitor 1C3 and supplies the stored voltage to the fourth node 1N4. At the same time, it supplies the output voltage V_{out} of the desired DC-DC converter 110 to the driving apparatus 120 by the output voltage of the booster 112.

[0072] For example, in case that a DC-DC converter 110 has the input voltage V_{in} of 3V and the output voltage V_{out} of 20V, i.e., the difference between the input voltage V_{in} and the output voltage V_{out} is more than four times the input voltage, it is described as follows. First of all, assumed that the output voltage V_{out} of the DC-DC converter 110 is 20V and the threshold voltage of the first diode 1D1 is 0.5V, $20 = 2 \times V - 0.5$. Accordingly, 'V' is 10.25V. It is the input voltage of the charge pump part 114 and, at the same time, the output voltage of the booster 112.

[0073] Thus, the DC-DC converter 110 according to the first embodiment of the present invention can have maximum efficiency of more than 95% because the difference between the input voltage V_{in} and the output voltage $V_{out}/2$ is not more than four times of the input voltage if the switching state of the switching device and the high voltage supplier 118 in order to have the output voltage $V_{out}/2$ of 10.25V with the input voltage V_{in} of 3V. Also, the DC-DC converter 110 according to the first embodiment of the present invention uses only the charge pump part 114 of one tier, so it can have maximum efficiency to enable to increase energy conversion efficiency in general.

[0074] Referring to FIGs. 5 and 6, a DC-DC converter 210 according to a second embodiment of the present invention includes a charge pump part 214 to boost a DC voltage V_{in} supplied from an input line to half $V_{out}/2$ of the output voltage V_{out} by use of a charge pumping method, a booster 212 to convert half $V_{out}/2$ of an output voltage V_{out} from the charge pump part 214 by use of a switching device 2Q1, a low voltage supplier 218 to

supply low voltage to the charge pump part 214, and a DC-DC controller 216 to control the low voltage supplier 218 as well as controlling the switching action of the switching device 2Q1.

5 **[0075]** The DC-DC controller 216 generates a first switching control signal SCS1 to switch the switching device 2Q1, and in addition, generates a second switching control signal SCS2 to switch the low voltage supplier 218, which is synchronized with the first switching control signal SCS2.

10 **[0076]** The low voltage supplier 218 supplies the low voltage V_L inputted from a voltage source V_{cc} to a second capacitor 2C2 by switching a switching device (not shown) in response to the second switching control signal SCS2 supplied from the DC-DC controller 216. At this moment, the low voltage V_L supplied to the second capacity from the low voltage supplier 218 in accordance with the second switching control signal SCS2 supplied from the DC-DC controller 216 has a shape of square wave as shown in FIG. 7.

20 **[0077]** The charge pump part 214 includes a first diode 2D1 and a second diode 2D2 connected between the input line and the output line $V_{out}/2$, a first capacitor 2C1 connected between a ground voltage source GND and a second node 2N2 that is between the first diode 2D1 and the input line, and a second capacitor connected between the low voltage supplier 218 and a third node 2N3 that is between the first and second diodes 2D1, 2D2.

25 **[0078]** The first capacitor 2C1 plays the role of stabilizing the DC voltage V_{in} supplied to the third node 2N3 from the input line through the first diode 2D1.

30 **[0079]** The first diode 2D1 plays the role of intercepting the voltage flowing toward the second node 2N2 from the second capacitor 2C2.

35 **[0080]** The second capacitor 2C2 receives the low voltage V_L of square wave, as shown in FIG. 7, from the low voltage supplier 218 and stores it, and then it supplies the stored low voltage V_L to the third node 2N3. Accordingly, the voltage on the third node 2N3 is outputted to the output line through the second diode 2D2. At this moment, the voltage on the third node 2N3 is the voltage $V_{out}/2$ corresponding to half of the output voltage V_{out} from the DC-DC converter 210.

40 **[0081]** Such a charge pump part 214 generates the voltage $V_{out}/2$ corresponding to the half of the output voltage V_{out} from the DC-DC converter 210 by use of the low voltage V_L from the low voltage supplier 218 stored at the second capacitor 2C2 and the DC voltage V_{in} supplied from the input line to supply the generated voltage to the booster 212.

45 **[0082]** The booster 212 includes an inductor 2L and a third diode 2D3 connected in serial between the output line V_{out} and a first node 2N1 that is connected to the output of the charge pump part 214; a third capacitor 2C3 connected between a ground voltage source GND and a first node 2N1; a switching device 2Q1 connected between the ground voltage source GND and a fourth

node 2N4 that is between the inductor 2L and the third diode 2D3; and a fourth capacitor 2C4 connected between the ground voltage source GND and a fifth node 2N5 that is between the third diode 2D3 and the output line Vout.

[0083] The third capacitor 2C3 plays the role of stabilizing the voltage $V_{out}/2$ corresponding to the half of the output voltage of the DC-DC converter 210 supplied to the inductor 2L from the charge pump part 214.

[0084] The switching device 2Q1 responds to the first switching control signal SCS1 supplied from the DC-DC controller 216 to cause the energy stored at the inductor 2L to be supplied to the fifth node 2N5 through the third diode 2D3 as well as causing the voltage on the first node 2N1 to be supplied to the inductor 2L.

[0085] The inductor 2L receives and stores the energy on the first node 2N1 and supplies the stored energy to the fifth node 2N5 through the third diode 2D3, in accordance with the switching state of the switching device 2Q1.

[0086] The third diode 2D3 plays the role of intercepting the reverse voltage flowing toward the inductor 2L from the fourth capacitor 2C4.

[0087] The fourth capacitor 2C4 plays the role of stabilizing that the energy from the inductor 2L via the third diode 2D3 in accordance with the switching state of the switching device 2Q1 is supplied to the driving apparatus 220 through the output line Vout.

[0088] In this way, the booster 212 boosts the voltage $V_{out}/2$ corresponding to the half of the output voltage Vout of the DC-DC converter 210 supplied from the charge pump part 214 to twice the voltage by use of the booster method, thereby generating the output voltage Vout of the DC-DC converter 210.

[0089] In this way, the DC-DC converter 210 according to the second embodiment of the present invention boosts the input voltage V_{in} to the voltage $V_{out}/2$ corresponding to the half of the output voltage Vout of the DC-DC converter 210 by use of the charge pump part 214, and boosts the voltage $V_{out}/2$ corresponding to the half of the output voltage of the DC-DC converter boosted at the charge pump part 214 again to twice the voltage by use of the booster 212, thereby generating the output voltage Vout of the desired DC-DC converter 210 to supply it to the driving apparatus 220.

[0090] More specifically, the charge pump 214 of the DC-DC converter 210 generates the voltage $V_{out}/2$ corresponding to the half of the output voltage Vout of the DC-DC converter 210 to supply it to the booster 212 by use of the low voltage VL from the low voltage supplier 218 stored at the second capacitor 2C2 by the second switching control signal SCS2 from the DC-DC controller 216 and the input voltage V_{in} supplied from the input line through the first diode 2D1.

[0091] And then, the booster 212 switches the switching device 2Q1 in accordance with the first switching control signal SCS1 of the DC-DC controller 216 to store the voltage $V_{out}/2$ corresponding to the half of the output

voltage Vout of the DC-DC converter 210 supplied from the charge pump part 214 at the inductor 2L. At the same time, it supplies the energy stored at the inductor 2L to the driving apparatus 220 through the third diode 2D3.

[0092] At this moment, the energy stored at the inductor 2L corresponds to the output voltage Vout of the DC-DC converter 110 in accordance with the switching status of the switching device 2Q1 by the first switching control signal SCS1 from the DC-DC controller 216 and the energy of the inductor 2L is supplied to the driving apparatus 220 through the third diode 2D3 in accordance with the switching state of the switching device 2Q1. As a result, the booster 212 boosts the voltage $V_{out}/2$ corresponding to the half of the output voltage Vout of the DC-DC converter 210 inputted from the charge pump part 214 to twice the voltage and supplies it to the driving apparatus 220.

[0093] For example, in case that a DC-DC converter 110 has the input voltage V_{in} of 3V and the output voltage Vout of 20V, i.e., the difference between the input voltage V_{in} and the output voltage Vout is more than four times the input voltage, it is described as follows. First of all, the output voltage $V_{out}/2$ of the charge pump part 214 is $2 \times$ input voltage V_{in} - the threshold voltage V_{th} of the first diode 2D1. Accordingly, assumed that the threshold voltage of the first diode 2D1 is 0.5V, the output voltage $V_{out}/2$ of the charge pump 214 is 5.5V. The output voltage $V_{out}/2$ of the charge pump part 214 becomes the input voltage of the booster 212. On the other hand, the booster 212 the switching state of the switching device 2Q1 is controlled for the input voltage of 5.5V from the charge pump part 212 to have the output voltage Vout of 20V, it can get the maximum efficiency because the difference between the input voltage V_{in} and the output voltage $V_{out}/2$ is not more than four times the input voltage. Further, the DC-DC converter 210 according to the second embodiment of the present invention might have the maximum efficiency because it only uses the charge pump part 214 of one tier, thus energy conversion efficiency can be increased in general.

[0094] Referring to FIG. 8, a driving apparatus of an electroluminescence display device using a power supply according to another embodiment of the present invention includes a display panel 302 having EL cells 318 at intersections of scan electrode lines SEL and data electrode lines DEL; a scan driver 306 to drive the scan electrode lines SEL of the display panel 302; a data driver 304 to drive the data electrode lines DEL of the display panel 302; a timing controller 308 to control each of the scan driver 306 and the data driver 304; and a DC-DC converter 310 to generate the voltage and current required for driving the EL display device.

[0095] Each of the EL cells 318 is selected to generate light corresponding to a pixel signal, i.e., current signal, supplied to the data electrode line DEL of positive pole when a scan pulse is applied to the scan electrode line SEL. The EL cells 318 are each formed at every intersection of the data electrode line DEL and the scan elec-

trode line SEL and might be expressed equivalent to a diode. The EL cells 318 each have a negative scan pulse supplied to an arbitrary scan electrode line SEL and at the same time a positive current applied to the data electrode line DEL in accordance with the data signal, so the EL cells 318 of the scan line, where a forward voltage is applied, are made to emit light.

[0096] The scan driver 306 sequentially supplies a negative scan pulse to a plurality of scan electrode lines SEL to select the scan line where the data is displayed. For this, the scan driver 306 includes shift registers that sequentially shift an input shift start pulse and output the shifted pulse; and level shifters that level-shift the shift signal from the shift register to the scan pulse suitable for driving the scan electrode line SEL and output the level-shifted pulse.

[0097] The data driver 304 supplies the constant current synchronized with the scan pulse to each of the data electrode lines DEL by use of a constant current source (not shown).

[0098] The timing controller 308 supplies control signals together with data to the data driver 304 as well as supplying gate control signals GCS to the scan driver 306.

[0099] The DC-DC converter 310 has the same components as the DC-DC converters 110, 210 according to the first and second embodiments of the present invention shown in FIGs. 3 and 6. Accordingly, the description about the DC-DC converter 310 according to another embodiment of the present invention is to be replaced with the description of the DC-DC converters 110, 210 according to the first and second embodiments of the foregoing present invention.

[0100] Therefore, an apparatus and method for driving an electroluminescence display device using a power supply according to another embodiment of the present invention generates the voltage $V_{out}/2$ which is half the output voltage V_{out} from the booster and boosts the half $V_{out}/2$ of the generated output voltage V_{out} to twice the voltage by the charge pumping method to generate a desired output voltage V_{out} in case that the difference between the input voltage V_{in} and the output voltage V_{out} of the DC-DC converter 310 is not less than four times the input voltage. Accordingly, maximum efficiency can be obtained because the difference between the input voltage and the output voltage is not more than four times the input voltage in the booster, and the energy conversion efficiency might be increased in general because the maximum efficiency is obtained due to the use of the charge pump part of one tier. Accordingly, an apparatus and method for driving an electroluminescence display device using a power supply according to another embodiment of the present invention can improve the energy conversion efficiency of the DC-DC converter 310 which generates the voltage and current required for driving the EL display device.

[0101] As described above, a power supply and a

driving method thereof and an apparatus and method for driving an electroluminescence display device using the same generates an input voltage that is half of the output voltage by the booster method and controls the output voltage by using the generated voltage in the charge pump method. Accordingly, the present invention might improve the energy conversion efficiency in case that the difference between the input voltage and the output voltage of the DC-DC converter is not less than four times the input voltage.

[0102] Although the present invention has been explained by the embodiments shown in the drawings described above, it should be understood to the ordinary skilled person in the art that the invention is not limited to the embodiments, but rather that various changes or modifications thereof are possible without departing from the spirit of the invention. Accordingly, the scope of the invention shall be determined only by the appended claims and their equivalents.

[0103] The claims refer to examples of preferred embodiments of the invention. However, the invention also refers to combinations of any claim or claims with any other claim or claims and/or with any feature or combination of features which is or are disclosed in the description and/or in the drawings.

Claims

1. A power supply that supplies a driving power to drive a driving apparatus, comprising:

a power source to supply a DC power; and
a DC-DC converter to convert the DC power from the power source to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power.

2. The power supply according to claim 1, further comprising:

a square wave signal supplier to supply a square wave signal to the DC-DC converter; and
a DC-DC controller that generates a second switching control signal for controlling the square wave signal supplier and in addition generates a first switching control signal synchronized with the second switching control signal.

3. The power supply according to claim 2, wherein the DC-DC converter includes:

a booster that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a switching device which is switched in re-

sponse to the first switching control signal and outputs the converted DC power to a first output line; and

a charge pump part that converts $1/N$ times the DC power from the first output line to a level of the driving power by use of the square wave signal supplied from the square wave signal supplier in response to the second switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

4. The power supply according to claim 3, wherein the booster includes:

an inductor and a first diode connected between the power source and the first output line; a first capacitor connected between a ground voltage source and a node that is between the power source and the inductor;

a second capacitor connected between the first output line and the square wave signal supplier; and

a switching device connected between the ground voltage source and a node that is between the first diode and the inductor to switch an energy stored at the inductor to the first output line as well as switching an energy from the power source to the inductor in response to the first switching control signal from the DC-DC controller.

5. The power supply according to claim 4, wherein the square wave signal supplier includes:

a high voltage source to supply high voltage; and

a switching device that converts the high voltage from the high voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

6. The power supply according to claim 4, wherein the charge pump part includes:

the second capacitor;

a second diode connected between the first output line and the second output line; and

a third capacitor connected between the ground voltage source and a node that is between the second diode and the second output line.

7. The power supply according to claim 2, wherein the DC-DC converter includes:

a charge pump part that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a square wave signal supplied from the square wave signal supplier in response to the second switching control signal and outputs the converted DC power to a first output line; and

a booster that converts $1/N$ times the DC power supplied from the first output line to a level of the driving power by use of a switching device that is switched in response to the first switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

8. The power supply according to claim 7, wherein the charge pump part includes:

a first diode and a second diode connected between the power source and the first output line; a first capacitor connected between a ground voltage source and a node that is between the power source and the first diode; and a second capacitor connected between the square wave signal supplier and a node that is between the first and second diodes.

9. The power supply according to claim 8, wherein the square wave signal supplier includes:

a low voltage source to supply low voltage; and a switching device that converts the low voltage from the low voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

10. The power supply according to claim 7, wherein the booster includes:

an inductor and a third diode connected between the first output line and the second output line;

a third capacitor connected between a ground voltage source and a node that is between the first output line and the inductor;

a fourth capacitor connected between the ground voltage source and a node that is between the third diode and the second output line; and

a switching device connected between the ground voltage source and a node that is between the inductor and the third diode to switch an energy stored at the inductor to the second output line as well as switching an energy from the input line to the inductor in response to the

first switching control signal from the DC-DC controller.

11. A driving apparatus of an electroluminescence display device, comprising:

an electroluminescence display device to display a picture;
a power source to supply DC power; and
a DC-DC converter to convert the DC power from the power source to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power for driving the electroluminescence display device, and then to convert the converted DC power of $1/N$ times to a level of the driving power.

12. The driving apparatus according to claim 11, further comprising:

a square wave signal supplier to supply a square wave signal to the DC-DC converter; and
a DC-DC controller that generates a second switching control signal for controlling the square wave signal supplier and in addition generates a first switching control signal synchronized with the second switching control signal.

13. The driving apparatus according to claim 12, wherein the DC-DC converter includes:

a booster that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a switching device which is switched in response to the first switching control signal and outputs the converted DC power to a first output line; and
a charge pump part that converts $1/N$ times the DC power from the first output line to a level of the driving power by use of the square wave signal supplied from the square wave signal supplier in response to the second switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

14. The driving apparatus according to claim 13, wherein the booster includes:

an inductor and a first diode connected between the power source and the first output line; a first capacitor connected between a ground voltage source and a node that is between the power source and the inductor; and a second capacitor connected between the first

output line and the square wave signal supplier; and

a switching device connected between the ground voltage source and a node that is between the first diode and the inductor to switch an energy stored at the inductor to the first output line as well as switching an energy from the power source to the inductor in response to the first switching control signal from the DC-DC controller.

15. The driving apparatus according to claim 14, wherein the square wave signal supplier includes:

a high voltage source to supply high voltage; and
a switching device that converts the high voltage from the high voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

16. The driving apparatus according to claim 14, wherein the charge pump part includes:

the second capacitor;
a second diode connected between the first output line and the second output line; and
a third capacitor connected between the ground voltage source and a node that is between the second diode and the second output line.

17. The driving apparatus according to claim 12, wherein the DC-DC converter includes:

a charge pump part that converts the DC power to a level of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power by use of a square wave signal supplied from the square wave signal supplier in response to the second switching control signal and outputs the converted DC power to a first output line; and
a booster that converts $1/N$ times the DC power supplied from the first output line to a level of the driving power by use of a switching device that is switched in response to the first switching control signal and supplies the converted DC power to the driving apparatus through the second output line.

18. The driving apparatus according to claim 17, wherein the charge pump part includes:

a first diode and a second diode connected between the power source and the first output line;

a first capacitor connected between a ground voltage source and a node that is between the power source and the first diode; and
a second capacitor connected between the square wave signal supplier and a node that is

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19. The driving apparatus according to claim 18, wherein the square wave signal supplier includes:

a low voltage source to supply low voltage; and
a switching device that converts the low voltage from the low voltage source into the square wave in accordance with the second switching control signal from the DC-DC controller and supplies the converted square wave to the second capacitor.

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20. The driving apparatus according to claim 17, wherein the booster includes:

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an inductor and a third diode connected between the first output line and the second output line;
a third capacitor connected between a ground voltage source and a node that is between the first output line and the inductor;
a fourth capacitor connected between the ground voltage source and a node that is between the third diode and the second output line; and
a switching device connected between the ground voltage source and a node that is between the inductor and the third diode to switch an energy stored at the inductor to the second output line as well as switching an energy from the input line to the inductor in response to the first switching control signal from the DC-DC controller.

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21. A driving method of a power supply that supplies a driving power to drive a driving apparatus, comprising:

a first step of generating a DC power;
a second step of converting the DC power into a voltage of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power and outputting the converted driving power to a first output line; and
a third step of converting the converted voltage of $1/N$ times the driving power to a level of the driving power and supplying the converted voltage to the driving apparatus through a second output line.

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22. The driving method according to claim 21, further comprising:

a fourth step of generating a first switching control signal to switch a switching device;
a fifth step of generating a second switching control signal synchronized with the first switching control signal; and
a sixth step of converting a voltage from a voltage source into a square wave signal in accordance with the second switching control signal.

23. The driving method according to claim 22, wherein the second step includes the steps of:

storing the DC power at an inductor and supplying the stored energy to the first output line through a first diode connected between the inductor and the first output line by switching the switching device in accordance with the first switching control signal;
supplying the square wave signal generated in accordance with the second switching control signal to a first capacitor connected to the first output line, and supplying the voltage stored at the first capacitor to the first output line; and
generating a voltage of $1/N$ times the driving power by use of an energy from the inductor and a voltage from the first capacitor supplied to the first output line.

24. The driving method according to claim 23, wherein the third step includes the steps of:

storing the voltage on the first output line to a second capacitor connected between the second output line and a ground voltage source through a second diode connected between the first and second output lines; and
generating the driving power by use of the voltage on the first output line supplied through the second diode and the voltage stored at the second capacitor and supplying the generated driving power to the driving apparatus through the second output line.

25. The driving method according to claim 22, wherein the second step includes the steps of:

storing the square wave signal generated in accordance with the second switching control signal to a first capacitor connected to the first output line;
supplying the DC power to the first output line through a first diode connected between an input line of the DC power and the first output line; and
generating a voltage of $1/N$ times the driving power by use of a voltage stored at the first capacitor and the DC power supplied through the first diode, and supplying the generated voltage

of $1/N$ times the driving power to the first output line through a second diode connected between the first output line and the first capacitor.

26. The driving method according to claim 25, wherein the third step includes the steps of: 5

storing the voltage of $1/N$ times supplied from the first output line and storing the stored energy at a second capacitor connected between the second output line and a ground voltage source; and
generating the driving power by use of the voltage stored at the second capacitor and the voltage from the first output line by switching the switching device in accordance with the first switching control signal and supplying the generated driving power to the driving apparatus. 10 15

27. A method for driving an electroluminescence display device to display a picture by use of a driving power from a power supply, comprising: 20

a first step of generating a DC power;
a second step of converting the DC power into a voltage of $1/N$ (provided that N is a positive integer of not less than 2) times the driving power and outputting the converted driving power to a first output line; and
a third step of converting the converted voltage of $1/N$ times the driving power to a level of the driving power and supplying the converted voltage to the electroluminescence display device through a second output line. 25 30 35

28. The method according to claim 27, further comprising: 40

a fourth step of generating a first switching control signal to switch a switching device;
a fifth step of generating a second switching control signal synchronized with the first switching control signal; and
a sixth step of converting a voltage from a voltage source into a square wave signal in accordance with the second switching control signal. 45

29. The method according to claim 28, wherein the second step includes the steps of: 50

storing the DC power at an inductor and supplying the stored energy to the first output line through a first diode connected between the inductor and the first output line by switching the switching device in accordance with the first switching control signal;
supplying the square wave signal generated in accordance with the second switching control 55

signal to a first capacitor connected to the first output line, and supplying the voltage stored at the first capacitor to the first output line; and
generating a voltage of $1/N$ times the driving power by use of an energy from the inductor and a voltage from the first capacitor supplied to the first output line.

30. The method according to claim 29, wherein the third step includes the steps of:

storing the voltage on the first output line to a second capacitor connected between the second output line and a ground voltage source through a second diode connected between the first and second output lines; and
generating the driving power by use of the voltage on the first output line supplied through the second diode and the voltage stored at the second capacitor and supplying the generated driving power to the electroluminescence display device through the second output line.

31. The method according to claim 28, wherein the second step includes the steps of:

storing the square wave signal generated in accordance with the second switching control signal to a first capacitor connected to the first output line;
supplying the DC power to the first output line through a first diode connected between an input line of the DC power and the first output line; and
generating a voltage of $1/N$ times the driving power by use of a voltage stored at the first capacitor and the DC power supplied through the first diode, and supplying the generated voltage of $1/N$ times the driving power to the first output line through a second diode connected between the first output line and the first capacitor. 30 35 40 45

32. The method according to claim 31, wherein the third step includes the steps of:

storing the voltage of $1/N$ times supplied from the first output line and storing the stored energy at a second capacitor connected between the second output line and a ground voltage source; and
generating the driving power by use of the voltage stored at the second capacitor and the voltage from the first output line by switching the switching device in accordance with the first switching control signal and supplying the generated driving power to the electroluminescence display device.

FIG.1

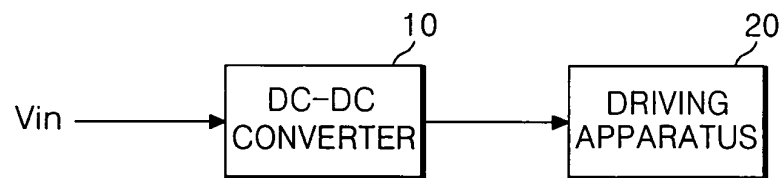


FIG.2

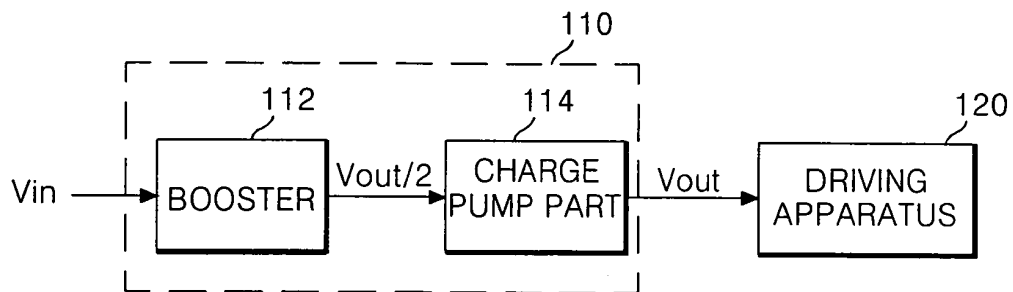


FIG.3

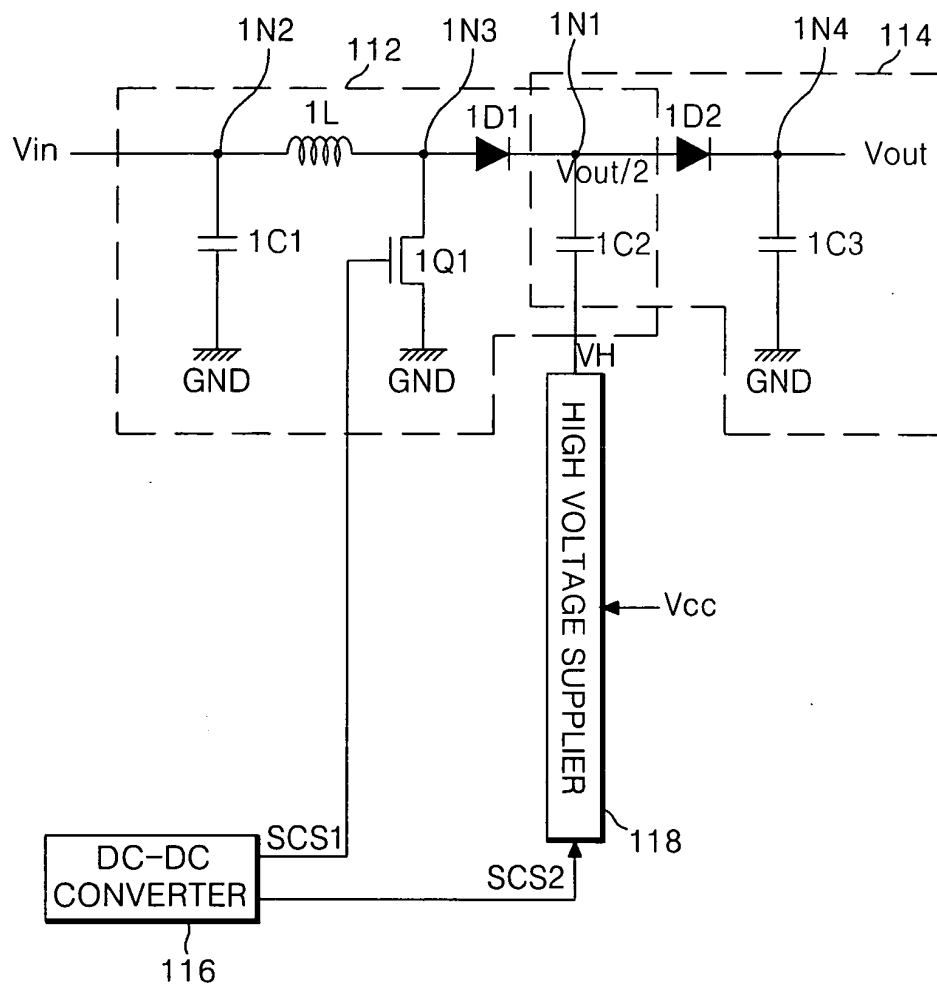


FIG.4

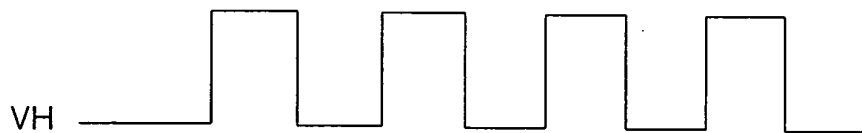


FIG.5

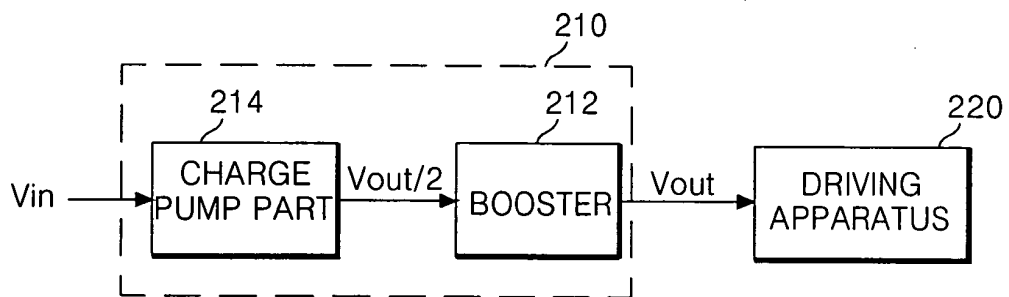


FIG. 6

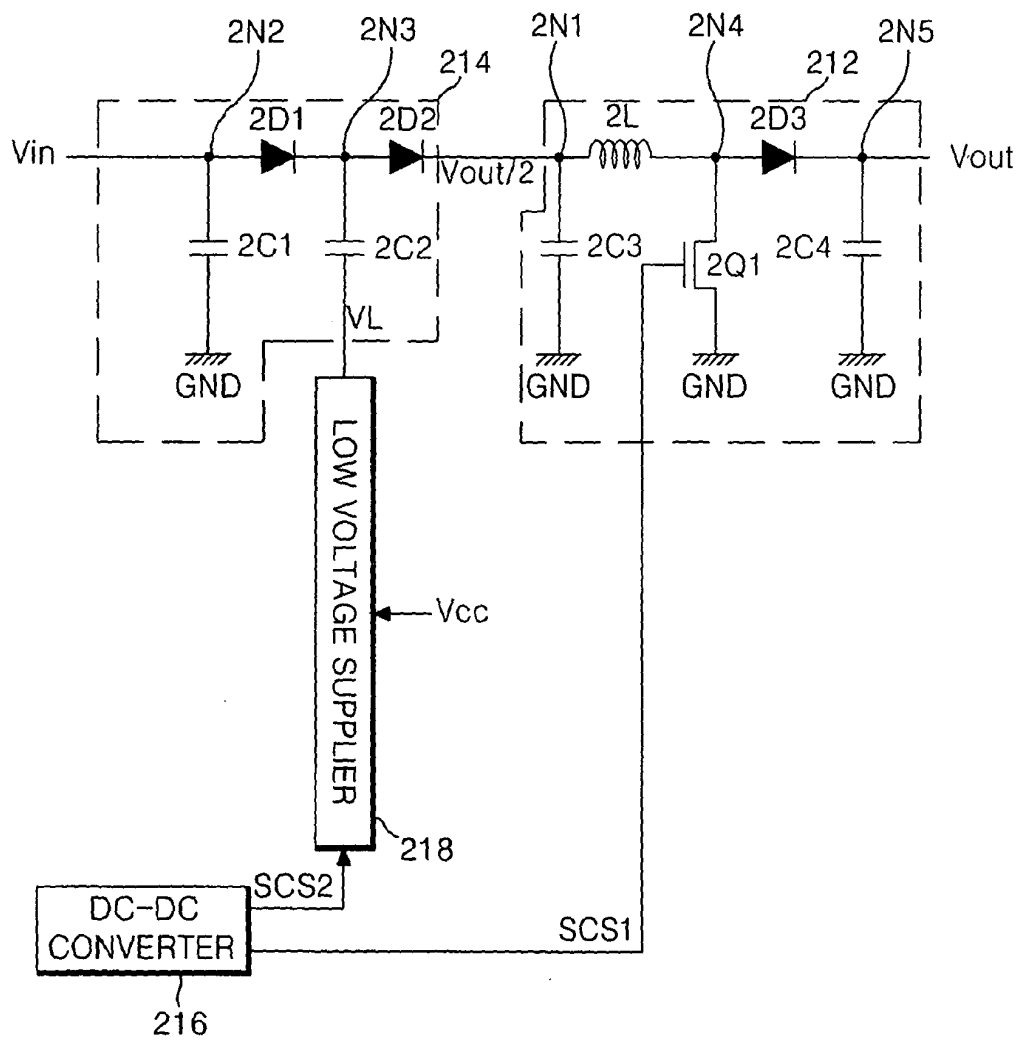


FIG.7

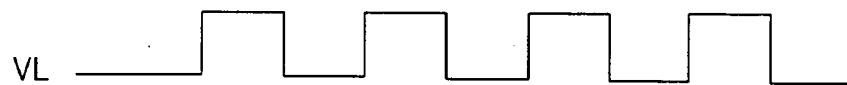
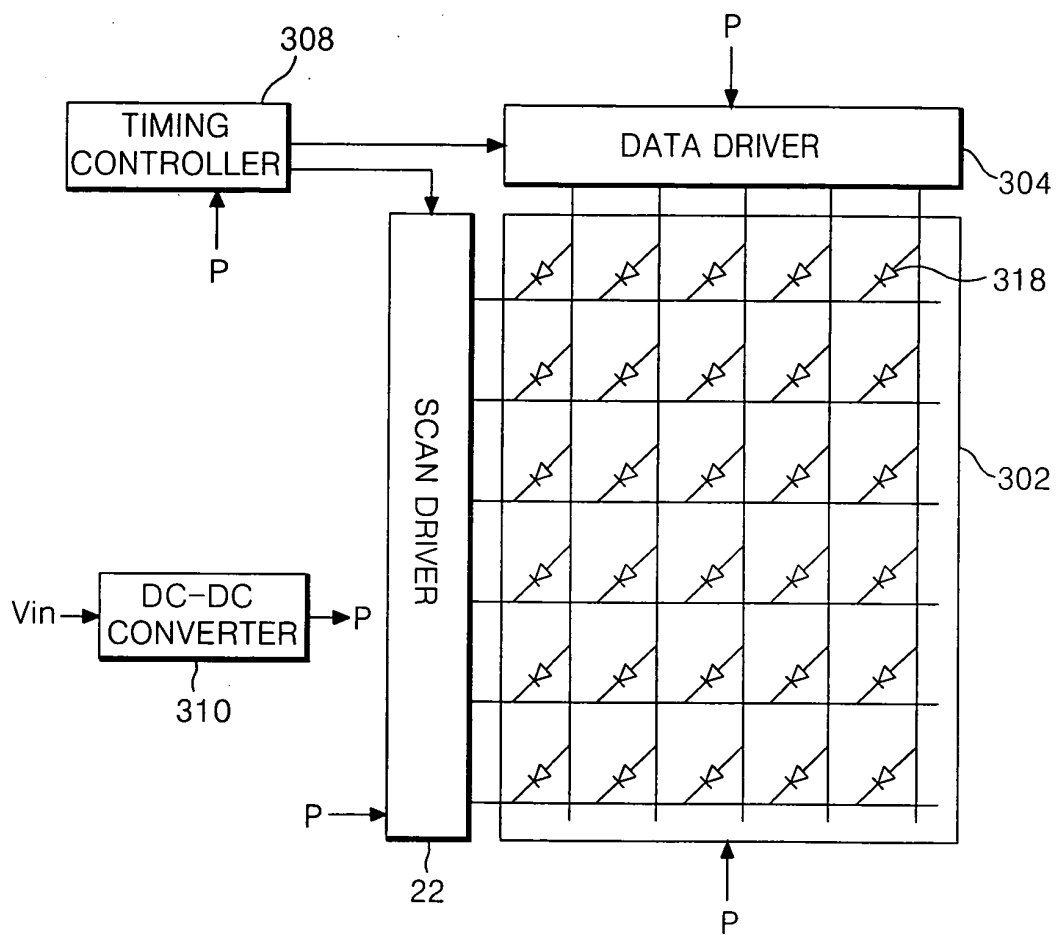


FIG. 8



专利名称(译)	电源及其驱动方法和使用该电源的驱动电致发光显示装置的装置和方法		
公开(公告)号	EP1505715A2	公开(公告)日	2005-02-09
申请号	EP2004018109	申请日	2004-07-30
申请(专利权)人(译)	LG电子株式会社.		
当前申请(专利权)人(译)	LG电子株式会社.		
[标]发明人	KIM HYUN JEOUNG KIM HAK SU		
发明人	KIM, HYUN JEOUNG KIM, HAK SU		
IPC分类号	H05B33/08 G09G3/20 G09G3/30 H01L51/50 H02M3/07 H02M3/155 H02M3/156 H05B33/14		
CPC分类号	H02M3/156 H02M3/07 H02M2001/007		
优先权	1020030052979 2003-07-31 KR		
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摘要(译)

本发明涉及一种适于提高能量转换效率的电源及其驱动方法，以及使用该电源驱动电致发光显示装置的装置和方法。提供驱动动力以驱动根据本发明实施例的驱动装置的电源包括用于提供DC电力的电源;和DC-DC转换器将来自电源的DC电源转换为 $1/N$ 的电平（假设N是不小于2的正整数）乘以驱动功率，然后转换转换后的DC功率 $1/N$ 次到驱动功率的水平。

FIG.2

