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(71) Applicant: Samsung SDI Co., Ltd.

Suwon-si
Gyeonggi-do (KR)

(72) Inventors:

- Lee, Kwan-Hee,
Samsung SDI Co., Ltd.
Suwon-si, Gyeonggi-do (KR)
- Im, Ja-Hyun,
Samsung SDI Co., Ltd.
Suwon-si, Gyeonggi-do (KR)

(74) Representative: Hengelhaupt, Jürgen et al

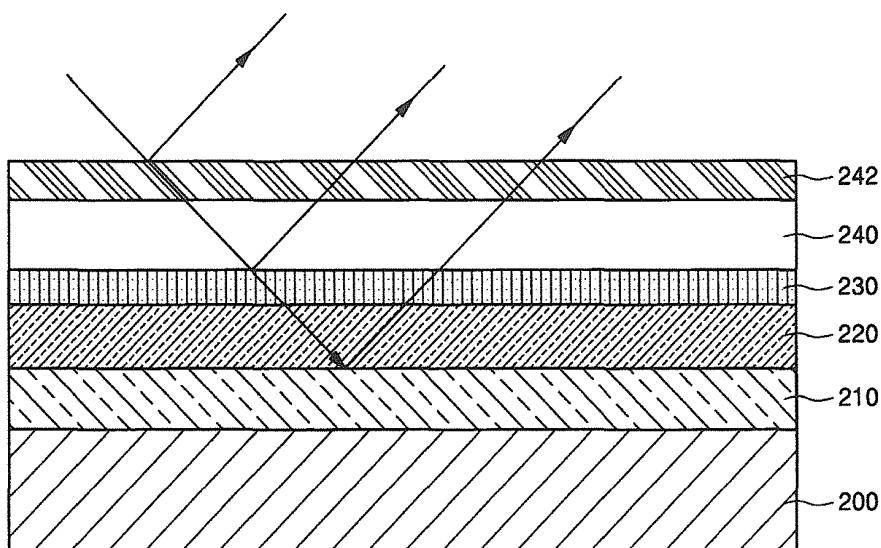
Gulde Hengelhaupt Ziebig & Scheider
Wallstrasse 58/59
10179 Berlin (DE)

(54) Flat panel display and method of fabricating the same

(57) A flat panel display includes a pixel electrode, an organic emission layer, an opposite electrode, a phase shift layer and a reflecting layer disposed on a

substrate. The phase shift layer and the reflecting layer are stacked on the opposite electrode to destructively interfere with reflected external light to realize black and achieve excellent luminous efficiency.

FIG. 2



Description**CROSS-REFERENCE TO RELATED APPLICATION**

[0001] This application claims priority to and the benefit of Korean Patent Application No. 10-2004-0098877, filed on November 29, 2004, which is hereby incorporated by reference for all purposes as if fully set forth herein.

BACKGROUND OF THE INVENTION**Field of the Invention**

[0002] This invention relates to a flat panel display that has a phase shift layer and a reflecting layer stacked on an opposite electrode to destructively interfere with reflected external light to realize black. The invention also relates to a method of fabricating the flat panel display.

Discussion of the Background

[0003] An organic light emitting display (OLED) has a contrast ratio depending on the intensity of external light. A black matrix material may be employed to improve the contrast ratio, but it is very difficult to realize perfect black by shielding the external light in an emission region.

[0004] One device used in the prior art to attempt to solve this problem is a circular polarizer 140 shown in FIG. 1. US Patent No. 5,596,246 discloses an OLED that employs the circular polarizer 140. The OLED includes a circular polarizer 140, a lower substrate 100, a transparent first electrode 110, an organic emission layer 120, and a reflecting second electrode 130.

[0005] The circular polarizer 140 is employed to reduce the amount of external light that is reflected off a display panel. The circular polarizer 140 includes a linear polarizer 145 and a quarter compensating plate 141. Two axes of the quarter compensating plate 141 form a 45° angle to an axis of the linear polarizer 145. External light passes through the linear polarizer 145 and becomes linearly polarized. The quarter compensating plate 141 rotates the polarized light 45° spirally. The rotation direction of the polarized light is reversed after it is reflected from a reflecting layer of the display. The polarized light is rotated another 45° when it passes back through the quarter compensating plate 141. At this point, the polarized light is perpendicular to the linear polarizer and is therefore blocked by the linear polarizer 145.

[0006] Only about 44% of light emitted from the organic emission layer 120 passes through the circular polarizer 140. A large amount of light emitted from the organic emission layer 120 is absorbed by the circular polarizer 140, which causes the luminous efficiency and the contrast ratio to be low. To compensate for the reduced luminance, a relatively high voltage must be applied to achieve the desired luminance, which increases the power consumption and decreases the life span of the display. Furthermore, the manufacturing costs of the OLED are increased because the manufacturing process is complicated and the polarizer is expensive.

[0007] Another device used in the prior art to attempt to solve this problem was developed by Luxell Technologies Inc. Instead of a polarizer, Luxell incorporated an absorbing layer and a dielectric layer between an inorganic phosphorescent layer and an opposite electrode in an inorganic device. The method achieves black by destructive interference of external light and was disclosed in the *Journal of Military and Aerospace Electronics*, Volume 9, No. 6, June, 1998. But the method does not realize black to a satisfactory degree because the thickness of a thin film should be adjusted in consideration of the refractive index and absorptivity thereof.

SUMMARY OF THE INVENTION

[0008] The present invention provides a flat panel display with a phase shift layer and a reflective layer stacked on an opposite electrode to destructively interfere with reflected external light to realize black. The flat panel display has excellent luminous efficiency and contrast ratio, which lowers the required luminance and thus reduces power consumption and increases the life span of the display.

[0009] Additional features of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention.

[0010] The present invention discloses a flat panel display that includes a pixel electrode disposed above a substrate, an organic layer disposed on the pixel electrode and having at least an emission layer, an opposite electrode disposed on the organic layer, a phase shift layer disposed on the opposite electrode, and a reflecting layer disposed on the phase shift layer.

Preferably the flat panel display further comprises at least one thin film transistor disposed between the substrate and the pixel electrode. Preferably the pixel electrode is a reflecting electrode. Preferably the organic layer further includes at least one additional layer selected from the group of a hole injection layer, a hole transport layer, a hole blocking layer, an electron transport layer, and an electron injection layer. Preferably the opposite electrode is formed of a material with a transmittance of about 10% to about 40%. Preferably the opposite electrode is formed of a material selected from the group of Mg, MgAg, Ca, CaAg, Ag, AlCa, AlAg, LiMg, and Li. Preferably the opposite electrode is about 150Å to about 250Å thick. Preferably the opposite electrode is about 180Å thick. Preferably the phase shift layer is about 400Å to about 1300Å thick. Preferably the phase shift layer is about 500Å to about 800Å thick.

Preferably the phase shift layer is a transparent layer. Preferably the transparent layer is an organic layer, an inorganic layer, or a stacked structure of an organic layer and an inorganic layer. Preferably the inorganic layer is formed of a material selected from the group of silicon nitride (SiNx), silicon oxide (SiO₂), silicon oxynitride (SiON), and a transparent conductive oxide (TCO). Preferably the transparent conductive oxide is indium tin oxide (ITO).

Preferably the transparent conductive oxide is a transparent conductive polymer (TCP). Preferably the TCP is poly(ethylene dioxythiophene) (PEDOT). Preferably the transparent conductive polymer is poly(3,4-ethylenedioxythiophene) (PEDOT). Preferably the transparent conductive polymer is poly(3,4-ethylenedioxythiophene) polystyrene sulfonate (PEDOT:PSS). Preferably the transparent conductive polymer is poly(3,4-ethylenedioxythiophene) polystyrene sulfonate (PEDOT:PSS).

erably the reflecting layer is formed of a material that has a transmittance of about 40% to about 60%. Preferably the reflecting layer is a semi-transparent metal layer. Preferably the semi-transparent metal layer is formed of a material selected from the group of Mg, Ag, MgAg, Cr, Pt, and Au. Preferably the reflecting layer is about 80Å to about 120Å thick.

[0011] The present invention also discloses a method of fabricating a flat panel display. The method includes forming a pixel electrode above a substrate, forming an organic layer having at least an emission layer on the pixel electrode, forming an opposite electrode on the organic layer, forming a phase shift layer on the opposite layer, and forming a reflecting layer on the phase shift layer.

Preferably the method further comprises the step of forming at least one thin film transistor between the substrate and the pixel electrode. Preferably the opposite electrode is formed of a material with a transmittance of about 10% to about 40%. Preferably the opposite electrode is formed of a material selected from the group of Mg, MgAg, Ca, CaAg, Ag, AlCa, AlAg, LiMg, and Li. Preferably the opposite electrode is about 150Å to about 250Å thick. Preferably the opposite electrode is about 180Å thick. Preferably the phase shift layer is about 400Å to about 1300Å thick. Preferably the phase shift layer is about 500Å to about 800Å thick. Preferably the phase shift layer is a transparent layer. Preferably the transparent layer is an organic layer, an inorganic layer, or a stacked structure of an organic layer and an inorganic layer. Preferably the inorganic layer is formed of a material selected from the group of silicon nitride (SiNx), silicon oxide (SiO₂), silicon oxynitride (SiON), and a transparent conductive oxide (TCO). Preferably the reflecting layer is formed of a material that has a transmittance of about 40% to about 60%. Preferably the reflecting layer is a semi-transparent metal layer. Preferably the semi-transparent metal layer is formed of a material selected from the group of Mg, Ag, MgAg, Cr, Pt, and Au. Preferably the reflecting layer is about 80Å to about 120Å thick.

[0012] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention, and together with the description serve to explain the principles of the invention.

[0014] FIG. 1 shows a cross-sectional view of a conventional OLED having a circular polarizer.

[0015] FIG. 2 shows a cross-sectional view of an OLED according to an exemplary embodiment of the present invention.

[0016] FIG. 3 shows a graph illustrating the luminance of an OLED according to an exemplary embodiment of the present invention.

[0017] FIG. 4 shows a graph illustrating the luminous efficiency of an OLED according to an exemplary embodiment of the present invention.

[0018] FIG. 5A, FIG. 5B, and FIG. 5C shows photographs of OLEDs which realize different degrees of black.

[0019] FIG. 6 shows a graph illustrating the reflectivity of an OLED according to an exemplary embodiment of the present invention.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0020] The invention is described more fully hereinafter with reference to the accompanying drawings, in which embodiments of the invention are shown. This invention may, however, be embodied in many different forms and should not be construed as limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure is thorough, and will fully convey the scope of the invention to those skilled in the art. In the drawings, the size and relative sizes of layers and regions may be exaggerated for clarity.

[0021] It will be understood that when an element such as a layer, film, region or substrate is referred to as being "on" another element, it can be directly on the other element or intervening elements may also be present. In contrast, when an element is referred to as being "directly on" another element, there are no intervening elements present.

[0022] FIG. 2 shows a cross-sectional view of a flat panel display according to an exemplary embodiment of the present invention. The present invention may be applied to any type of flat panel display device. FIG. 2 shows an exemplary embodiment in which the flat panel display is an OLED.

[0023] A pixel electrode 210 is disposed on a substrate 200, an organic layer 220 having at least an emission layer is disposed on the pixel electrode 210, and an opposite electrode 230, a phase shift layer 240 and a reflecting layer 242 are sequentially disposed on the organic layer 220. A plurality of thin film transistors (TFTs) may be disposed between the substrate 200 and the pixel electrode 210.

[0024] The pixel electrode 210 is a reflecting electrode with a transparent conductive oxide (TCO) such as indium tin oxide (ITO) or indium zinc oxide (IZO) formed on a reflecting layer made of A1 (Nd), an A1 alloy, Ag, or an Ag alloy. The opposite electrode 230 is a reflecting electrode layer. The opposite electrode 230 may be made of one of Mg, MgAg, Ca, CaAg, Ag, AlCa, AlAg, LiMg, or Li. The opposite electrode 230 is about 150Å to about 250Å thick, and preferably is about 180Å thick.

[0025] The phase shift layer 240 may be an organic layer, an inorganic layer, or a stacked structure of layers. The phase shift layer 240 may be about 400Å to about

1300Å thick, and preferably is about 500Å to about 800Å thick. The phase shift layer 240 serves as a transparent layer that inverts the phase of light reflected from the opposite electrode 230 to create a phase shift. The phase shift creates an annihilation phenomenon that causes external reflected light to destroy itself.

[0026] The reflecting layer 242 may be a semi-transparent metal layer made of Mg, Ag, MgAg, Cr, Pt, or Au. The reflecting layer 242 may be about 60Å to about 130Å thick and preferably is about 80Å to about 120Å thick. The reflecting layer 242 should have a higher transmittance than the opposite electrode 230. The reflecting layer 242 may have a transmittance of about 40% to about 60%, and the opposite electrode 230 may have a transmittance of about 10% to about 40%.

[0027] A method of fabricating the OLED will now be explained below.

[0028] First, a pixel electrode 210 is disposed on a substrate 200. The pixel electrode 210 is a transparent electrode and may be made of a transparent conductive oxide (TCO) such as indium tin oxide (ITO) or indium zinc oxide (IZO). The pixel electrode 210 includes a reflecting layer (not shown) positioned below it. At least one thin film transistor (TFT) may be disposed between the transparent substrate 200 and the pixel electrode 210.

[0029] An organic layer 220 with at least an emission layer is formed on the pixel electrode 210. The organic layer 220 may also include one or more of a hole injection layer, a hole transport layer, a hole blocking layer, an electron transport layer, and an electron injection layer.

[0030] An opposite electrode 230 is formed on the organic layer 220. The opposite electrode 230 is a reflecting electrode layer and may be made of Mg, MgAg, Ca, CaAg, Ag, AlCa, AlAg, LiMg, or Li. The opposite electrode 230 may be about 150Å to about 250Å thick and preferably is about 180Å thick.

[0031] A phase shift layer 240 is formed on the opposite electrode 230. The phase shift layer 240 serves to invert a phase of light reflected from the opposite electrode 230. The phase shift layer 240 may be about 400Å to about 1300Å thick, and preferably is about 500Å to about 800Å thick. The annihilation phenomenon does not occur when the thickness of the phase shift layer 240 falls outside the range of about 400Å to about 1300Å thick.

[0032] The phase shift layer 240 may be formed of an organic layer, an inorganic layer, or a stacked structure of layers. Any type of transparent organic layer may be used to form an organic phase shift layer 240. SiNx, SiO₂, SiON, or transparent conductive oxide (TCO) may be used to form an inorganic phase shift layer 240.

[0033] A reflecting layer 242 is formed on the phase shift layer 240. The reflecting layer 242 may be a semi-transparent metal layer made of Mg, Ag, MgAg, Cr, Pt, or Au. The reflecting layer 242 may be about 50Å to about 120Å thick. The thickness of the reflecting layer 242 may be combined with the thickness of the phase shift layer 240 to create an optimal thickness to destructively inter-

fere with and extinguish reflected external light. The reflecting layer 242 reflects external incident light to realize black by creating destructive interference with the phase-inverted light from the phase shift layer 240.

[0034] In an exemplary embodiment of the present invention, the phase shift layer 240 and the reflecting layer 242 are patterned only on an emission region of the OLED.

[0035] FIG. 3 shows a graph illustrating the luminance versus the voltage applied to a group of OLEDs. The graph in FIG. 3 shows the luminance of an OLED where the phase shift layer 240 is formed of an organic or inorganic material on an opposite electrode and the reflecting layer 242 is formed of MgAg on the phase shift layer. The graph in FIG. 3 shows luminance data for an OLED with a reflecting layer that is 50Å (MgAg50Å) and 100Å (MgAg100Å) thick. The graph in FIG. 3 also shows luminance data for an OLED on which a polarizer is employed (POLARIZER).

[0036] Referring to FIG. 3, it can be seen that an OLED that employs a phase shift layer 240 and a reflecting layer 242 is much higher in luminance than an OLED on which only a polarizer is employed. It can be also seen that an OLED that employs a reflecting layer 242 that is 50Å thick is similar in luminance to one where the reflecting layer 242 is 100Å thick.

[0037] FIG. 4 shows a graph illustrating luminous efficiency versus luminance for the same group of OLEDs shown in FIG. 3. It can be seen that an OLED that uses a phase shift layer 240 and a reflecting layer 242 (MgAg50Å and MgAg1001Å) is much higher in luminous efficiency than in an OLED on which a polarizer is employed (POLARIZER).

[0038] An example of an embodiment of the present invention will now be described below. The embodiment is given as an example to further understanding of the present invention, but the scope of the present invention is not limited to the embodiment described.

40 <Embodiment 1>

[0039] An OLED was formed as follows. A pixel electrode 210 was formed on a substrate 200. An organic emission layer 220 was formed on the pixel electrode 210. The organic emission layer 220 was made to different thicknesses according to its color. Red and green emission layers were 400Å thick, and a blue emission layer was 150Å thick. An electron transport layer about 250Å to about 300Å thick was formed as part of the emission layer. An opposite electrode 230, 180Å thick was formed on the organic layer 220. A phase shift layer 240, 500Å to 800Å thick was formed on the opposite electrode 230. A reflecting layer 242, 80Å to 120Å thick was formed on the phase shift layer 240. The luminous efficiency of Embodiment 1 is shown in Table 1.

<Comparative Example 1>

[0040] An OLED was formed in the same way as Embodiment 1 except that a reflecting layer 242 was excluded. The luminous efficiency of Comparative Example 1 is shown in Table 1.

<Comparative Example 2>

[0041] An OLED was formed in the same way as Embodiment 1 except that a polarizer was formed on the opposite electrode instead of a phase shift layer 240 and a reflecting layer 242. The luminous efficiency of Comparative Example 2 is shown in Table 1.

[Table 1]

| | Red | Green | Blue |
|--------------|---------|--------|---------|
| Embodiment 1 | 8cd/A | 28cd/A | 1.4cd/A |
| Comparison 1 | 11cd/A | 56cd/A | 1.8cd/A |
| Comparison 2 | 4.8cd/A | 24cd/A | 0.8cd/A |

[0042] As shown in Table 1, the luminous efficiency of Embodiment 1 is 170% in red, 116% in green and 175% in blue, compared to Comparative Example 2.

[0043] FIG. 5A, FIG. 5B, and FIG. 5C show photographs illustrating OLEDs with different degrees of embodied blacks. FIG. 5A shows the embodied black when only a phase shift layer is stacked on an opposite electrode. FIG. 5B and FIG. 5C show the embodied black when a phase shift layer and a reflecting layer are stacked on an opposite electrode. The reflecting layer of FIG. 5B is 50Å thick, and the reflecting layer of FIG. 5C is 100Å thick. It can be seen that black, shown in the center of the photograph, is best realized when the reflecting layer is 100Å thick.

[0044] FIG. 6 shows a graph illustrating the reflectivity of various wavelengths of light of an OLED according to an exemplary embodiment of the present invention. In FIG. 6, "A" denotes the reflectivity of a pixel electrode alone, "B" denotes the reflectivity of aluminum (Al) alone, and "C" denotes the reflectivity of an opposite electrode with a phase shift layer 240 and a reflecting layer 242 stacked upon it.

[0045] As can be seen in FIG. 6, reflected external light is significantly reduced when the phase shift layer and the reflecting layer are stacked on the opposite electrode.

[0046] It will be apparent to those skilled in the art that various modifications and variation can be made in the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

Claims

1. A flat panel display, comprising:
 - 5 a pixel electrode disposed above a substrate; an organic layer including an emission layer disposed on the pixel electrode; an opposite electrode disposed on the organic layer;
 - 10 a phase shift layer disposed on the opposite electrode; and
 - 15 a reflecting layer disposed on the phase shift layer.
2. The flat panel display of claim 1, further comprising at least one thin film transistor disposed between the substrate and the pixel electrode.
- 20 3. The flat panel display of claim 1 or 2, wherein the pixel electrode is a reflecting electrode and/or wherein the organic layer further includes at least one additional layer selected from the group of a hole injection layer, a hole transport layer, a hole blocking layer, an electron transport layer, and an electron injection layer.
- 25 4. The flat panel display of claim 1-3, wherein the opposite electrode is formed of a material with a transmittance of about 10% to about 40% and/or wherein the opposite electrode is formed of a material selected from the group of Mg, MgAg, Ca, CaAg, Ag, AlCa, AlAg, LiMg, and Li and/or wherein the opposite electrode is about 150Å to about 250Å thick.
- 30 5. The flat panel display of claim 1-4, wherein the opposite electrode is about 180Å thick and/or wherein the phase shift layer is about 400Å to about 1300Å thick.
- 35 6. The flat panel display of claim 5, wherein the phase shift layer is about 500Å to about 800Å thick.
- 40 7. The flat panel display according to at least one of the preceding claims, wherein the phase shift layer is a transparent layer and/or wherein the transparent layer is an organic layer, an inorganic layer, or a stacked structure of an organic layer and an inorganic layer.
- 45 8. The flat panel display of claim 7, wherein the inorganic layer is formed of a material selected from the group of silicon nitride (SiNx), silicon oxide (SiO₂), silicon oxynitride (SiON), and a transparent conductive oxide (TCO).

9. The flat panel display according to at least one of the preceding claims,
wherein the reflecting layer is formed of a material that has a transmittance of about 40% to about 60% and/or wherein the reflecting layer is a semi-transparent metal layer and/or wherein the semi-transparent metal layer is formed of a material selected from the group of Mg, Ag, MgAg, Cr, Pt, and Au and/or wherein the reflecting layer is about 80Å to about 120Å thick. 10 transparent conductive oxide (TCO).

10. A method of fabricating a flat panel display, comprising:
forming a pixel electrode above a substrate, 15
forming an organic layer including an emission layer on the pixel electrode,
forming an opposite electrode on the organic layer,
forming a phase shift layer on the opposite electrode, and 20
forming a reflecting layer on the phase shift layer.

11. The method of claim 10, 25
further comprising forming at least one thin film transistor between the substrate and the pixel electrode.

12. The method of claim 10 or 11,
wherein the opposite electrode is formed of a material with a transmittance of about 10% to about 40% and/or wherein the opposite electrode is formed of a material selected from the group of Mg, MgAg, Ca, CaAg, Ag, AlCa, AlAg, LiMg, and Li and/or wherein the opposite electrode is 150Å to 250Å thick. 30 35

13. The method of claim 12,
wherein the opposite electrode is 170Å to 190Å, preferably 180Å thick. 40

14. The method according to at least one of the claims 10-13,
wherein the phase shift layer is 400Å to 1300Å thick.

15. The method of claim 14, 45
wherein the phase shift layer is 500Å to 800Å thick.

16. The method according to at least one of the claims 10-15,
wherein the phase shift layer is a transparent layer 50
and/or wherein the transparent layer is an organic layer, an inorganic layer, or a stacked structure of an organic layer and an inorganic layer.

17. The method of claim 16,
wherein the inorganic layer is formed of a material selected from the group of silicon nitride (SiNx), silicon oxide (SiO₂), silicon oxynitride (SiON), and a 55

FIG. 1

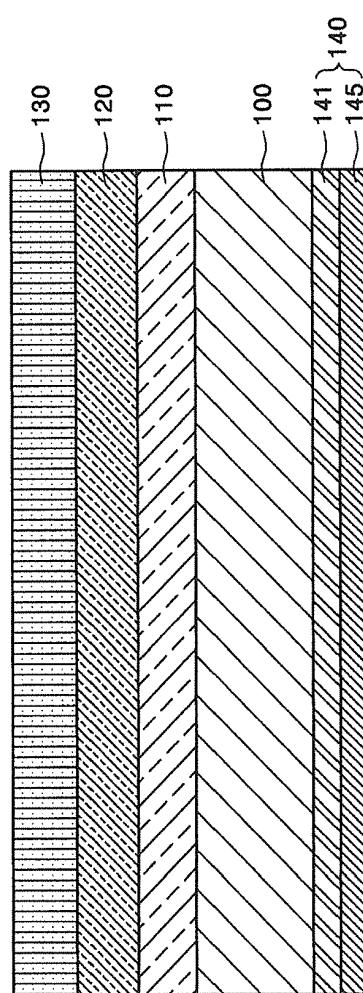


FIG. 2

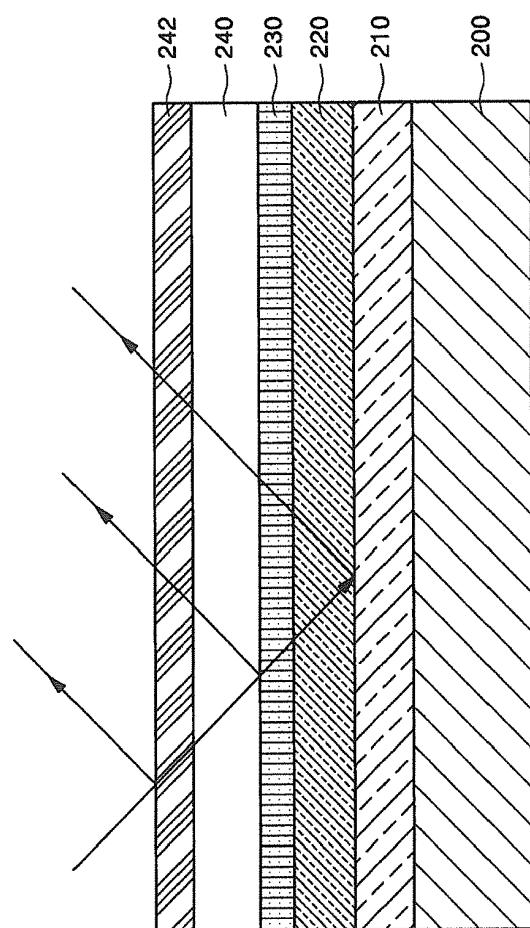


FIG. 3

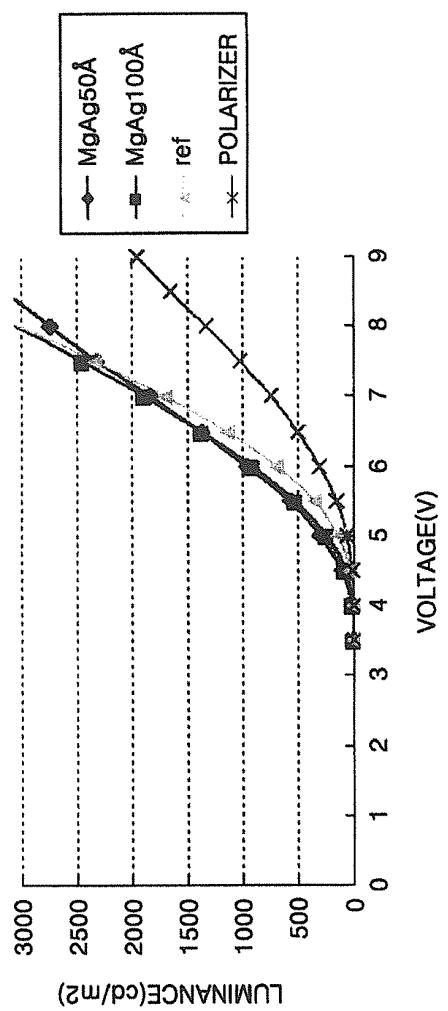


FIG. 4

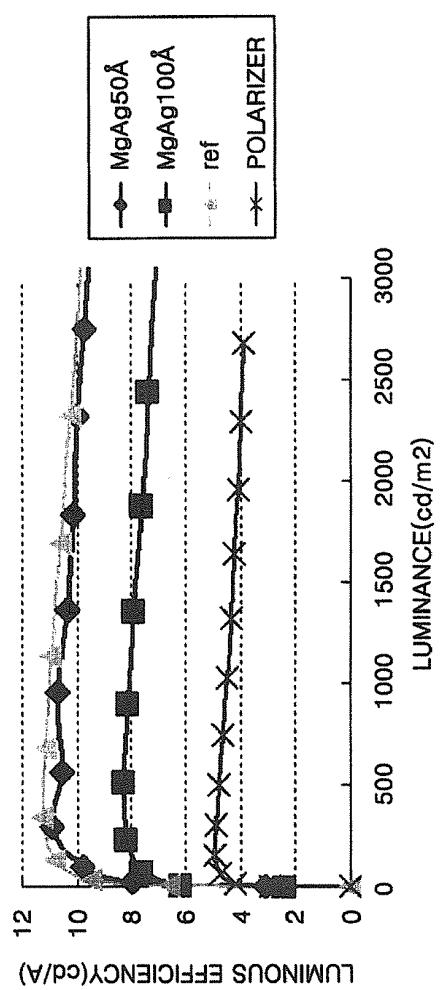


FIG. 5A

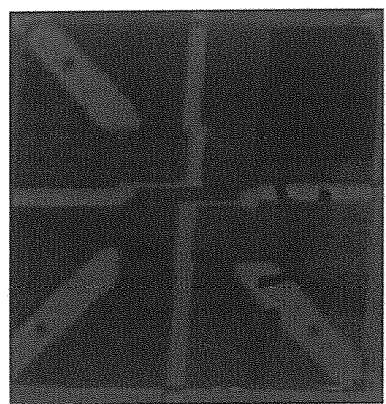


FIG. 5B

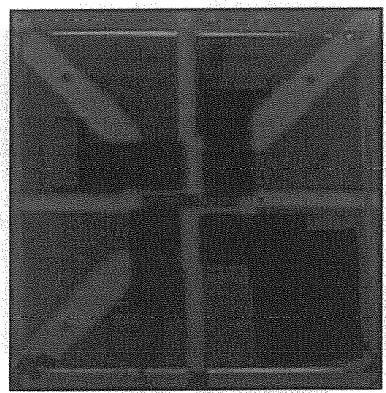


FIG. 5C

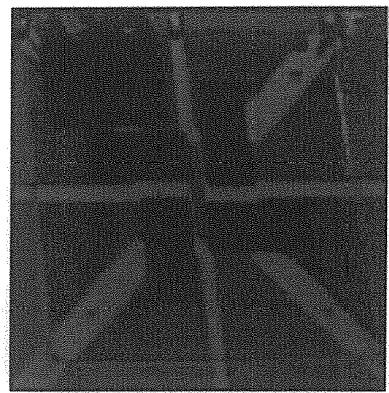
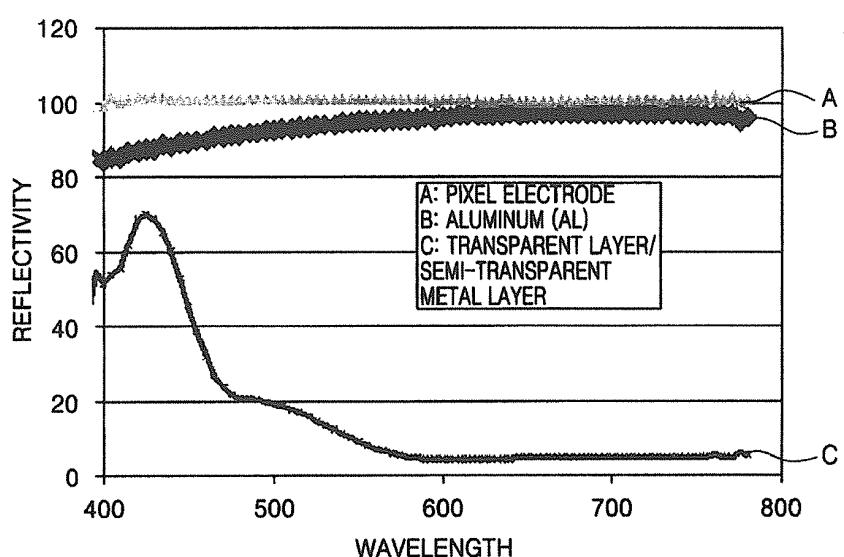


FIG. 6



| | | | |
|----------------|--|---------|------------|
| 专利名称(译) | 平板显示器及其制造方法 | | |
| 公开(公告)号 | EP1662574A2 | 公开(公告)日 | 2006-05-31 |
| 申请号 | EP2005111347 | 申请日 | 2005-11-28 |
| [标]申请(专利权)人(译) | 三星斯笛爱股份有限公司 | | |
| 申请(专利权)人(译) | 三星SDI CO. , LTD. | | |
| 当前申请(专利权)人(译) | 三星移动显示器有限公司. | | |
| 发明人 | LEE, KWAN-HEE, SAMSUNG SDI CO., LTD. IM, JA-HYUN, SAMSUNG SDI CO., LTD. | | |
| IPC分类号 | H01L27/32 H01L51/52 | | |
| CPC分类号 | H01L51/5265 H01L27/32 H01L51/5281 | | |
| 代理机构(译) | hengelhaupt , Jürgen | | |
| 优先权 | 1020040098877 2004-11-29 KR | | |
| 其他公开文献 | EP1662574B1 EP1662574A3 | | |
| 外部链接 | Espacenet | | |

摘要(译)

平板显示器包括像素电极，有机发光层，相对电极，相移层和设置在基板上的反射层。相移层和反射层堆叠在相对电极上，以破坏性地干扰反射的外部光，以实现黑色并实现优异的发光效率。

FIG. 2

