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(54) **DISPLAY APPARATUS**

(57) A display apparatus includes a plurality of pixels, each of the pixels including an organic light emitting diode, a first transistor providing a driving current to operate the organic light emitting diode, a second transistor including a (GEb) rode that receives a first scan signal, a first electrode that receives a data signal, and a second electrode electrically connected to the first electrode of

the first transistor, a storage capacitor including a first electrode receiving a first power voltage and a second electrode electrically connected to the gate electrode of the first transistor, and a color accuracy enhancement transistor that applies a first back bias voltage to the first transistor in response to a color accuracy enhancement signal.

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EUROPEAN SEARCH REPORT

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 2007/152919 A1 (TSENG CHANG-HO [TW] ET AL) 5 July 2007 (2007-07-05) * the whole document *	1-6	INV. G09G3/3233
X	JP 2005 004183 A (ADV LCD TECH DEV CT CO LTD) 6 January 2005 (2005-01-06) * figures 1,9,10 *	1-6	
			TECHNICAL FIELDS SEARCHED (IPC)
			G09G
-The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
The Hague		13 June 2019	Vázquez del Real, S
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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### CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing claims for which payment was due.

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Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s):

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No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due.

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### LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

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see sheet B

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All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.

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As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.

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Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:

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None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:

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1-6

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The present supplementary European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims (Rule 164 (1) EPC).

**LACK OF UNITY OF INVENTION  
SHEET B**

Application Number

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The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

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**1. claims: 1-6**

These claims refers to a display apparatus comprising OLED pixels wherein the OLED pixel comprises a color accuracy enhancement transistor that applies a back bias voltage to the dual gate driving transistor to avoid dispersion of the  $I_{ds}$  current for high  $V_{ds}$  values in the driving transistor.

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**2. claims: 1, 8-10**

These claims refers to a display apparatus comprising OLED pixels wherein the OLED pixel comprises a brightness boosting transistor that applies a back bias voltage to the dual gate driving transistor to increase  $I_{ds}$  current and hence overall brightness through the driving transistor

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**3. claims: 1, 7, 11, 12**

These claims refers to a display apparatus comprising OLED pixels wherein the OLED pixel comprises a color accuracy enhancement transistor and a brightness boosting transistor that are controlled independently and alternatively to apply different back bias voltages to the dual gate driving transistor either to avoid dispersion of the  $I_{ds}$  current for high  $V_{ds}$  values in the driving transistor or to increase or to increase  $I_{ds}$  current

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 19 16 1540

5 This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.  
The members are as contained in the European Patent Office EDP file on  
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

13-06-2019

10	Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

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CPC分类号	G09G3/3233 G09G2320/0233 G09G2300/043 G09G2300/0842 G09G2300/0861 G09G2320/0242 G09G2320/045 G09G2320/0626 G09G3/3225 G09G2300/0819 H01L27/3262		
代理机构(译)	DR.威猛和合作伙伴		
优先权	1020180028265 2018-03-09 KR		
其他公开文献	EP3537420A2		
外部链接	<a href="#">Espacenet</a>		

摘要(译)

一种显示装置，包括多个像素，每个像素包括有机发光二极管，提供驱动电流以操作有机发光二极管的第一晶体管，包括接收第一扫描信号的 ( GEB ) 杆的第二晶体管。接收数据信号的第一电极和电连接到第一晶体管的第一电极的第二电极，存储电容器包括接收第一电源电压的第一电极和电连接到第一晶体管的栅极的第二电极 晶体管和颜色精度增强晶体管，其响应于颜色精度增强信号而将第一反向偏置电压施加到第一晶体管。

		<b>EUROPEAN SEARCH REPORT</b>		Application Number EP 19 16 1540
<b>DOCUMENTS CONSIDERED TO BE RELEVANT</b>				
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE PATENT DOCUMENT (IPC)	
X	US 2007/152919 A1 (TSENG CHANG-HO [TW] ET AL) 5 July 2007 (2007-07-05) * the whole document *	1-6	INV. G09G3/3233	
X	JP 2005 004183 A (ADV LCD TECH DEV CT CO LTD) 6 January 2005 (2005-01-06) * Figures 1,9,10 * -----	1-6		
-The present search report has been drawn up for all claims-				
Place of search <b>The Hague</b>		Date of publication of the abstract <b>13 June 2019</b>		Examiner <b>Vázquez del Real, S</b>
<b>CATEGORY OF CITED DOCUMENTS</b>				
X : particularly relevant # taken alone Y : particularly relevant if considered with another document of the same category A : technological background P : intermediate document				
Z : thesis or priority underlying the invention E : earlier patent document, but published on, or after the filing date O : document cited in the application I : document cited for other reasons A : counterpart of the same patent family, corresponding document				