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(54) **ARRAY-TYPE DOUBLE-SIDED LIGHT EMITTING DEVICE AND MANUFACTURING METHOD THEREOF, AND DOUBLE-SIDED DISPLAY DEVICE**

(57) The present invention relates to an array-type double-side light-emitting device, a manufacturing method thereof and a double-side display device. The array-type double-side light-emitting device comprises: a first protective layer, a first fluorescent layer or quantum dot layer, an array of first transparent conductive layers, a first anisotropic conductive adhesive layer, an array of light-emitting wafers, a second anisotropic conductive adhesive layer, an array of second transparent conductive layers, a second fluorescent layer or quantum dot layer and a second protective layer, which are attached together sequentially.

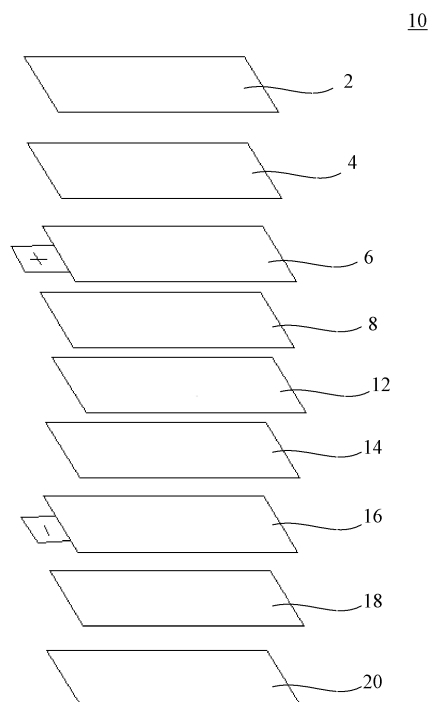


Fig. 1

Description

FIELD OF THE INVENTION

[0001] The present invention relates to the field of double-side display, and in particular to a double-side light-emitting device, an array-type double-side light-emitting device, a method for manufacturing an array-type double-side light-emitting device as well as a double-side display device.

BACKGROUND ART

[0002] A double-side light-emitting device in the prior art, e.g., a sandwich type light-emitting diode (LED) light source module with two sides emitting light as disclosed in CN203363722U, comprises an upper electricity conducting layer, an upper insulating layer, a heat conducting layer, a lower insulating layer and a lower electricity conducting layer stacked from top to bottom. The upper electricity conducting layer and the lower electricity conducting layer are both provided with a plurality of LED components. The sandwich type structural design lays a structural foundation for the LED light source module to emit light on two sides. The LED components are directly arranged on upper and lower surfaces to emit light on two sides. However, many defects exist, such as impossibility of flexible display, complication of structure and manufacturing process and a thicker overall thickness of the sandwich type LED light source module with two sides emitting light.

[0003] Therefore, there is an urgent need for improvement in the prior art.

SUMMARY

[0004] To this end, the present invention provides a double-side light-emitting device, an array-type double-side light-emitting device, a method for manufacturing an array-type double-side light-emitting device and a double-side display device capable of solving or at least alleviating at least part of the defects in the prior art.

[0005] According to a first aspect of the present invention, a double-side light-emitting device is provided, which can comprise: a first protective layer, a first fluorescent layer or quantum dot layer, a first transparent conductive layer, a first anisotropic conductive adhesive layer, a light-emitting wafer, a second anisotropic conductive adhesive layer, a second transparent conductive layer, a second fluorescent layer or quantum dot layer and a second protective layer, which are arranged together sequentially.

[0006] By means of the first anisotropic conductive adhesive layer, the light-emitting wafer is electrically connected to the first transparent conductive layer. By means of the second anisotropic conductive adhesive layer, the light-emitting wafer is electrically connected to the second transparent conductive layer. As compared with a

double-side light-emitting device in the prior art, the double-side light-emitting device in the present invention is simpler in structure and manufacture, and smaller in overall thickness, which facilitates the manufacture of a minimized ultrathin electronic device with two sides emitting light.

[0007] In an embodiment of the present invention, the double-side light-emitting device can further comprise: a first transparent gel layer arranged between the first fluorescent layer or quantum dot layer and the first transparent conductive layer, and a second transparent gel layer arranged between the second fluorescent layer or quantum dot layer and the second transparent conductive layer.

[0008] In another embodiment of the present invention, the first anisotropic conductive adhesive layer electrically connects the first transparent conductive layer to the light-emitting wafer, and the second anisotropic conductive adhesive layer electrically connects the second transparent conductive layer to the light-emitting wafer.

[0009] In yet another embodiment of the present invention, the first protective layer, the first fluorescent layer or quantum dot layer, the first transparent conductive layer, the first anisotropic conductive adhesive layer, the light-emitting wafer, the second anisotropic conductive adhesive layer, the second transparent conductive layer, the second fluorescent layer or quantum dot layer and the second protective layer are arranged plane-symmetrically about the light-emitting wafer.

[0010] In a further embodiment of the present invention, the light-emitting wafer is an LED light-emitting wafer emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

[0011] According to a second aspect of the present invention, an array-type double-side light-emitting device is provided, which can comprise: a first protective layer, a first fluorescent layer or quantum dot layer, an array of first transparent conductive layers, a first anisotropic conductive adhesive layer, an array of light-emitting wafers, a second anisotropic conductive adhesive layer, an array of second transparent conductive layers, a second fluorescent layer or quantum dot layer and a second protective layer, which are arranged together sequentially.

[0012] By means of the first anisotropic conductive adhesive layer and the second anisotropic conductive adhesive layer, the array of light-emitting wafers is electrically connected to the array of first transparent conductive layers and the array of second transparent conductive layers respectively. As compared with an array-type double-side light-emitting device in the prior art, the array-type double-side light-emitting device in the present invention achieves flexible display and hence is suitable for the manufacture of a flexible display device. It is simpler in structure, which helps to cut down costs, and smaller in overall thickness, which facilitates the manu-

facture of a minimized ultrathin electronic device with two sides emitting light.

[0013] In an embodiment of the present invention, the array-type double-side light-emitting device can further comprise: a first transparent gel layer arranged between the first fluorescent layer or quantum dot layer and the array of first transparent conductive layers, and a second transparent gel layer arranged between the second fluorescent layer or quantum dot layer and the array of second transparent conductive layers.

[0014] In another embodiment of the present invention, the first anisotropic conductive adhesive layer electrically connects each first transparent conductive layer in the array of first transparent conductive layers to each light-emitting wafer in the array of light-emitting wafers in a corresponding manner, and the second anisotropic conductive adhesive layer electrically connects each second transparent conductive layer in the array of second transparent conductive layers to each light-emitting wafer in the array of light-emitting wafers in a corresponding manner.

[0015] In yet another embodiment of the present invention, the first protective layer, the first fluorescent layer or quantum dot layer, the array of first transparent conductive layers, the first anisotropic conductive adhesive layer, the array of light-emitting wafers, the second anisotropic conductive adhesive layer, the array of second transparent conductive layers, the second fluorescent layer or quantum dot layer and the second protective layer are arranged plane-symmetrically about the array of light-emitting wafers.

[0016] In a further embodiment of the present invention, the array of light-emitting wafers comprises LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

[0017] According to a third aspect of the present invention, a method for manufacturing an array-type double-side light-emitting device is provided, comprising steps of: coating on or attaching to a first protective layer a first fluorescent layer or quantum dot layer; arranging an array of first transparent conductive layers on the first fluorescent layer or quantum dot layer; arranging a first anisotropic conductive adhesive layer on the array of first transparent conductive layers; arranging an array of light-emitting wafers on the first anisotropic conductive adhesive layer; arranging a second anisotropic conductive adhesive layer on the array of light-emitting wafers; arranging an array of second transparent conductive layers on the second anisotropic conductive adhesive layer; arranging a second fluorescent layer or quantum dot layer on the array of second transparent conductive layers; arranging a second protective layer on the second fluorescent layer or quantum dot layer; laminating the above layers such that each light-emitting wafer in the array of light-emitting wafers is electrically connected to a corresponding first

transparent conductive layer in the array of first transparent conductive layers and a corresponding second transparent conductive layer in the array of second transparent conductive layers.

[0018] In an embodiment of the present invention, wherein arranging an array of light-emitting wafers on the first anisotropic conductive adhesive layer comprises: the array of first transparent conductive layers comprises a first transparent conductive layer substrate and an array of first transparent conductive layer electrodes carried on the first transparent conductive layer substrate, and each light-emitting wafer in the array of light-emitting wafers is arranged on the first anisotropic conductive adhesive layer corresponding to each first transparent conductive layer electrode in the array of first transparent conductive layer electrodes.

[0019] In another embodiment of the present invention, wherein arranging a second anisotropic conductive adhesive layer on the array of light-emitting wafers comprises: the array of second transparent conductive layers comprises a second transparent conductive layer substrate and an array of second transparent conductive layer electrodes carried on the second transparent conductive layer substrate, and each second transparent conductive layer electrode in the array of second transparent conductive layer electrodes is arranged on the second anisotropic conductive adhesive layer corresponding to each light-emitting wafer in the array of light-emitting wafers.

[0020] In yet another embodiment of the present invention, wherein arranging an array of first transparent conductive layers on the first fluorescent layer or quantum dot layer comprises: arranging the array of first transparent conductive layers on the first fluorescent layer or quantum dot layer by means of a first transparent gel layer; wherein arranging a second fluorescent layer or quantum dot layer on an array of second transparent conductive layers comprises: arranging the second fluorescent layer or quantum dot layer on the array of second transparent conductive layers by means of a second transparent gel layer.

[0021] In a further embodiment of the present invention, the array of light-emitting wafers comprises LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

[0022] According to a fourth aspect of the present invention, a method for manufacturing an array-type double-side light-emitting device is provided, comprising steps of: attaching an array of first transparent conductive layers to an array of light-emitting wafers by means of a first anisotropic conductive adhesive layer; attaching the array of light-emitting wafers to an array of second transparent conductive layers by means of a second anisotropic conductive adhesive layer; laminating the array of first transparent conductive layers, the array of light-emitting

ting wafers and the array of second transparent conductive layers such that the array of first transparent conductive layers and the array of second transparent conductive layers are electrically connected to the array of light-emitting wafers; coating a first fluorescent layer or quantum dot layer and a second fluorescent layer or quantum dot layer respectively on a surface of the array of first transparent conductive layers and a surface of the array of second transparent conductive layers, and coating a first protective layer and a second protective layer respectively on the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer.

[0023] In an embodiment of the present invention, wherein attaching an array of first transparent conductive layers to an array of light-emitting wafers by means of a first anisotropic conductive adhesive layer comprises: the array of first transparent conductive layers comprises a first transparent conductive layer substrate and an array of first transparent conductive layer electrodes carried on the first transparent conductive layer substrate, and each first transparent conductive layer electrode in the array of first transparent conductive layer electrodes is attached to each light-emitting wafer in the array of light-emitting wafers by means of the first anisotropic conductive adhesive layer in a corresponding manner.

[0024] In another embodiment of the present invention, wherein attaching the array of light-emitting wafers to an array of second transparent conductive layers by means of a second anisotropic conductive adhesive layer comprises: the array of second transparent conductive layers comprises a second transparent conductive layer substrate and an array of second transparent conductive layer electrodes carried on the second transparent conductive layer substrate, and each light-emitting wafer in the array of light-emitting wafers is attached to each second transparent conductive layer electrode in the array of second transparent conductive layer electrodes by means of the second anisotropic conductive adhesive layer in a corresponding manner.

[0025] In yet another embodiment of the present invention, the array of light-emitting wafers comprises LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

[0026] According to a fifth aspect of the present invention, a double-side display device is provided, comprising the above double-side light-emitting device, the above array-type double-side light-emitting device, or an array-type double-side light-emitting device manufactured by using the above method for manufacturing an array-type double-side light-emitting device.

BRIEF DESCRIPTION OF DRAWINGS

[0027]

Fig. 1 schematically shows a double-side light-emitting device according to an embodiment of the present invention.

Fig. 2 schematically shows a double-side light-emitting device according to another embodiment of the present invention.

Fig. 3 schematically shows an array-type double-side light-emitting device according to an embodiment of the present invention.

Fig. 4 schematically shows an array of first transparent conductive layers according to an embodiment of the present invention.

Fig. 5 schematically shows a flow diagram of the manufacture of an array-type double-side light-emitting device according to an embodiment of the present invention.

Fig. 6 schematically shows a flow diagram of the manufacture of an array-type double-side light-emitting device according to another embodiment of the present invention.

Reference signs:

[0028] 2- first protective layer, 4- first fluorescent layer or quantum dot layer, 6- first transparent conductive layer, 8- first anisotropic conductive adhesive layer, 10- double-side light-emitting device, 12- light-emitting wafer, 14- second anisotropic conductive adhesive layer, 16- second transparent conductive layer, 18- second fluorescent layer or quantum dot layer, 20- second protective layer, 5- first transparent gel layer, 17- second transparent gel layer, 30- array-type double-side light-emitting device, 3- conductive electrode of first transparent conductive layer, 9- first transparent conductive layer substrate, 7- conductive line of first transparent conductive layer, D- symmetry plane, 15- conductive electrode of second transparent conductive layer, 11- second transparent conductive layer substrate, 13- conductive line of second transparent conductive layer.

DETAILED DESCRIPTION OF EMBODIMENTS

[0029] The present invention shall be described in details as follows with reference to the drawings of the present invention.

[0030] Fig. 1 schematically shows a double-side light-emitting device 10 according to a first aspect of the present invention, which can comprise: a first protective layer 2, a first fluorescent layer or quantum dot layer 4, a first transparent conductive layer 6, a first anisotropic conductive adhesive layer 8, a light-emitting wafer 12, a second anisotropic conductive adhesive layer 14, a second transparent conductive layer 16, a second fluorescent layer or quantum dot layer 18 and a second protective layer 20, which are arranged together sequentially. The first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 can be fluorescent layers made of yttrium aluminum garnet

(YAG) fluorescent powder or quantum dot layers obtained by thin coating quantum dots. Using the fluorescent layers or the quantum dot layers can help to improve the color gamut of the display and increase the brightness of the double-side light-emitting device.

[0031] The first anisotropic conductive adhesive layer 8 and the second anisotropic conductive adhesive layer 14 to be used in each embodiment of the present invention mainly comprise electrically conductive particles and insulating adhesive materials, and a protective film is provided on the upper and lower surfaces of an anisotropic conductive adhesive layer respectively for protecting the major components. The upper protective film is usually torn off firstly in use, and anisotropic conductive adhesive films are arranged onto electrodes of a substrate, and then the lower protective film is also torn off. After accurate alignment, objects above are laminated with underlying plates, and the insulating adhesive materials are cured after being heated and pressed for a certain period, and a stable structure which is perpendicularly conductive and laterally insulating is finally formed. Anisotropic conductive adhesive layers are mainly applied in a manufacturing process where high-temperature tin/lead soldering is inapplicable, e.g., for connecting lines of a flexible circuit board, a plastic credit card and a liquid crystal display, and mainly applied in fields relative to drive integrated circuits. In each embodiment of the present invention, by means of the first anisotropic conductive adhesive layer 8 and the second anisotropic conductive adhesive layer 14, the light-emitting wafer 12 is electrically connected to the first transparent conductive layer 6 and the second transparent conductive layer 16. As compared with a double-side light-emitting device in the prior art, the double-side light-emitting device in the present invention is simpler in structure and manufacture, and smaller in overall thickness.

[0032] The first transparent conductive layer 6 shown in Fig. 1 serves as an anode of the double-side light-emitting device, and the second transparent conductive layer 16 serves as a cathode of the double-side light-emitting device. The anode and the cathode are configured to receive a voltage and a current to be applied. It will be appreciated that, alternatively, the first transparent conductive layer 6 can also serve as a cathode of the double-side light-emitting device, and the second transparent conductive layer 16 can also serve as an anode of the double-side light-emitting device.

[0033] In an alternative embodiment of the present invention, the double-side light-emitting device can further comprise: a first transparent gel layer 5 arranged between the first fluorescent layer or quantum dot layer 4 and the first transparent conductive layer 6, and a second transparent gel layer 17 arranged between the second fluorescent layer or quantum dot layer 18 and the second transparent conductive layer 16. By means of the first transparent gel layer 5 and the second transparent gel layer 17, the first fluorescent layer or quantum dot layer 4 can also be arranged together with the first transparent

conductive layer 6, and the second fluorescent layer or quantum dot layer 18 can also be arranged together with the second transparent conductive layer 16. Fig. 4 shows a case for the first transparent gel layer 5 and the second transparent gel layer 17.

[0034] In one embodiment of the present invention, the first anisotropic conductive adhesive layer 8 electrically connects the first transparent conductive layer 6 to the light-emitting wafer 12, and the second anisotropic conductive adhesive layer 14 electrically connects the second transparent conductive layer 16 to the light-emitting wafer 12. As mentioned above, the first anisotropic conductive adhesive layer 8 and the second anisotropic conductive adhesive layer 14 comprise electrically conductive particles. The first anisotropic conductive adhesive layer 8, the second anisotropic conductive adhesive layer 14 and the light-emitting wafer 12 are laminated in a lamination step to be described hereinafter. Namely, a pressure is applied such that the first anisotropic conductive adhesive layer 8, the second anisotropic conductive adhesive layer 14 and the light-emitting wafer 12 are combined more firmly and that the light-emitting wafer 12 is electrically connected with conductive electrodes in the first anisotropic conductive adhesive layer 8 and the second anisotropic conductive adhesive layer 14 by means of the electrically conductive particles. The electrical connection by means of the electrically conductive particles will be described in detail hereinafter.

[0035] Alternatively, the first protective layer 2, the first fluorescent layer or quantum dot layer 4, the first transparent conductive layer 6, the first anisotropic conductive adhesive layer 8, the light-emitting wafer 12, the second anisotropic conductive adhesive layer 14, the second transparent conductive layer 16, the second fluorescent layer or quantum dot layer 18 and the second protective layer 20 are arranged plane-symmetrically about the light-emitting wafer 12. In other words, if the light-emitting wafer is taken as a symmetry plane, the first protective layer 2, the first fluorescent layer or quantum dot layer 4, the first transparent conductive layer 6, the first anisotropic conductive adhesive layer 8, the second anisotropic conductive adhesive layer 14, the second transparent conductive layer 16, the second fluorescent layer or quantum dot layer 18 and the second protective layer 20 are arranged plane-symmetrically. Fig. 1 basically shows such a symmetrical structure, and specific description of the symmetrical structure shall be described later.

[0036] Alternatively, the light-emitting wafer 12 can be an LED light-emitting wafer emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 emit light of complementary color selected from cyan, purple and yellow.

[0037] For example, when the light-emitting wafer 12 is an LED light-emitting wafer emitting red light, both the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 emit light of a complementary color, i.e., cyan light, such that the

double-side light-emitting device emits white light. When the light-emitting wafer 12 is an LED light-emitting wafer emitting green light, both the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 emit light of a complementary color, i.e., purple light, such that the double-side light-emitting device emits white light. When the light-emitting wafer 12 is an LED light-emitting wafer emitting blue light, both the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 emit light of a complementary color, i.e., yellow light, such that the double-side light-emitting device emits white light. In virtue of such arrangement, white light is generated by mixing, e.g., red light, green light and blue light emitted by the LED light-emitting wafer with the light of complementary colors emitted by the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18, e.g., cyan light, purple light or yellow light. Explanations are given here only by taking a double-side light-emitting device emitting white light as an example, and when it is necessary for the double-side light-emitting device to emit light of other colors, a person with ordinary skill in the art can arrange correspondingly the light-emitting wafer 12 and the corresponding first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 upon needs, which is not difficult to understand for the person with ordinary skill in the art.

[0038] Fig. 3 schematically shows an array-type double-side light-emitting device 30, which can comprise: a first protective layer 2, a first fluorescent layer or quantum dot layer 4, an array of first transparent conductive layers, a first anisotropic conductive adhesive layer 8, an array of light-emitting wafers 12, a second anisotropic conductive adhesive layer 14, an array of second transparent conductive layers, a second fluorescent layer or quantum dot layer 18 and a second protective layer 20, which are arranged together sequentially. The array of first transparent conductive layers shown in Fig. 3 comprises a plurality of conductive electrodes 3 of the first transparent conductive layer, a first transparent conductive layer substrate 9, and a plurality of conductive lines 7 of the first transparent conductive layer electrically connecting two adjacent conductive electrodes 3. Similarly, the array of second transparent conductive layers comprises a plurality of conductive electrodes 15 of the second transparent conductive layer, a second transparent conductive layer substrate 11, and a plurality of conductive lines 13 of the second transparent conductive layer electrically connecting two adjacent conductive electrodes 15. The array-type double-side light-emitting device 30 shown in Fig. 3 is essentially equivalent to an array of a plurality of double-side light-emitting devices shown in Fig. 1 or Fig. 2. The first protective layer 2, the first fluorescent layer or quantum dot layer 4, the first anisotropic conductive adhesive layer 8, the second anisotropic conductive adhesive layer 14, the second fluorescent layer or quantum dot layer 18 and the second protective layer 20

shown in Fig. 3 can be single layers respectively, e.g., the first protective layer 2 can be a single protective layer, instead of one in an array form, and the first fluorescent layer or quantum dot layer 4 can be a single fluorescent layer or quantum dot layer, instead of one in an array form. The array of first transparent conductive layers shown in Fig. 3 can be taken as a plurality of conductive electrodes 3 arranged on the first transparent conductive layer substrate 9, these conductive electrode 3 being electrically connected together by means of the plurality of conductive lines 7. Similarly, the array of second transparent conductive layers can be taken as a plurality of conductive electrodes 15 arranged on the second transparent conductive layer substrate 11, these conductive electrode 15 being electrically connected together by means of the plurality of conductive lines 13. The array of light-emitting wafers is an array composed of a plurality of light-emitting wafers 12. Fig. 4 schematically shows an array of first transparent conductive layers composed of a plurality of first transparent conductive layers 6 according to an embodiment of the present invention. Although Fig. 4 schematizes the array of first transparent conductive layers 6, it can also be used for describing the array of second transparent conductive layers.

[0039] Alternatively, the array-type double-side light-emitting device 30 can further comprise: a first transparent gel layer 5 arranged between the first fluorescent layer or quantum dot layer 4 and the first transparent conductive layer 6 array, and a second transparent gel layer 17 arranged between the second fluorescent layer or quantum dot layer 18 and the second transparent conductive layer 16 array as shown in Fig. 2. Since the array-type double-side light-emitting device 30 shown in Fig. 3 can be regarded as an array of a plurality of double-side light-emitting devices shown in Fig. 1 or Fig. 2, references to contents shown in Fig. 1 and Fig. 2 can be made for the array-type double-side light-emitting device 30 described herein.

[0040] In an embodiment of the array-type double-side light-emitting device 30 of the present invention, the first anisotropic conductive adhesive layer 8 electrically connects each first transparent conductive layer in the array of first transparent conductive layers to each light-emitting wafer 12 in the array of light-emitting wafers 12 in a corresponding manner, and the second anisotropic conductive adhesive layer 14 electrically connects each second transparent conductive layer 16 in the array of second transparent conductive layers to each light-emitting wafer in the array of light-emitting wafers 12 in a corresponding manner. Fig. 3 shows that the array of first transparent conductive layers comprises a first transparent conductive layer substrate 9 and an array of conductive electrodes 3 carried on the first transparent conductive layer substrate 9, and each light-emitting wafer in the array of light-emitting wafers 12 is arranged on the first anisotropic conductive adhesive layer 8 corresponding to each conductive electrode 3 in the array of first transparent conductive layer electrodes. Similarly, Fig. 3

shows that the array of second transparent conductive layers comprises a second transparent conductive layer substrate 11 and an array of conductive electrodes 15 carried on the second transparent conductive layer substrate 11, and each light-emitting wafer in the array of light-emitting wafers 12 is arranged on the second anisotropic conductive adhesive layer 18 corresponding to each conductive electrode 15 in the array of second transparent conductive layer electrodes.

[0041] In an embodiment of the array-type double-side light-emitting device 30, the first protective layer 2, the first fluorescent layer or quantum dot layer 4, the array of first transparent conductive layers 6, the first anisotropic conductive adhesive layer 8, the array of light-emitting wafers 12, the second anisotropic conductive adhesive layer 14, the array of second transparent conductive layers 16, the second fluorescent layer or quantum dot layer 18 and the second protective layer 20 are arranged plane-symmetrically about the array of light-emitting wafers 12. For example, Fig. 3 shows that the first protective layer 2, the first fluorescent layer or quantum dot layer 4, the array of first transparent conductive layers 6, the first anisotropic conductive adhesive layer 8, the second anisotropic conductive adhesive layer 14, the array of second transparent conductive layers 16, the second fluorescent layer or quantum dot layer 18 and the second protective layer 20 are arranged plane-symmetrically about the symmetry plane D. The symmetry plane D is a plane in which the plurality of light-emitting wafers 12 are located. The person with ordinary skill in the art can understand that in case of a curved array-type double-side light-emitting device, the array-type double-side light-emitting device 30 shown in Fig. 3 may be bended in a curved shape. Accordingly the symmetry plane D should also be in a curved shape, which is not difficult to understand.

[0042] Alternatively, the array of light-emitting wafers composed of a plurality of light-emitting wafers 12 can comprise LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer 6 and the second fluorescent layer or quantum dot layer 18 emit light of complementary color selected from cyan, purple and yellow. As mentioned above, for example when the light-emitting wafer 12 is an LED light-emitting wafer emitting red light, both the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 emit light of a complementary color, i.e., cyan light, such that the double-side light-emitting device emits white light. When the light-emitting wafer 12 is an LED light-emitting wafer emitting green light, both the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 emit light of a complementary color, i.e., purple light, such that the double-side light-emitting device emits white light. When the light-emitting wafer 12 is an LED light-emitting wafer emitting blue light, both the first fluorescent layer or quantum dot layer 4 and the second fluorescent layer or quantum dot layer 18 emit

light of a complementary color, i.e., yellow light, such that the double-side light-emitting device emits white light.

[0043] Fig. 5 shows a method for manufacturing an array-type double-side light-emitting device, which can comprise the following steps: S102, coating on or attaching to a first protective layer a first fluorescent layer or quantum dot layer; S104, arranging an array of first transparent conductive layers on the first fluorescent layer or quantum dot layer; S106, arranging a first anisotropic conductive adhesive layer on the array of first transparent conductive layers; S108, arranging an array of light-emitting wafers on the first anisotropic conductive adhesive layer; S110, arranging a second anisotropic conductive adhesive layer on the array of light-emitting wafers; S112, arranging an array of second transparent conductive layers on the second anisotropic conductive adhesive layer; S114, arranging a second fluorescent layer or quantum dot layer on the array of second transparent conductive layers; S116, arranging a second protective layer on the second fluorescent layer or quantum dot layer; S118, laminating the above layers such that each light-emitting wafer in the array of light-emitting wafers is electrically connected to a corresponding first transparent conductive layer in the array of first transparent conductive layers and a corresponding second transparent conductive layer in the array of second transparent conductive layers. The above steps of manufacturing an array-type double-side light-emitting device is equivalent to arranging together the first protective layer 2, the first fluorescent layer or quantum dot layer 4, the first transparent conductive layer 4, the first anisotropic conductive adhesive layer 8, the array of light-emitting wafers 12, the second anisotropic conductive adhesive layer 14, the array of second transparent conductive layers 16, the second fluorescent layer or quantum dot layer 18 and the second protective layer 20 in the above sequence. Finally, the above layers are laminated such that each light-emitting wafer in the array of light-emitting wafers is electrically connected to a corresponding first transparent conductive layer in the array of first transparent conductive layers and a corresponding second transparent conductive layer in the array of second transparent conductive layers.

[0044] In an embodiment of the method for manufacturing an array-type double-side light-emitting device, wherein the step S108 of arranging the array of light-emitting wafers on the first anisotropic conductive gel layer can further comprise a step of S109, providing a first transparent conductive layer substrate, arranging an array of first transparent conductive layer electrodes on the first transparent conductive layer substrate to form the array of first transparent conductive layer, and arranging the array of light-emitting wafers on the first anisotropic conductive adhesive layer in such a manner that each light-emitting wafer in the array of light-emitting wafers corresponds to each first transparent conductive layer electrode in the array of first transparent conductive layer electrodes.

[0045] In an embodiment of the method for manufac-

turing an array-type double-side light-emitting device, wherein the step S112 of arranging the array of second transparent conductive layers on the second anisotropic conductive adhesive layer can further comprise a step of S113, providing a second transparent conductive layer substrate, arranging an array of second transparent conductive layer electrodes on the second transparent conductive layer substrate to form the array of second transparent conductive layers, and arranging the array of second transparent conductive layers on the second anisotropic conductive adhesive layer in such a manner that each second transparent conductive layer electrode in the array of second transparent conductive layer electrodes corresponds to each light-emitting wafer in the array of light-emitting wafers.

[0046] Alternatively, wherein the step S104 of arranging the array of first transparent conductive layers on the first fluorescent layer or quantum dot layer can comprise a step of arranging the array of first transparent conductive layers on the first fluorescent layer or quantum dot layer by means of a first transparent gel layer; and the step S114 of arranging the second fluorescent layer or quantum dot layer on the array of second transparent conductive layers can comprise a step of arranging the second fluorescent layer or quantum dot layer on the array of second transparent conductive layers by means of a second transparent gel layer.

[0047] Optionally, the array of light-emitting wafers comprises LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

[0048] Fig. 6 shows a method of manufacturing an array-type double-side light-emitting device, which can comprise steps of: S202, attaching an array of first transparent conductive layers to an array of light-emitting wafers by means of a first anisotropic conductive adhesive layer; S204, attaching the array of light-emitting wafers to an array of second transparent conductive layers by means of a second anisotropic conductive adhesive layer; S206, laminating the array of first transparent conductive layers, the array of light-emitting wafers and the array of second transparent conductive layers such that the array of first transparent conductive layers and the array of second transparent conductive layers are electrically connected to the array of light-emitting wafers; and S208, coating a first fluorescent layer or quantum dot layer and a second fluorescent layer or quantum dot layer on a surface of the array of first transparent conductive layers and a surface of the array of second transparent conductive layers respectively, and coating a first protective layer and a second protective layer on the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer respectively. The method of manufacturing an array-type double-side light-emitting device shown in Fig. 6 is equivalent to, centering on an array of the light-emitting wafers, sequentially ar-

ranging an array of first transparent conductive layers on a surface of an array of light-emitting wafers (by means of a first anisotropic conductive adhesive layer), an array of second transparent conductive layers on an opposite surface of the array of light-emitting wafers (by means of a second anisotropic conductive adhesive layer), and then sequentially coating a first fluorescent layer or quantum dot layer and a second fluorescent layer or quantum dot layer on a surface of a corresponding array of transparent conductive layers respectively and coating a first protective layer and a second protective layer on a surface of the corresponding first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer respectively.

[0049] In an embodiment of the method for manufacturing an array-type double-side light-emitting device of the present invention, wherein the step S202 of attaching an array of first transparent conductive layers to an array of light-emitting wafers by means of a first anisotropic conductive adhesive layer can further comprise a step of S203, providing a first transparent conductive layer substrate, arranging an array of first transparent conductive layer electrodes on the first transparent conductive layer substrate to form the array of first transparent conductive layers, and attaching each first transparent conductive layer electrode in the array of first transparent conductive layer electrodes to each light-emitting wafer in the array of light-emitting wafers in a corresponding manner by means of the first anisotropic conductive adhesive layer.

[0050] Likewise, in an embodiment of the method for manufacturing an array-type double-side light-emitting device of the present invention, wherein the step S204 of attaching the array of light-emitting wafers to the array of second transparent conductive layers by means of the second anisotropic conductive adhesive layer can further comprise a step of S205, providing a second transparent conductive layer substrate, arranging an array of second transparent conductive layer electrodes carried on the second transparent conductive layer substrate to form the array of second transparent conductive layers, and attaching each light-emitting wafer in the array of light-emitting wafers to each second transparent conductive layer electrode in the array of second transparent conductive layer electrodes in a corresponding manner by means of the second anisotropic conductive adhesive layer.

[0051] Alternatively, wherein the array of light-emitting wafers can comprise LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

[0052] According to a fifth aspect of the present invention, a double-side display device is provided, comprising the above double-side light-emitting device, the above array-type double-side light-emitting device, or an array-type double-side light-emitting device manufactured by

using the above method for manufacturing an array-type double-side light-emitting device.

[0053] Although the present invention has been described with reference to the embodiments currently taken into consideration, it should be understood that the present invention is not limited to the disclosed embodiments. On the contrary, the present invention aims to cover various modifications and equivalent arrangements within the spirit and scope of the appended claims. The scopes of the following claims are subject to the broadest explanations so as to include each of such modifications and equivalent structures and functions.

Claims

1. A double-side light-emitting device (10), comprising:
a first protective layer (2), a first fluorescent layer or quantum dot layer (4), a first transparent conductive layer (6), a first anisotropic conductive adhesive layer (8), a light-emitting wafer (12), a second anisotropic conductive adhesive layer (14), a second transparent conductive layer (16), a second fluorescent layer or quantum dot layer (18) and a second protective layer (20), which are arranged together sequentially.
2. The double-side light-emitting device according to claim 1, **characterized in that** the double-side light-emitting device further comprises: a first transparent gel layer arranged between the first fluorescent layer or quantum dot layer and the first transparent conductive layer, and a second transparent gel layer arranged between the second fluorescent layer or quantum dot layer and the second transparent conductive layer.
3. The double-side light-emitting device according to any one of claims 1-2, **characterized in that** the first anisotropic conductive adhesive layer electrically connects the first transparent conductive layer to the light-emitting wafer, and the second anisotropic conductive adhesive layer electrically connects the second transparent conductive layer to the light-emitting wafer.
4. The double-side light-emitting device according to any one of claims 1-2, **characterized in that** the first protective layer, the first fluorescent layer or quantum dot layer, the first transparent conductive layer, the first anisotropic conductive adhesive layer, the light-emitting wafer, the second anisotropic conductive adhesive layer, the second transparent conductive layer, the second fluorescent layer or quantum dot layer and the second protective layer are arranged plane-symmetrically about the light-emitting wafer.
5. The double-side light-emitting device according to claim 4, **characterized in that** the light-emitting wafer is an LED light-emitting wafer emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.
6. An array-type double-side light-emitting device (30), comprising:
a first protective layer (2), a first fluorescent layer or quantum dot layer (4), an array of first transparent conductive layers (6), a first anisotropic conductive adhesive layer (8), an array of light-emitting wafers (12), a second anisotropic conductive adhesive layer, an array of second transparent conductive layers, a second fluorescent layer or quantum dot layer and a second protective layer, which are arranged together sequentially.
7. The array-type double-side light-emitting device according to claim 6, **characterized in that** the array-type double-side light-emitting device further comprises:
a first transparent gel layer arranged between the first fluorescent layer or quantum dot layer and the array of first transparent conductive layers, and a second transparent gel layer arranged between the second fluorescent layer or quantum dot layer and the array of second transparent conductive layers.
8. The array-type double-side light-emitting device according to any one of claims 6-7, **characterized in that** the first anisotropic conductive adhesive layer electrically connects each first transparent conductive layer in the array of first transparent conductive layers to each light-emitting wafer in the array of light-emitting wafers in a corresponding manner, and the second anisotropic conductive adhesive layer electrically connects each second transparent conductive layer in the array of second transparent conductive layers to each light-emitting wafer in the array of light-emitting wafers in a corresponding manner.
9. The array-type double-side light-emitting device according to any one of claims 6-7, **characterized in that** the first protective layer, the first fluorescent layer or quantum dot layer, the array of first transparent conductive layers, the first anisotropic conductive adhesive layer, the array of light-emitting wafers, the second anisotropic conductive adhesive layer, the array of second transparent conductive layers, the second fluorescent layer or quantum dot layer and the second protective layer are arranged plane-symmetrically about the array of light-emitting wafers.
10. The array-type double-side light-emitting device according to claim 9, **characterized in that** the array

of light-emitting wafers comprises LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

11. A method for manufacturing an array-type double-side light-emitting device, comprising steps of:

S102, coating or arranging a first fluorescent layer or quantum dot layer on a first protective layer;
 S104, arranging an array of first transparent conductive layers on the first fluorescent layer or quantum dot layer;
 S106, arranging a first anisotropic conductive adhesive layer on the array of first transparent conductive layers;
 S108, arranging an array of light-emitting wafers on the first anisotropic conductive adhesive layer;
 S110, arranging a second anisotropic conductive adhesive layer on the array of light-emitting wafers;
 S112, arranging an array of second transparent conductive layers on the second anisotropic conductive adhesive layer;
 S114, arranging a second fluorescent layer or quantum dot layer on the array of second transparent conductive layers;
 S116, arranging a second protective layer on the second fluorescent layer or quantum dot layer; and
 S118, laminating the layers such that each light-emitting wafer in the array of light-emitting wafers is electrically connected to a corresponding first transparent conductive layer in the array of first transparent conductive layers and a corresponding second transparent conductive layer in the array of second transparent conductive layers.

12. The method according to claim 11, wherein the step S108 of arranging the array of light-emitting wafers on the first anisotropic conductive gel layer comprises a step of:

S109, providing a first transparent conductive layer substrate, arranging an array of first transparent conductive layer electrodes on the first transparent conductive layer substrate to form the array of first transparent conductive layer, and arranging the array of light-emitting wafers on the first anisotropic conductive adhesive layer in such a manner that each light-emitting wafer in the array of light-emitting wafers corresponds to each first transparent conductive layer electrode in the array of first transparent conductive layer electrodes.

13. The method according to claim 11, wherein the step S112 of arranging the array of second transparent conductive layers on the second anisotropic conductive adhesive layer comprises a step of:

S113, providing a second transparent conductive layer substrate, arranging an array of second transparent conductive layer electrodes on the second transparent conductive layer substrate to form the array of second transparent conductive layers, and arranging the array of second transparent conductive layers on the second anisotropic conductive adhesive layer in such a manner that each second transparent conductive layer electrode in the array of second transparent conductive layer electrodes corresponds to each light-emitting wafer in the array of light-emitting wafers.

14. The method according to any one of claims 11-13, wherein the step S104 of arranging the array of first transparent conductive layers on the first fluorescent layer or quantum dot layer comprises a step of arranging the array of first transparent conductive layers on the first fluorescent layer or quantum dot layer by means of a first transparent gel layer, and the step S114 of arranging the second fluorescent layer or quantum dot layer on the array of second transparent conductive layers comprises a step of arranging the second fluorescent layer or quantum dot layer on the array of second transparent conductive layers by means of a second transparent gel layer.

15. The method according to any one of claims 11-13, wherein the array of light-emitting wafers comprises LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.

16. A method for manufacturing an array-type double-side light-emitting device, comprising steps of:

S202, attaching an array of first transparent conductive layers to an array of light-emitting wafers by means of a first anisotropic conductive adhesive layer;

S204, attaching the array of light-emitting wafers to an array of second transparent conductive layers by means of a second anisotropic conductive adhesive layer;

S206, laminating the array of first transparent conductive layers, the array of light-emitting wafers and the array of second transparent conductive layers such that the array of first transparent conductive layers and the array of second transparent conductive layers are electrically connected to the array of light-emitting wafers;

and
 S208, coating a first fluorescent layer or quantum dot layer and a second fluorescent layer or quantum dot layer respectively on a surface of the array of first transparent conductive layers and a surface of the array of second transparent conductive layers, and coating a first protective layer and a second protective layer respectively on the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer.

by the method according to any one of claims 11-15, or an array-type double-side light-emitting device manufactured by the method according to any one of claims 16-19.

17. The method according to claim 16, wherein the step S202 of attaching the array of first transparent conductive layers to the array of light-emitting wafers by means of the first anisotropic conductive adhesive layer comprises a step of:
 S203, providing a first transparent conductive layer substrate, arranging an array of first transparent conductive layer electrodes on the first transparent conductive layer substrate to form the array of first transparent conductive layers, and attaching each first transparent conductive layer electrode in the array of first transparent conductive layer electrodes to each light-emitting wafer in the array of light-emitting wafers in a corresponding manner by means of the first anisotropic conductive adhesive layer.
18. The method according to claim 16, wherein the step S204 of attaching the array of light-emitting wafers to the array of second transparent conductive layers by means of the second anisotropic conductive adhesive layer comprises a step of:
 S205, providing a second transparent conductive layer substrate, arranging an array of second transparent conductive layer electrodes carried on the second transparent conductive layer substrate to form the array of second transparent conductive layers, and attaching each light-emitting wafer in the array of light-emitting wafers to each second transparent conductive layer electrode in the array of second transparent conductive layer electrodes in a corresponding manner by means of the second anisotropic conductive adhesive layer.
19. The method according to any one of claims 16-18, wherein the array of light-emitting wafers comprises LED light-emitting wafers emitting red light, green light or blue light, and both the first fluorescent layer or quantum dot layer and the second fluorescent layer or quantum dot layer emit light of complementary color selected from cyan, purple and yellow.
20. A double-side display device, comprising the double-side light-emitting device according to any one of claims 1-5, the array-type double-side light-emitting device according to any one of claims 6-10, an array-type double-side light-emitting device manufactured

10

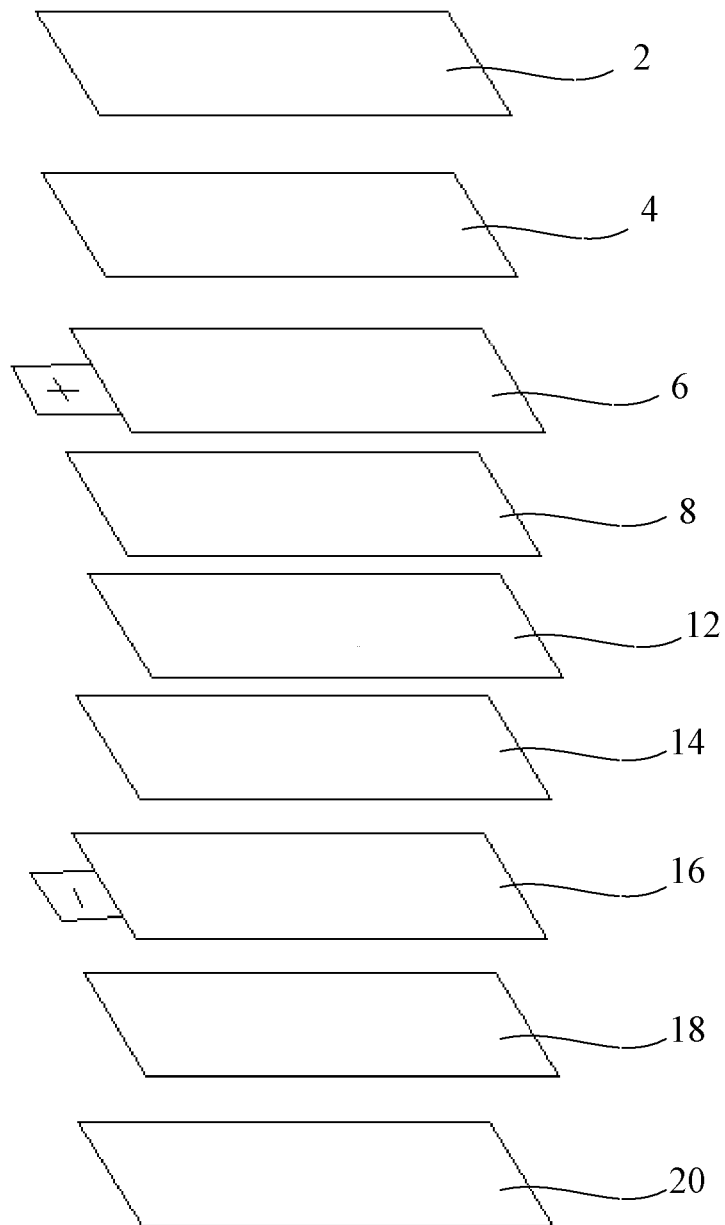


Fig. 1

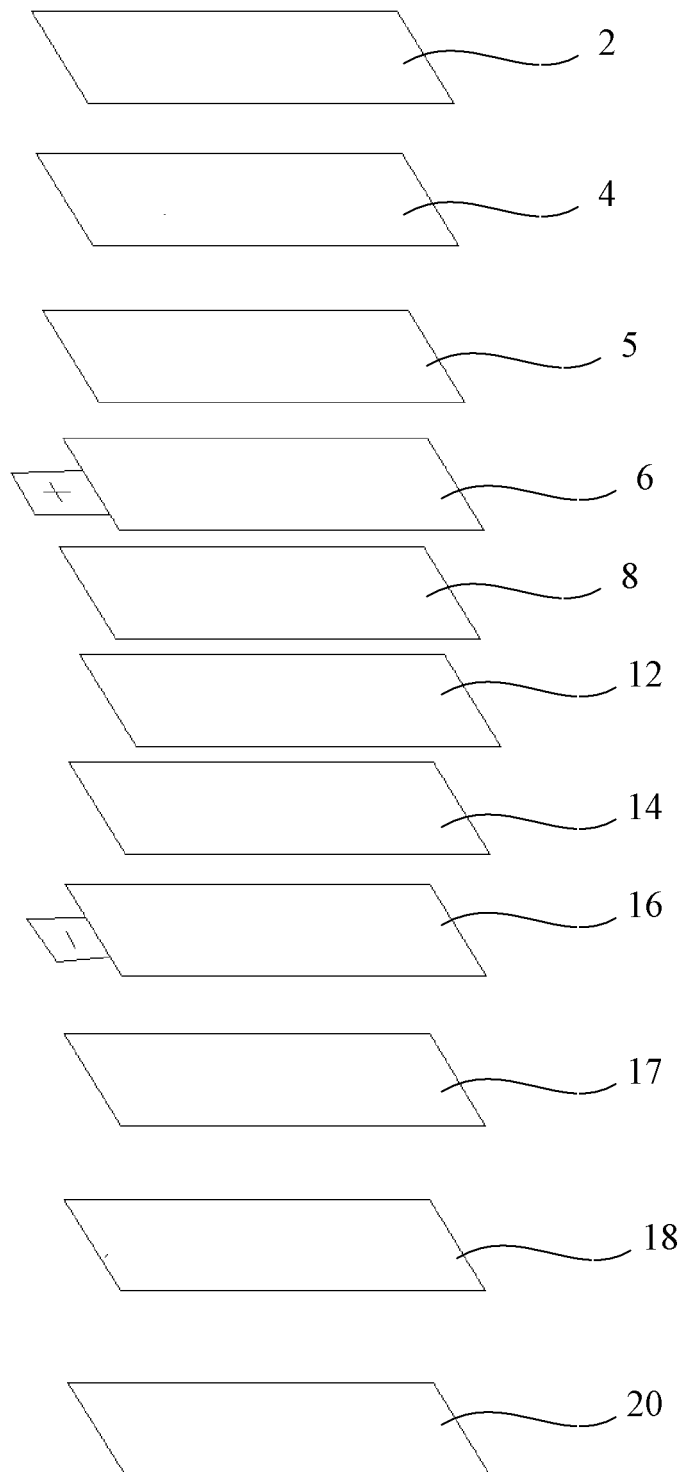


Fig. 2

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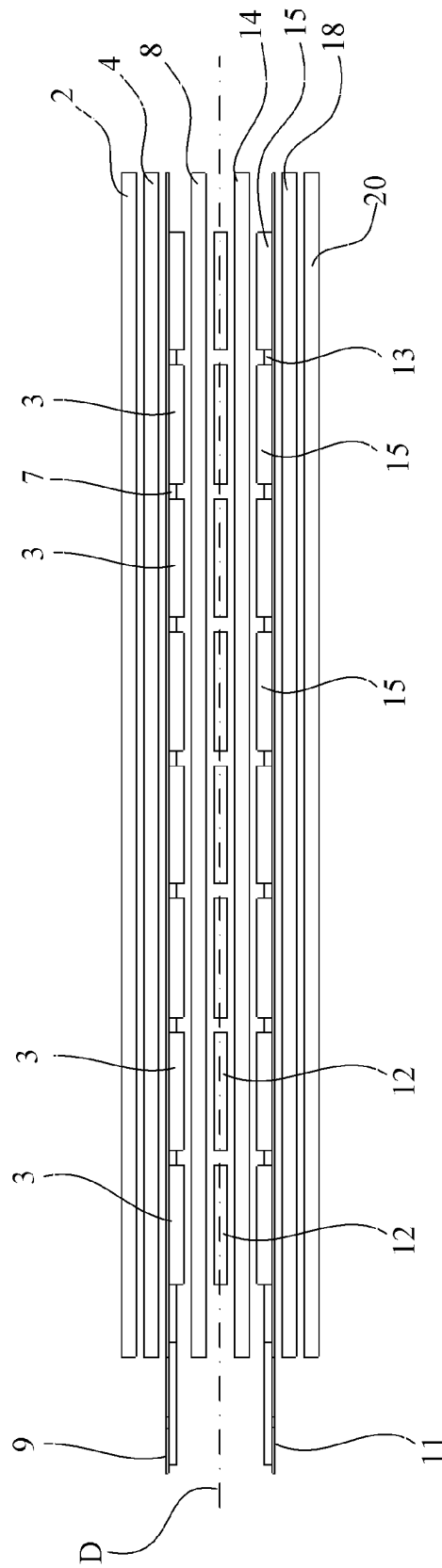


Fig. 3

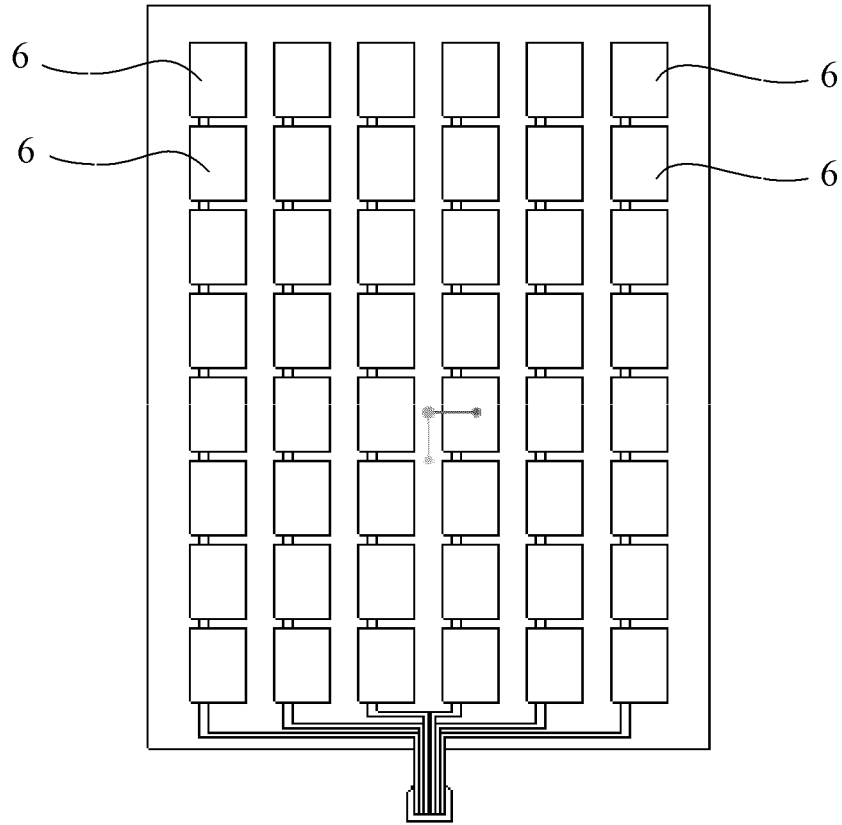


Fig. 4

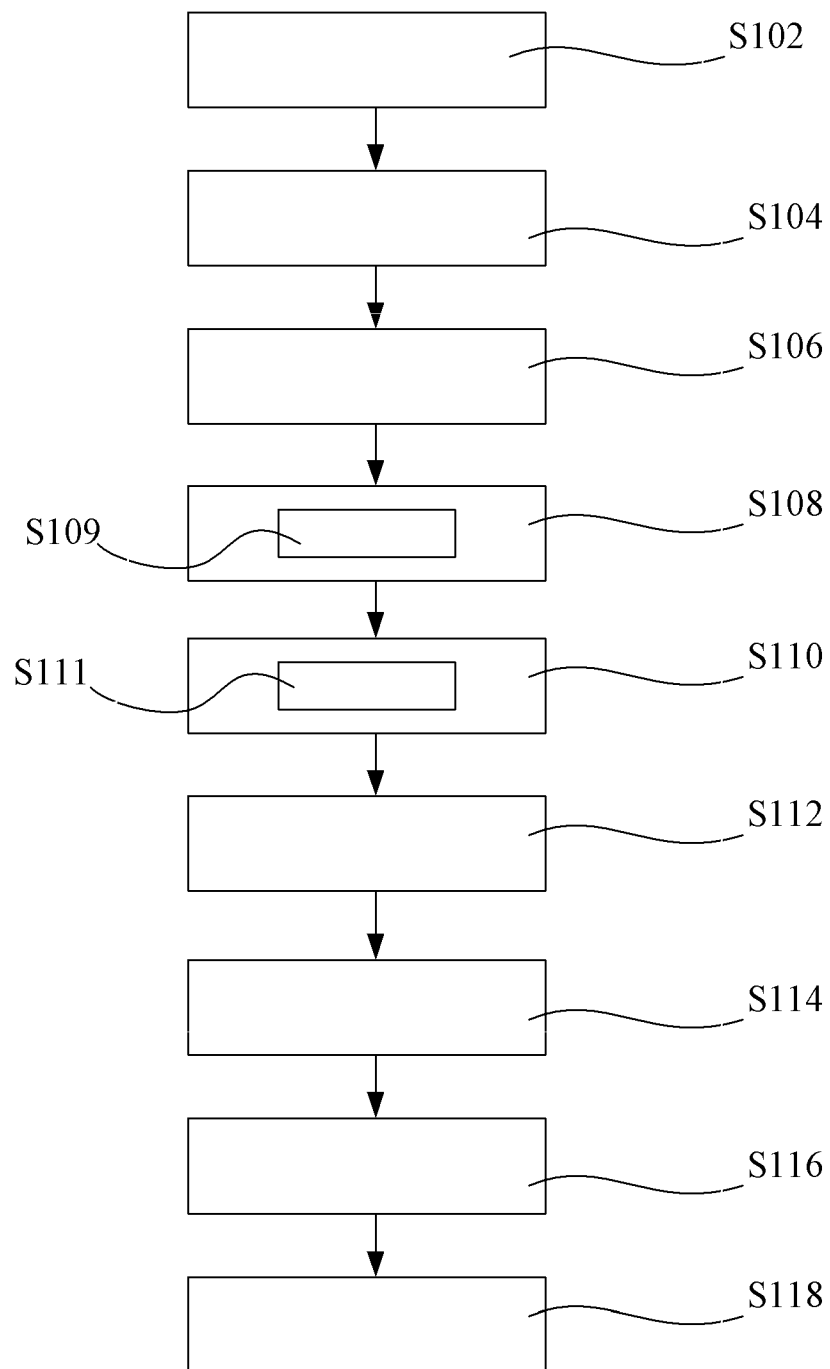


Fig. 5

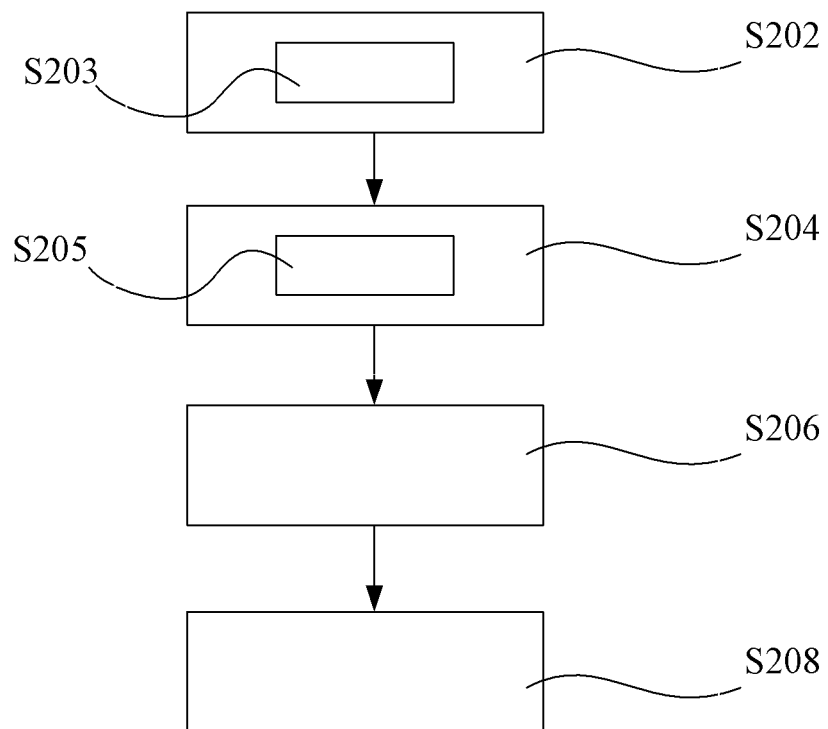


Fig. 6

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2015/091433

A. CLASSIFICATION OF SUBJECT MATTER

H01L 27/15 (2006.01) i; H01L 21/77 (2006.01) i; F21S 2/00 (2016.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L; F21S

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNABS, CNKI; VEN: double side+, light emitting, protective layer, conductive film, conductive gule, fluorescence material layer, light emitting diode, LED

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	CN 202940270 U (KUNSHAN KAIWEI ELECTRONIC CO., LTD.) 15 May 2013 (15.05.2013) description, paragraphs [0024]-[0040] and figure 1	1-20
Y	TW 201503424 A (LIGITEK ELECTRONICS CO., LTD.) 16 January 2015 (16.01.2015) description, page 3, line 23 to page 7, line 1 and figure 2	1-20
A	US 2008084159 A1 (FERY et al.) 10 April 2008 (10.04.2008) the whole document	1-20
A	CN 202796951 U (SUZHOU JINGPIN PHOTOELECTRIC TECHNOLOGY CO., LTD.) 13 March 2013 (13.03.2013) the whole document	1-20

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	"&" document member of the same patent family

Date of the actual completion of the international search 16 March 2016	Date of mailing of the international search report 01 July 2016
Name and mailing address of the ISA State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No. (86-10) 62019451	Authorized officer JIANG, Yujing Telephone No. (86-10) 62411559

Form PCT/ISA /210 (second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT
 Information on patent family members

 International application No.
 PCT/CN2015/091433

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
CN 202940270 U	15 May 2013	None	
TW 201503424 A	16 January 2015	None	
US 2008084159 A1	10 April 2008	CN 100533810 C	26 August 2009
		CN 101080827 A	28 November 2007
		WO 2006067200 A3	21 December 2006
		KR 20070087616 A	28 August 2007
		JP 5762667 B2	12 August 2015
		KR 101321956 B1	25 October 2013
		WO 2006067200 A2	29 June 2006
		EP 1836737 A2	26 September 2007
		JP 2008524819 A	10 July 2008
		IN 200704334 P1	31 August 2007
CN 202796951 U	13 March 2013	None	

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

- CN 203363722 U [0002]

专利名称(译)	阵列型双面发光装置及其制造方法，以及双面显示装置		
公开(公告)号	EP3316306A4	公开(公告)日	2019-01-16
申请号	EP2015860013	申请日	2015-10-08
[标]申请(专利权)人(译)	京东方科技集团股份有限公司 合肥京东方显示光源有限公司		
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[标]发明人	KANG YALING		
发明人	KANG, YALING		
IPC分类号	H01L27/15 H01L21/77 F21S2/00		
CPC分类号	H01L25/0756 H01L25/0753 H01L25/50 H01L27/153 H01L27/156 H01L27/32 H01L27/3202 H01L27/3204 H01L27/3206 H01L27/3241 H01L27/3244 H01L27/3251 H01L33/502 H01L33/504 H01L33/507 H01L33/62 H01L2251/5323 H01L2251/5338		
代理机构(译)	Fritzsche也, THOMAS		
优先权	201510356574.7 2015-06-25 CN		
其他公开文献	EP3316306A1		
外部链接	Espacenet		

摘要(译)

阵列型双面发光装置，其制造方法和双面显示装置技术领域本发明涉及阵列型双面发光装置，其制造方法和双面显示装置。阵列型双面发光装置包括：第一保护层，第一荧光层或量子点层，第一透明导电层阵列，第一各向异性导电粘合剂层，发光晶片阵列，第二各向异性导电粘合剂层，第二透明导电层阵列，第二荧光层或量子点层和第二保护层，它们依次连接在一起。