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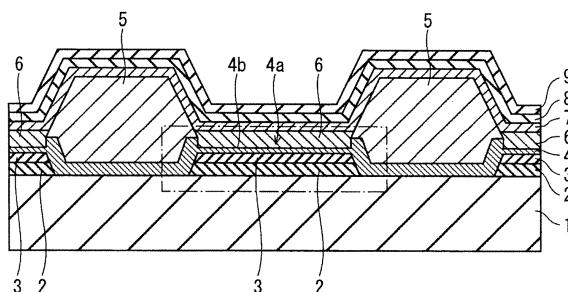
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**(54) LIGHT-EMITTING ELEMENT, DISPLAY DEVICE, AND METHOD FOR MANUFACTURING  
LIGHT-EMITTING ELEMENT**

(57) Provided is a light emitter in which high-definition light-emitting layer is patterned, and which can be manufactured at low cost. The light emitter includes first electrode 2, charge injection transport layer 4, light-emitting layer 6, and second electrode 8 layered in this order. At least the layer 6 is defined by bank 5. The layer 4 includes recessed portion 4a having (i) inner bottom surface 4c in

contact with bottom surface 6a of the layer 6 and (ii) inner side surface 4d continuous with the inner bottom surface 4c and in contact at least partly with side surface 6b of the layer 6. The surface 4d has lower edge continuous with the inner bottom surface 4c and upper edge 4e aligned with portion of bottom periphery 5d of the bank 5, the portion being in contact with the layer 6, or in contact with bottom surface 5a of bank 5.

FIG. 1



**Description****[Technical Field]**

**[0001]** The present invention relates to a light emitter, a display apparatus and a method of manufacturing the light emitter, and in particular to an organic EL element used in a display apparatus such as a flat display apparatus.

**[Background Art]**

**[0002]** Conventionally, in a manufacturing process of an organic EL element, patterning by an ink-jet method is used to form a light-emitting layer. The ink-jet method is suitable for forming a uniform thin film pattern in a micro area. The ink-jet method forms the uniform thin film pattern at a pixel region defined by a bank by ejecting drops of an ink composition (hereinafter, referred to simply as "ink") containing an organic EL material into the pixel region and drying the ink.

**[0003]** When the above method is employed, a surface of the bank is subjected to, for example, a liquid-repellent treatment using fluorine plasma. As a result, the surface of the bank has low wettability to the ink and the ejected ink becomes less likely to overflow the bank and flow in an adjacent pixel region. Accordingly, high-definition patterning becomes possible.

**[0004]** Moreover, Patent Literature 1 discloses a technology for patterning a higher-definition light-emitting layer by: having a two-layered bank composed of an upper bank layer made of a liquid-repellent material and a lower bank layer made of a liquid-philic material; making the upper bank layer have low wettability to the ink so that the ink is less likely to overflow the bank; and making the lower bank layer have high wettability to the ink so that the ink easily stays in the pixel region.

**[Citation List]****[Patent Literature]**

[Patent Literature 1]

**[0005]** Japanese Patent Application Publication No. 2003-249375

**[Summary of Invention]****[Technical Problem]**

**[0006]** However, in order to have the two-layered bank, manufacturing cost of the organic EL element increases since the number of processes increases compared with having a single-layered bank.

**[0007]** In view of the above problem, the present invention aims to provide a light emitter on which a high definition light-emitting layer is patterned and which can

be manufactured at a low price.

**[Solution to Problem]**

**[0008]** A light emitter pertaining to an embodiment of the present invention is a light emitter including a first electrode, a charge injection transport layer, a light-emitting layer, and a second electrode that are layered in this order, at least the light-emitting layer being defined by a bank, wherein the charge injection transport layer includes a recessed portion having (i) an inner bottom surface that is in contact with a bottom surface of the light-emitting layer and (ii) an inner side surface that is continuous with the inner bottom surface and in contact at least with a part of a side surface of the light-emitting layer, and the inner side surface of the recessed portion has a lower edge and an upper edge, the lower edge being continuous with the inner bottom surface, the upper edge being one of (i) aligned with a part of a bottom periphery of the bank, the part being in contact with the light-emitting layer, and (ii) in contact with a bottom surface of the bank.

**[Advantageous Effects of Invention]**

**[0009]** The light emitter pertaining to the embodiment of the present invention includes a charge injection transport layer including a recessed portion having an inner bottom surface that is in contact with a bottom surface of the light-emitting layer and an inner side surface that is continuous with the inner bottom surface. Accordingly, in a manufacturing process, the recessed portion can accumulate drops of ink that have been ejected in a region defined by a bank. In addition, on an inner surface of the recessed portion, the charge injection transport layer is principally composed of a metal compound that is more liquid-philic than the surface of the bank and has high wettability to the ink. Accordingly, the inner surface of the recessed portion can stably hold the drops of the ink therein. Therefore, the drops of the ink do not easily overflow the bank and flow in an adjacent pixel region, and a high definition light-emitting layer can be patterned. Additionally, the recessed portion can be easily formed by, for example, melting a part of the charge injection transport layer with pure water. Also, unlike a light emitter of Patent Literature 1, a complicated process for making a two-layer bank is unnecessary. As a result, it is possible to enable such a light emitter at a low price.

**[Brief Description of Drawings]****[0010]**

FIG. 1 is a schematic view showing a layered condition of layers constituting a light emitter pertaining to a first embodiment.

FIG. 2 is an enlarged view of a part enclosed with a dashed line in FIG. 1.

FIGs. 3A-3D are schematic views for explaining a form of a recessed portion.

FIG. 4 is an enlarged view of a part enclosed with a dashed line in FIG. 1, with respect to a light emitter pertaining to a modification.

FIGs. 5A-5D are schematic views for explaining the form of the recessed portion.

FIGs. 6A and 6B are schematic views for explaining a best thickness of a light-emitting layer.

FIG. 7 is an enlarged view of the part enclosed with the dashed line in FIG. 1, with respect to the light emitter pertaining to the modification.

FIGs. 8A-8D are processing drawings for explaining a method of manufacturing the light emitter pertaining to the first embodiment.

FIGs. 9E-9H are processing drawings for explaining the method of manufacturing the light emitter pertaining to the first embodiment, which follows FIG. 8D.

FIG. 10 is a schematic view showing a layered condition of layers constituting a light emitter pertaining to a second embodiment.

FIGs. 11A-11D are processing drawings for explaining a method of manufacturing the light emitter pertaining to the second embodiment.

FIG. 12 is a schematic view showing a layered condition of layers constituting a light emitter pertaining to a third embodiment.

FIGs. 13A-13D are processing drawings for explaining a method of manufacturing the light emitter pertaining to the third embodiment.

FIG. 14 is a perspective view showing apparatuses such as a display apparatus pertaining to a fourth embodiment.

### [Description of Embodiments]

#### [Embodiments]

**[0011]** A light emitter pertaining to an embodiment of the present invention is a light emitter including a first electrode, a charge injection transport layer, a light-emitting layer, and a second electrode that are layered in this order, at least the light-emitting layer being defined by a bank, wherein the charge injection transport layer includes a recessed portion having (i) an inner bottom surface that is in contact with a bottom surface of the light-emitting layer and (ii) an inner side surface that is continuous with the inner bottom surface and in contact at least with a part of a side surface of the light-emitting layer, and the inner side surface of the recessed portion has a lower edge and an upper edge, the lower edge being continuous with the inner bottom surface, the upper edge being one of (i) aligned with a part of a bottom periphery of the bank, the part being in contact with the light-emitting layer, and (ii) in contact with a bottom surface of the bank.

**[0012]** Here, a term "charge injection transport layer"

is a collective term for layers such as a hole injection layer, a hole transport layer, a hole injection and transport layer, an electron injection layer, an electron transport layer and an electron injection and transport layer. For

5 example, the charge injection transport layer may be composed of the hole injection layer, the hole transport layer, two layers composed of the hole injection layer and the hole transport layer, the hole injection and transport layer, the electron injection layer, the electron transport layer, two layers composed of the electron injection layer and electron transport layer, and an electron injection transport layer.

**[0013]** Note that, terms "liquid-philic" and "liquid-repellent" are each used in a relative sense. As described above, a bank has liquid-repellency at least on a surface thereof. On the other hand, when the charge injection transport layer is principally composed of a metal compound with liquid-philicity, a surface of the charge injection transport layer is more liquid-philic than the surface 10 of the bank, and the surface of the bank is more liquid-repellent than the surface of the charge injection transport layer. Also, the surface of the charge injection transport layer with liquid-philicity relatively has high wettability to ink, and the surface of the bank with liquid-repellency 15 relatively has low wettability to the ink. Note that, liquid-philicity or liquid-repellency can be, for example, defined by a contact angle at which the ink meets the surface of the bank or the charge injection transport layer. For example, when the contact angle is equal to or smaller than 20 10°, the surface is defined to have liquid-philicity, and when the contact angle is equal to or greater than 35°, the surface is defined to have liquid-repellency.

**[0014]** Also, according to a specific aspect of the light emitter the charge injection transport layer is liquid-philic, and the bank is liquid-repellent.

**[0015]** Also, according to a specific aspect of the light emitter, the charge injection transport layer includes a metal compound that is soluble in a predetermined solvent, and the recessed portion is a portion of the charge 25 injection transport layer where the metal compound has been dissolved by the predetermined solvent.

**[0016]** Also, according to a specific aspect of the light emitter, the predetermined solvent is one or both of (i) a developing solution for removing a part of a resist film 30 used to form the bank and (ii) a cleaning liquid for cleaning residuals of the resist film that are remaining after the formation of the bank.

**[0017]** Also, according to a specific aspect of the light emitter, the charge injection transport layer is a hole injection layer including a metal oxide.

**[0018]** Also, according to a specific aspect of the light emitter, the metal oxide is an oxide of one of tungsten and molybdenum.

**[0019]** A display apparatus pertaining to the embodiment of the present invention includes any of the above light emitters.

**[0020]** A manufacturing method of the light emitter pertaining to the embodiment of the present invention com-

prises: a first step of forming a first electrode on a substrate; a second step of forming, above the first electrode, a thin film including a metal compound that is soluble in a predetermined solvent; a third step of forming a bank on the thin film by (i) forming a resist film including a resist material on the thin film and (ii) etching the resist film with a developing solution; a fourth step of, after the formation of the bank, forming a charge injection transport layer by cleaning residuals of the resist film that adhere to the thin film with a cleaning liquid and dissolving a part of the thin film with the cleaning liquid, the charge injection transport layer including a recessed portion having (i) an inner bottom surface and (ii) an inner side surface that is continuous with the inner bottom surface and in contact at least with a part of a side surface of the light-emitting layer, the inner side surface of the recessed portion having a lower edge and an upper edge, the lower edge being continuous with the inner bottom surface, the upper edge being one of (i) aligned with a part of a bottom periphery of the bank, the part being in contact with the light-emitting layer, and (ii) in contact with a bottom surface of the bank; a fifth step of forming a light-emitting layer by (i) coating the inner bottom surface and the inner side surface of the charge injection transport layer with ink by ejecting drops of the ink into a region defined by the bank and (ii) drying the ink; and a sixth step of forming a second electrode above the light-emitting layer.

**[0021]** Also, a manufacturing method of a light emitter, comprises: a first step of forming a first electrode on a substrate; a second step of forming, above the first electrode, a thin film including a metal compound that is soluble in a predetermined solvent; a third step of forming a bank on the thin film by (i) forming a resist film including a resist material on the thin film and (ii) etching the resist film with a developing solution, and of forming a charge injection transport layer by cleaning residuals of the resist film that adhere to the thin film and dissolving a part of the thin film with the developing liquid, the charge injection transport layer including a recessed portion having (i) an inner bottom surface and (ii) an inner side surface that is continuous with the inner bottom surface and in contact at least with a part of a side surface of the light-emitting layer, the inner side surface of the recessed portion having a lower edge and an upper edge, the lower edge being continuous with the inner bottom surface, the upper edge being one of (i) aligned with a part of a bottom periphery of the bank, the part being in contact with the light-emitting layer, and (ii) being in contact with a bottom surface of the bank; a fourth step of forming a light-emitting layer by (i) coating the inner bottom surface and the inner side surface of the charge injection transport layer with ink by ejecting drops of the ink into a region defined by the bank and (ii) drying the ink; and a sixth step of forming a second electrode above the light-emitting layer.

**[0022]** The following explains a light emitter, a display apparatus and a method of manufacturing the light emitter with reference to the drawings. Note that contraction scale of members shown in each drawing differs from

real scale.

### [First Embodiment]

#### 5 <Schematic Structure of Light emitter>

**[0023]** FIG. 1 is a schematic view showing a layered condition of layers constituting a light emitter pertaining to a first embodiment, and FIG. 2 is an enlarged view of 10 a part enclosed with a dashed line in FIG. 1.

**[0024]** As FIG. 1 shows, the light emitter pertaining to the first embodiment is a top emission type organic EL element including RGB pixels that are arranged in a matrix or in line. Each pixel has a layer structure and each 15 layer is disposed on a substrate.

**[0025]** On a TFT substrate 1 (hereinafter, referred to simply as "substrate 1"), a first electrode 2, which is an anode, is formed together with other first electrodes 2 in a matrix or in line. On the first electrode 2, an indium tin 20 oxide (ITO) layer 3, and a hole injection layer 4 as the charge injection transport layer are layered in this order. Note that, while the ITO layer 3 is layered only on the first electrode 2, the hole injection layer 4 is formed not only above the first electrode 2 but also across an entire 25 upper surface of the substrate 1.

**[0026]** On the hole injection layer 4, a bank 5 that defines pixels is formed, and in a region defined by the bank 5, a light-emitting layer 6 is disposed. Furthermore, on the light-emitting layer 6, an electron injection layer 7, a 30 second electrode 8, which is a cathode, and a passivation layer 9 are formed so as to be continuous with respective layers of an adjacent pixel, passing over the region defined by the bank 5.

**[0027]** The region defined by the bank 5 has a multi-layer structure according to which the ITO layer 3, the hole injection layer 4, the light-emitting layer 6 and the electron injection layer 7 are layered in this order. Such a layer structure constitutes a functional layer. Note that, 35 the functional layer may include other layers such as the hole transport layer or the electron transport layer.

#### <Constituent Components of Light emitter>

**[0028]** The substrate 1 is formed with an insulating material such as soda glass, nonfluorescent glass, phosphate glass, borate glass, quartz, acrylic resin, styrenic resin, polycarbonate resin, epoxy resin, polyethylene, polyester, silicone resin, alumina, etc.

**[0029]** The first electrode 2 is formed with Ag (silver). 50 Note that the first electrode 2 may be formed with APC (alloy of silver, palladium, and copper), ARA (alloy of silver, rubidium, and gold), MoCr (alloy of molybdenum and chromium), NiCr (alloy of nickel and chromium), etc. In the case of a top emission type light emitter, it is preferable that the first electrode 2 be formed with a light-reflective material.

**[0030]** The ITO layer 3 exists between the first electrode 2 and the hole injection layer 4 and has a function

of guaranteeing excellent bondability between the first electrode 2 and the hole injection layer 4.

**[0031]** The hole injection layer 4 contains a metal compound soluble in a predetermined solvent. To be specific, the hole injection layer 4 is formed with WO<sub>x</sub> (tungsten oxide) or MoWO<sub>x</sub> (molybdenum tungsten oxide). Note that the hole injection layer 4 only has to be formed with a metal compound that is more liquid-philic than a surface of the bank 5. Examples of a metal compound having liquid-philicity include a metal oxide, metal nitride, or metal oxynitride.

**[0032]** When the hole injection layer 4 is formed with a metal oxide, holes can be easily injected, contributing to effective light emission by electrons in the light-emitting layer 6, which allows for excellent light-emitting characteristics to be obtained. Examples of a metal oxide include an oxide of chromium (Cr), molybdenum (Mo), tungsten (W), vanadium (V), niobium (Nb), tantalum (Ta), titanium (Ti), zircon (Zr), hafnium (Hf), scandium (Sc), yttrium (Y), thorium (Th), manganese (Mn), iron (Fe), ruthenium (Ru), osmium (Os), cobalt (Co), nickel (Ni), copper (Cu), zinc (Zn), cadmium (Cd), aluminum (Al), gallium (Ga), indium (In), silicon (Si), germanium (Ge), stannum (Sn), lead (Pb), antimony (Sb), bismuth (Bi), and so-called rare earth from lanthanum (La) to lutetium (Lu). Especially, aluminum oxide (Al<sub>2</sub>O<sub>3</sub>), copper oxide (CuO) and silicon monoxide (SiO) can contribute to a prolonged service life.

**[0033]** It is preferable that the metal compound be composed of a transition metal. The transition metal occupies a plurality of potential levels since there are a plurality of oxidation numbers. This makes hole injection easy and allows for reduction of driving voltage.

**[0034]** As FIG. 2 shows, the hole injection layer 4 extends along the bottom surface of the bank 5 to an adjacent pixel. Also, a portion of the charge injection transport layer 4 in a region defined by the bank 5 has a recessed structure in which the portion of the charge injection transport layer 4 is lower than the bottom surface of the bank 5, and includes a recessed portion 4a (indicated with mesh hatching in FIG. 2) formed by being dissolved by a predetermined solvent. In addition, with regard to the hole injection layer 4, only a film thickness in the region defined by the bank 5 is smaller than a film thickness in other regions, and the film thickness in the other regions is entirely and substantially uniform. Since the hole injection layer 4 is formed by a metal compound having liquid-philicity, an inner surface 4b of the recessed portion 4a has excellent wettability to ink. Accordingly, this allows for drops of the ink ejected into the region defined by the bank 5 to easily adhere to the inner surface 4b of the recessed portion 4a, and the drops of the ink easily stay within the region defined by the bank 5.

**[0035]** Note that, the hole injection layer 4 only has to have a recessed structure according to which the recessed portion 4a is lower than a peripheral portion 5a of the bottom surface of the bank 5, and does not have to be lower than the entire bottom surface of the bank 5.

In the recessed structure of the present embodiment, the recessed portion 4a is lower than the peripheral portion 5a of the bottom surface of the bank but not lower than a central part 5b of the bottom surface of the bank. Alternatively, for example, by setting the central part 5b as high as the bottom peripheral portion 5a and planarizing the bottom surface of the bank 5, as an alternate long and two short dashes line 5c shows in FIG. 2, it may be possible to have the recessed structure according to which the recessed portion 4a is lower than the entire bottom surface of the bank 5.

**[0036]** The hole injection layer 4 has a recessed structure according to which a portion of the hole injection layer 4 is lower than and aligned with the bottom periphery 5d of the bank. To be specific, an upper surface of the hole injection layer 4, which is defined by the bank 5, is sunken from a level of the bottom periphery 5d in a direction substantially vertical to the upper surface of the substrate 1. Like this, in the case of the recessed structure according to which the portion of the hole injection layer 4 is lower than and aligned with the bottom periphery 5d of the bank 5, a film thickness of the light-emitting layer 6 can be uniformed over a wide range, and as a result, irregular luminance is not likely to occur in the light-emitting layer 6.

**[0037]** The recessed structure of the hole injection layer 4 has a cup-like shape. To be more specific, an inner surface 4b of the recessed portion 4a is composed of an inner bottom surface 4c and an inner side surface 4d. The inner bottom surface 4c is substantially parallel with the upper surface of the substrate 1 and uniformed, and is in contact with a bottom surface 6a of the light-emitting layer 6. The inner side surface 4d extends from a periphery of the inner bottom surface 4c in a direction perpendicular to the upper surface of the substrate 1 and is in contact with a side surface 6b of the light-emitting layer 6. When the recessed structure has a cup-like shape, the inner side surface 4d prevents the drops of the ink within the recessed portion 4a from moving in a direction parallel to the upper surface of the substrate 1. Accordingly, it is possible to more stably hold the drops of the ink within the region defined by the bank 5. Moreover, when the recessed structure has the cup-like shape, the inner surface 4b of the recessed portion 4a becomes larger in area, and a contact surface of the drops of the ink and the hole injection layer 4 becomes larger in area. Accordingly, it is possible to more stably hold the drops of the ink within the region defined by the bank 5. Therefore, high-definition patterning of the light-emitting layer 6 is possible.

**[0038]** As described above, in the present embodiment, the bank 5 and the hole injection layer 4 are connected to each other in a substantially vertical direction, and then the ink easily gets wet at a bottom of the light-emitting layer 6. Accordingly, the light-emitting layer 6 with excellent efficiency can be formed. Here, when the bank 5 and the hole injection layer 4 are connected to each other in a horizontal direction, the ink do not easily

get wet in the vicinity of a connection section of the bank 5 with the hole injection layer 4. Accordingly, the light-emitting layer 6 might not be fully formed at the bottom surface thereof, and as a result, electrical leakage might occur. That is, a technical meaning resides in that the bank 5 and the hole injection layer 4 are connected to each other not in the horizontal direction but in the substantially vertical direction in order to excellently form the light-emitting layer 6.

**[0039]** Note that, in the case that the bank 5 and the hole injection layer 4 are connected to each other in the substantially vertical direction, the direction is not limited to the vertical direction but may be a diagonal direction. In other word, the direction only has to be a direction substantially perpendicular to the horizontal direction.

**[0040]** The following explains the recessed structure of the hole injection layer 4 in more detail. As FIG. 3A shows, the inner side surface 4d of the recessed portion 4a is composed of an lower side edge that is continuous with the inner bottom surface 4c and an upper side edge 4e (hereinafter, referred to as "upper edge 4e") that is continuous with the lower side edge. The upper edge 4e of the inner side surface 4d of the recessed portion 4a is aligned with a part of the bottom periphery 5d of the bank 5, the part of being in contact with the light-emitting layer 6, and the inner side surface 4d and the inner bottom surface 4c are continuous with each other in an R shape. Note that, when the upper edge 4e of the inner side surface 4d is aligned with the bottom periphery 5d of the bank 5, the recessed portion 4a is not limited to the shape shown in FIG. 3A, in which the inner side surface 4d is substantially vertical to the inner bottom surface 4c. As FIG. 3B shows, the inner side surface 4d and a side surface 5e of the bank 5 may have substantially the same inclination and extend on the same plane. As FIG. 3C shows, the inner side surface 4d and the inner bottom surface 4c may be continuous with each other not in the R shape. As FIG. 3D shows, the inner side surface 4d may be inclined opposite to the side surface 5e of the bank 5 so as to go under the bank 5.

**[0041]** Alternatively, the hole injection layer 4 is not limited to have the recessed structure according to which the hole injection layer 4 is lower than and aligned with the bottom periphery 5d of the bank 5. For example, as FIG. 4 shows, the upper edge 4e of the inner side surface 4d may not be aligned with the bottom periphery 5d of the bank 5, and the upper edge 4e may be closer to an adjacent pixel than the bottom periphery 5d is. In such a case, As FIG. 5A shows, the inner side surface 4d of the recessed portion 4a has the upper edge 4e that is in contact with a bottom surface 5a of the bank 5. Note that, when the upper edge 4e of the inner side surface 4d is in contact with the bottom surface 5a of the bank 5, the recessed portion 4a is not limited to the shape shown in FIG. 5A, in which the inner side surface 4d is substantially vertical to the inner bottom surface 4c. As FIG. 5B shows, the inner side surface 4d and a side surface 5e of the bank 5 may have a substantially same inclination. As

FIG. 5C shows, the inner side surface 4d and the inner bottom surface 4c may be continuous with each other not in the R shape. As FIG. 5D shows, the inner side surface 4d may be inclined opposite to the side surface 5e of the bank 5 so as to go under the bank 5.

**[0042]** Since the inner side surface 4d has the upper edge 4e that is aligned with the bottom periphery 5d of the bank 5 or the upper edge 4e is in contact with the bottom surface 5a of the bank 5, a short circuit is not likely to occur between the electrodes 2 and 8. Supposedly, as a line with alternate long and two short dashes 10 shows in FIG. 4, when the upper edge 4e of the inner side surface 4d is not aligned with the bottom periphery 5d of the bank 5 and the upper edge 4e is closer to an adjacent pixel than the bottom periphery 5d is, a short circuit might occur between the electrodes 2 and 8 via an exposed area 4f that is a part of an upper surface of the hole injection layer 4 and exposed from the bank 5. Especially, as described later, when an average film thickness h of the light-emitting layer 6 is smaller or equal to an average depth t of the recessed portion 4a, an uncovered area 4f of hole injection layer 4 that is a part of an upper surface of the hole injection layer 4 and uncovered with the bank 5 might be in contact with the electron injection layer 7 or the second electrode 8. Accordingly, a short circuit might occur between the electrodes 2 and 8.

**[0043]** Returning to FIG. 2, though the invention of the present application does not specifically specify the average depth t of the recessed portion 4a, the average depth t may be 5-100 nm, for example. If the average depth t of the recessed portion 4a is equal to or greater than 5 nm, it is possible to hold sufficient amount of the ink within the recessed portion 4a. Accordingly, it is possible to stably maintain the ink within the region defined by the bank 5. Furthermore, since the light-emitting layer 6 is formed until a periphery of the bank 5 without being rejected, the short circuit between the electrodes 2 and 8 can be prevented.

**[0044]** Note that, the average depth t of the recessed portion 4a can be obtained by measuring a surface profile of the hole injection layer 4 with use of a stylus profiler meter or an AFM (Atomic Force Microscope), and calculating a difference between an average height of a portion that is a crest and an average height of a portion that is a trough based on the surface profile.

**[0045]** On the other hand, the film thickness of the light-emitting layer 6 is not specifically determined. However, if an average film thickness h of the light-emitting layer 6 after dried is equal to or greater than 100 nm and an average depth t of the recessed portion 4a is equal to or smaller than 100 nm, for example, it is possible to uniform a film thickness of the light-emitting layer 6 at the region defined by the bank 5.

**[0046]** Furthermore, it is preferable that a difference between the average film thickness h of the light-emitting layer 6 and the average depth t of the recessed portion 4a be equal to or smaller than 20 nm. When the average

film thickness  $h$  of the light-emitting layer 6 is much smaller than the average depth  $t$  of the recessed portion 4a (for example,  $t - h > 20$  nm), there is a portion that is out of contact with the light-emitting layer 6 (portion not covered with the light-emitting layer 6) at the inner side surface 4d of the recessed portion 4a, as FIG. 6A shows. Then a short circuit might occur between the electrodes 2 and 8 at that portion. On the other hand, when the average film thickness  $h$  of the light-emitting layer 6 is much greater than the average depth  $t$  of the recessed portion 4a (for example,  $h - t > 20$  nm), liquid-repellency of the bank 5 causes a film thickness of a bank vicinity portion 6c of the light-emitting layer 6 to become smaller than other portions, as FIG. 6B shows. As a result, a cross-sectional shape of the light-emitting layer 6 has a substantially convex shape, and this might result in irregular light emission caused by a difference of film thickness.

**[0047]** Note that, the inner side surface 4d of the recessed portion 4a only has to be in contact with at least a part of the side surface 6b of the light-emitting layer 6. For example, as FIG. 2 and FIG. 6B show, when the average film thickness  $h$  of the light-emitting layer 6 is equal to or greater than the average depth  $t$  of the recessed portion 4a, only a part of a lower part of the side surface 6b of the light-emitting layer 6 is in contact with the inner side surface 4d of the recessed portion 4a. On the other hand, as FIG. 6A shows, when the average film thickness  $h$  of the light-emitting layer 6 is smaller than the average depth  $t$  of the recessed portion 4a, the side surface 6b of the light-emitting layer 6 is entirely in contact with the inner side surface 4d of the recessed portion 4a.

**[0048]** As FIG. 7 shows, in the recessed portion 4a of the hole injection layer 4, a liquid-philic layer 12 such as an IL layer (intermediate layer) may be formed under the light-emitting layer 6. In this case, the drops of the ink are ejected not on the inner bottom surface 4c of the recessed portion 4a but on an upper surface 12a of the liquid-philic layer 12. However, since the upper surface 12a is liquid-philic, it is possible to stably hold the drops of the ink within the region defined by the bank 5. However, if the recessed portion 4a is totally filled with the liquid-philic layer 12, the inner side surface 4d of the recessed portion 4a becomes out of contact with the ink. Accordingly, an average film thickness  $g$  of the liquid-philic layer 12 is preferably smaller than the average depth  $t$  of the recessed portion 4a.

**[0049]** The bank 5 is formed with an organic material such as resin or an inorganic material such as glass and has insulating properties. Examples of the organic material include acrylic resin, polyimide resin, novolac-type phenolic resin, etc. Examples of the inorganic material include silicon oxide ( $\text{SiO}_2$ ), silicon nitride ( $\text{Si}_3\text{N}_4$ ), etc. It is preferable that the bank 5 have organic solvent resistance, and have certain light transparency to visible light. Furthermore, since the bank 5 is etched, baked, etc. when formed, it is preferable that the bank 5 be formed from a material highly resistant to the etching and baking

processes.

**[0050]** At least the surface of the bank 5 is provided with liquid-repellency. Accordingly, when the bank 5 is formed with a liquid-philic material, it is necessary that the surface of the bank 5 be subjected to, for example, a liquid-repellent treatment in order to make the surface of the bank 5 liquid-repellent.

**[0051]** Note that the bank 5 may be a pixel bank or a line bank. In the case of a pixel bank, the bank 5 is formed to surround a whole circumference of the light-emitting layer 6 including a pixel. In the case of a line bank, the bank 5 is formed to define a plurality of pixels by column or by row. In other words, the bank 5 exists only as columns or as rows on either side of a light-emitting layer 6, and a light-emitting layer 6 is continuous with other light-emitting layers 6 in the same column or row.

**[0052]** Specifically, it is preferable that the organic light-emitting layer 6 be formed from a fluorescent material such as an oxinoid compound, perylene compound, coumarin compound, azacoumarin compound, oxazole compound, oxadiazole compound, perinone compound, pyrrolo-pyrrole compound, naphthalene compound, anthracene compound, fluorene compound, fluoranthene compound, tetracene compound, pyrene compound, coronene compound, quinolone compound and azaquinolone compound, pyrazoline derivative and pyrazolone derivative, rhodamine compound, chrysene compound, phenanthrene compound, cyclopentadiene compound, stilbene compound, diphenylquinone compound, styryl compound, butadiene compound, dicyanomethylene pyran compound, dicyanomethylene thiopyran compound, fluorescein compound, pyrylium compound, thiapyrylium compound, selenapyrylium compound, telluropyrylium compound, aromatic aladiene compound, oligophenylene compound, thioxanthene compound, anthracene compound, cyanine compound, acridine compound, metal complex of a 8-hydroxyquinoline compound, metal complex of a 2-bipyridine compound, complex of a Schiff base and a group three metal, metal complex of oxine, rare earth metal complex, etc., as recited in Japanese Unexamined Patent Application Publication No. H5-163488. When the light-emitting layer 6 includes a layer formed with a high-polymer material, the light-emitting layer 6 can be formed by a printing technology

such as an ink-jet method, and a nozzle coating method. Accordingly, compared with a deposition method using a low-molecular material, is it possible to easily contribute to cost reduction.

**[0053]** The electron injection layer 7 has the function of transporting electrons injected through the second electrode 8 to the light-emitting layer 6 and is preferably formed, for example, from barium, phthalocyanine, lithium fluoride, or a combination thereof.

**[0054]** The second electrode 8 is formed from ITO, indium zinc oxide (IZO), etc. In the case of a top emission type light emitter, it is preferable that the first electrode 8 be formed with a light transparent material.

**[0055]** The passivation layer 9 has the function of con-

trolling the light-emitting layer 6 or other layers from being exposed to water or air and is formed, for example, from silicon nitride (SiN), silicon oxynitride (SiON), etc. In the case of a top emission type light emitter, it is preferable that the passivation layer 9 be formed from a light transparent material.

<Method of Manufacturing Light emitter>

**[0056]** FIGs. 8A-8D are processing drawings for explaining a method of manufacturing a light emitter pertaining to the first embodiment. FIGs. 9E-9H are processing drawings for explaining the method of manufacturing the light emitter pertaining to the first embodiment, which follows FIG. 8D.

**[0057]** In the manufacturing process of a light emitter pertaining to the first embodiment, first, as FIG. 8A shows, the first electrode 2 is formed on the substrate 1 that is made of glass together with other first electrodes 2 in a matrix or in line, by forming a thin Ag film, for example, by a sputtering method and then patterning the thin Ag film, for example, by photolithography. Note that the thin Ag film may be formed by vacuum deposition or the like.

**[0058]** Next, as FIG. 8B shows, a thin ITO film is formed by, for example, sputtering, and then the ITO layer 3 is formed by patterning the thin ITO film by photolithography, for example.

**[0059]** Following this, the thin film 11 containing a metal compound soluble in a predetermined solvent is formed. For example, using a compound containing WO<sub>x</sub> or MoWO<sub>x</sub>, the thin film 11 formed from WO<sub>x</sub> or MoWO<sub>x</sub> are formed by a vacuum deposition method or a sputtering method, to be uniform all over an upper surface of the substrate 1.

**[0060]** Next, as FIG. 8C shows, the bank 5 is formed, for example, by photolithography, so as to surround each pixel region (region at which the first electrode 2 is positioned). In such a case, for example, a resist film (for example, resin film) including a resist material is formed on the thin film 11, for example, by coating, and a resist pattern is formed on the resist film. After that, a desired portion of the resist film is etched by applying a developing solution and removed to form patterning of the bank 5. Note that, when the bank 5 is formed from an inorganic material, the bank is formed by a CVD method, for example. Residuals of the resist film that are attached on a surface of the thin film 11 after etching are removed by hydrofluoric acid, for example. Furthermore, a liquid-repellent treatment is applied to the surface of the bank 5, if necessary.

**[0061]** Next, as FIG. 8D shows, the hole injection layer 4 is formed by forming the recessed portion 4a by melting a part of the thin film 11. Thereby, in the hole injection layer 4, the region defined by the bank 5 has a film thickness smaller than a film thickness of other areas. The recessed portion 4a is formed, for example, when an impure substance such as hydrofluoric acid remaining on

the surface of the bank 5 is being cleaned with pure water after residuals of the resist film are removed, by melting the region defined by the bank 5 on an upper surface of the thin film 11 with the pure water. In such a case, pure

5 water is the predetermined solvent, and it is possible to change a depth and a shape of the recessed portion 4a by changing condition of cleaning with the pure water.

**[0062]** As a specific method, for example, the substrate 1 is cleaned by ejecting pure water (for example, of a

10 room temperature) thereon, while a spin coater keeps rotating the substrate 1. After that, while the substrate 1 is kept rotating, pure water is stopped being ejected and then drained. In this case, it is possible to change the depth and the shape of the recessed portion 4a by changing 15 a time period for ejecting the pure water. Since a melting speed of the thin film 11 also changes according to a temperature of pure water, it is also possible to adjust the depth and the shape of the recessed portion 4a by the temperature of pure water.

**[0063]** A method for forming the recessed portion 4a is not limited to the above. For example, after the formation 20 of the bank 5, while the residuals of the resist film that are attached on the surface of the thin film 11 are being cleaned with a cleaning liquid such as pure water,

25 the recessed portion 4a may be formed by melting a part of the thin film 11 by the cleaning liquid at the same time. In such a case, the predetermined solvent is the cleaning liquid. Alternatively, while the resist film is being etched by the developing solution to form the bank 5 and the 30 residuals of the resist film that are attached on the thin film 11 are being cleaned by the developing solution, the recessed portion 4a may be formed by melting the part of the thin film 11 at the same time. In such a case, the predetermined solvent is the developing solution.

**[0064]** When the hole injection layer 4 is formed by 35 dissolving the thin film 11 with use of a solvent such as a cleaning liquid and a developing solution that are used in forming the bank, productive efficiency is high since a different predetermined solvent to form the recessed portion 4a is not required and an additional process to form the recessed portion 4a is also not required.

**[0065]** Note that the recessed portion 4a is not limited to be formed by using the predetermined solvent. Another method like the following may be used. For example, first,

40 a thin film made of WO<sub>x</sub> or MoWO<sub>x</sub> is formed with use of spattering and photolithography at all the area except an area at which the first electrode 2 is located. After that, on the thin film, another thin film made of WO<sub>x</sub> or MoWO<sub>x</sub> is formed to cover all the area and the hole injection layer 45 having a recessed shape is formed at the area at which the first electrode 2 is located.

**[0066]** Next, as FIG. 9E shows, the light-emitting layer 6 is formed by ejecting drops of the ink by, for example, the ink-jet method in the region defined by the bank 5,

50 coating the ink along the inner bottom surface 4c and the inner side surface 4d of the hole injection layer 4, and drying the ink. Note that the ink may be ejected by other methods such as a dispenser method, a nozzle coating

method, a spin coat method, an intaglio printing, and a letterpress printing.

**[0067]** Next, as FIG. 9F shows, a thin barium film that will be the electron injection layer 7 is formed by, for example, vacuum deposition. Then as FIG. 9G shows, an ITO thin film that will be the second electrode 8 is formed by, for example, spattering. After that, as FIG. 9H shows, the passivation layer 9 is formed.

### [Second Embodiment]

**[0068]** A light emitter pertaining to a second embodiment is greatly different from the light emitter pertaining to the first embodiment in that the ITO layer is not formed under the hole injection layer and that a protection film is formed on the hole injection layer. The following explanation focuses on the difference from the first embodiment, and explanation of the same structure as the first embodiment will be simplified or omitted.

#### <Structure of Light emitter>

**[0069]** FIG. 10 is a schematic view showing a layered condition of layers constituting a light emitter pertaining to a second embodiment. As FIG. 10 shows, a light emitter pertaining to the second embodiment includes a first electrode 102, which is an anode, formed on a substrate 101, and a hole injection layer 104 and a protective layer 110 are layered thereon in this order as a charge injection transport layer. Note that the hole injection layer 104 is formed across an entire upper surface of the substrate 101, but the protective layer 110 is not formed above the first electrode 102. In addition, an ITO layer does not exist between the first electrode 102 and the hole injection layer 104.

**[0070]** On the hole injection layer 104, a bank 105 for defining pixels is formed. A light-emitting layer 106 is layered in a region defined by the bank 105, and on the light-emitting layer 106, an electron injection layer 107, a second electrode 108, which is a cathode, and a passivation layer 109 are formed so as to be continuous with respective layers of an adjacent pixel, passing over the region defined by the bank 105.

#### <Method of Manufacturing Light emitter>

**[0071]** FIGs. 11A-11D are processing drawings for explaining a method of manufacturing a light emitter pertaining to the second embodiment. In the manufacturing process of the light emitter pertaining to the second embodiment, as FIG. 11A shows, first, on the substrate 101 that is made of glass, the first electrode 102 is formed with an aluminum-based (Al) material. Next, a thin film 111 made of WO<sub>x</sub> or MoWO<sub>x</sub>, which will be the hole injection layer 104 later, is formed on the first electrode 102. Then a thin film 112 made of WO<sub>x</sub> or MoWO<sub>x</sub>, which will be the protective layer 110, is formed on the thin film 111. The thin film 112 protects the hole injection layer

104 during etching for forming the bank 105.

**[0072]** Next, as FIG. 11B shows, the bank 105 is formed on the thin film 112. To be specific, a resist film including a resist material is formed on the thin film 112, and a resist pattern is formed on the film. After that a desired portion of the resist film is etched by applying a developing solution and removed to form patterning of the bank 105. Note that an impure substance such as hydrofluoric acid remaining on a surface of the bank 105 after the formation of the bank is cleaned by a cleaning liquid such as pure water and removed, and a region defined by the bank 105 on an upper surface of the thin film 112 is melted by the cleaning liquid and becomes recessed.

**[0073]** Furthermore, as FIG. 11C shows, as the treatment with the cleaning liquid continues, the entire region defined by the bank 105 on the thin film 112 melts and accordingly the protective layer 110 is formed. When the thin film 112 melts, the thin film 111 is exposed and the region defined by the bank 105 on the upper surface of the thin film 111 melts and becomes recessed and then a recessed portion 104a is formed. Thus, the hole injection layer 104 is formed.

**[0074]** Next, as FIG. 11D shows, the light-emitting layer 106 is formed within the region defined by the bank 105. Subsequent processes are the same as in Embodiment 1, and therefore a description thereof is omitted.

### [Third Embodiment]

**[0075]** A light emitter pertaining to a third embodiment is greatly different from the light emitter pertaining to the second embodiment in an area at which a hole injection layer is formed. The following explanation focuses on the difference from the second embodiment, and explanation of the same structure as the second embodiment will be simplified or omitted.

#### <Structure of Light emitter>

**[0076]** FIG. 12 is a schematic view showing a layered condition of layers constituting a light emitter pertaining to the third embodiment. As FIG. 12 shows, a light emitter pertaining to the third embodiment includes a first electrode 202, which is an anode, formed on a substrate 201, and a hole injection layer 204 and a protective layer 210 are layered thereon in this order as a charge injection transport layer. Note that the hole injection layer 204 is not formed across the entire upper surface of the substrate 1, but formed only on the first electrode 202 and at a surrounding area of the first electrode 202. On the other hand, the protective layer 210 is not formed above the first electrode 202.

**[0077]** On the hole injection layer 204, a bank 205 for defining pixels is formed. A light-emitting layer 206 is layered in a region defined by the bank 205, and on the light-emitting layer 206, an electron injection layer 207, a second electrode 208, which is a cathode, and a passivation

layer 209 are formed so as to be continuous with respective layers of an adjacent pixel, passing over the region defined by the bank 205.

<Method of Manufacturing Light emitter>

**[0078]** FIGs. 13A-13D are processing drawings for explaining a method of manufacturing a light emitter pertaining to the third embodiment. In the manufacturing process of the light emitter pertaining to the third embodiment, as FIG. 13A shows, first, on the substrate 101 that is made of glass, the first electrode 102 is formed with an Al material. Next, an oxide film 211 that will be a hole injection layer 204 is formed by oxidizing an exposed surface (upper surface and side surface) of the first electrode 102. Then a thin film 212 made of WO<sub>x</sub> or MoWO<sub>x</sub>, which will be the protective layer 210 later, is formed on the oxide film 211.

**[0079]** Next, as FIG. 13B shows, the bank 205 is formed on the thin film 212. An impure substance such as hydrofluoric acid remaining on a surface of the bank 205 is cleaned with a cleaning liquid such as pure water and removed, and a region defined by the bank 205 on an upper surface of the thin film 212 is melted by the cleaning liquid and becomes recessed.

**[0080]** Furthermore, as FIG. 13C shows, as the treatment with the cleaning liquid continues, all the region of the thin film 212, which is defined by the bank 205, melts and accordingly a final form, that is, the protective layer 210 is formed. In addition, when the thin film 212 melts, the region defined by the bank 205 on the thin film 211 is exposed. Accordingly, the upper surface of the region also melts and becomes recessed, and then the recessed portion 204a is formed. Thus, the hole injection layer 204 is formed.

**[0081]** Next, as FIG. 13D shows, the light-emitting layer 206 is formed within the region defined by the bank 205. Subsequent processes are the same as in First Embodiment, and therefore a description thereof is omitted.

**[Fourth Embodiment]**

**[0082]** FIG. 14 is a perspective view showing apparatuses such as a display apparatus pertaining to a fourth embodiment. As FIG. 14 shows, a display apparatus 300 pertaining to an embodiment of the present invention is an organic EL display formed by a plurality of pixels arranged in a matrix. Each pixel emits a color corresponding to one of R (red), G (green), or B (blue) and composed of a light emitter pertaining to the embodiment of the present invention.

**[Modification]**

**[0083]** As described above, the light emitter, the display apparatus and the manufacturing method of the light emitter pertaining to the present embodiments have been explained. However, the light emitter, the display appa-

ratus and the manufacturing method of the light emitter pertaining to an embodiment of the present invention are not limited to the above embodiments.

**[0084]** For example, the charge injection transport layer is not limited to the hole injection layer, and may be the hole transport layer or hole injection and transport layer. Also, the first electrode may be a cathode, and the second electrode may be an anode. In such a case, the charge injection transport layer may be the electron injection layer, the electron transport layer or the electron injection and transport layer.

**[0085]** Also, the light emitter is not limited to the top-emission type, and may be a bottom-emission type.

**15 [Industrial Applicability]**

**[0086]** The present invention can be utilized for an organic EL display apparatus used in an apparatus such as a plane light source and a flat display apparatus.

**20 [Reference Signs List]**

**[0087]**

25	2, 102, 102	first electrodes
	4, 104, 204	charge injection transport layer
	4a	recessed portion
	4c	inner bottom surface of recessed portion
	4d	inner side surface of recessed portion
30	4e	upper edge
	5, 105, 205	banks
	5a	bottom surface of bank
	5d	bottom periphery of bank
	6, 106, 206	light-emitting layers
35	6a	bottom surface of light-emitting layer
	6b	side surface of light-emitting layer
	8, 108, 208	second electrodes
	300	display apparatus

**40 [Claims]**

1. A light emitter including a first electrode, a charge injection transport layer, a light-emitting layer, and a second electrode that are layered in this order, at least the light-emitting layer being defined by a bank, wherein the charge injection transport layer includes a recessed portion having (i) an inner bottom surface that is in contact with a bottom surface of the light-emitting layer and (ii) an inner side surface that is continuous with the inner bottom surface and in contact at least with a part of a side surface of the light-emitting layer, and the inner side surface of the recessed portion has a lower edge and an upper edge, the lower edge being continuous with the inner bottom surface, the upper edge being one of (i) aligned with a part of a bottom

periphery of the bank, the part being in contact with the light-emitting layer, and (ii) in contact with a bottom surface of the bank.

2. The light emitter of Claim 1, wherein the charge injection transport layer is liquid-philic, and the bank is liquid-repellent. 5

3. The light emitter of Claim 1, wherein the charge injection transport layer includes a metal compound that is soluble in a predetermined solvent, and the recessed portion is a portion of the charge injection transport layer where the metal compound has been dissolved by the predetermined solvent. 10 15

4. The light emitter of Claim 3, wherein the predetermined solvent is one or both of (i) a developing solution for removing a part of a resist film used to form the bank and (ii) a cleaning liquid for cleaning residuals of the resist film that are remaining after the formation of the bank. 20

5. The light emitter of Claim 1, wherein the charge injection transport layer is a hole injection layer including a metal oxide. 25

6. The light emitter of Claim 5, wherein the metal oxide is an oxide of one of tungsten and molybdenum. 30

7. A display apparatus comprising the light emitter of any of Claims 1 to 6.

8. A manufacturing method of a light emitter, comprising: 35

a first step of forming a first electrode on a substrate;

a second step of forming, above the first electrode, a thin film including a metal compound that is soluble in a predetermined solvent; 40

a third step of forming a bank on the thin film by (i) forming a resist film including a resist material on the thin film and (ii) etching the resist film with a developing solution; 45

a fourth step of, after the formation of the bank, forming a charge injection transport layer by cleaning residuals of the resist film that adhere to the thin film with a cleaning liquid and dissolving a part of the thin film with the cleaning liquid, the charge injection transport layer including a recessed portion having (i) an inner bottom surface and (ii) an inner side surface that is continuous with the inner bottom surface and in contact at least with a part of a side surface of the light-emitting layer, the inner side surface of the recessed portion having a lower edge and an upper edge, the lower edge being continuous with the inner bottom surface, the upper edge being one of (i) aligned with a part of a bottom periphery of the bank, the part being in contact with the light-emitting layer, and (ii) in contact with a bottom surface of the bank; 50

a fifth step of forming a light-emitting layer by (i) coating the inner bottom surface and the inner side surface of the charge injection transport layer with ink by ejecting drops of the ink into a region defined by the bank and (ii) drying the ink; and

a sixth step of forming a second electrode above the light-emitting layer. 55

9. A manufacturing method of a light emitter, comprising:

a first step of forming a first electrode on a substrate;

a second step of forming, above the first electrode, a thin film including a metal compound that is soluble in a predetermined solvent;

a third step of forming a bank on the thin film by (i) forming a resist film including a resist material on the thin film and (ii) etching the resist film with a developing solution, and of forming a charge injection transport layer by cleaning residuals of the resist film that adhere to the thin film and dissolving a part of the thin film with the developing liquid, the charge injection transport layer including a recessed portion having (i) an inner bottom surface and (ii) an inner side surface that is continuous with the inner bottom surface and in contact at least with a part of a side surface of the light-emitting layer, the inner side surface of the recessed portion having a lower edge and an upper edge, the lower edge being continuous with the inner bottom surface, the upper edge being one of (i) aligned with a part of a bottom periphery of the bank, the part being in contact with the light-emitting layer, and (ii) being in contact with a bottom surface of the bank;

a fourth step of forming a light-emitting layer by (i) coating the inner bottom surface and the inner side surface of the charge injection transport layer with ink by ejecting drops of the ink into a region defined by the bank and (ii) drying the ink; and

a sixth step of forming a second electrode above the light-emitting layer.

FIG. 1

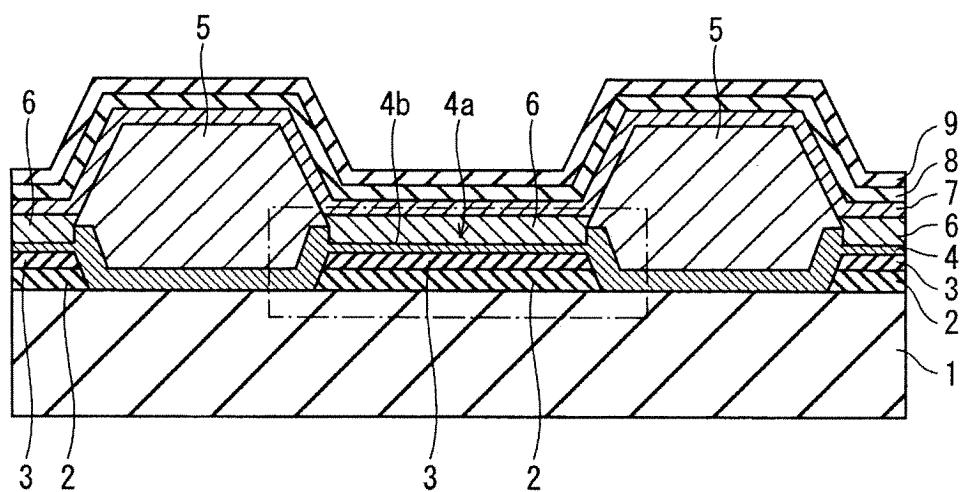


FIG. 2

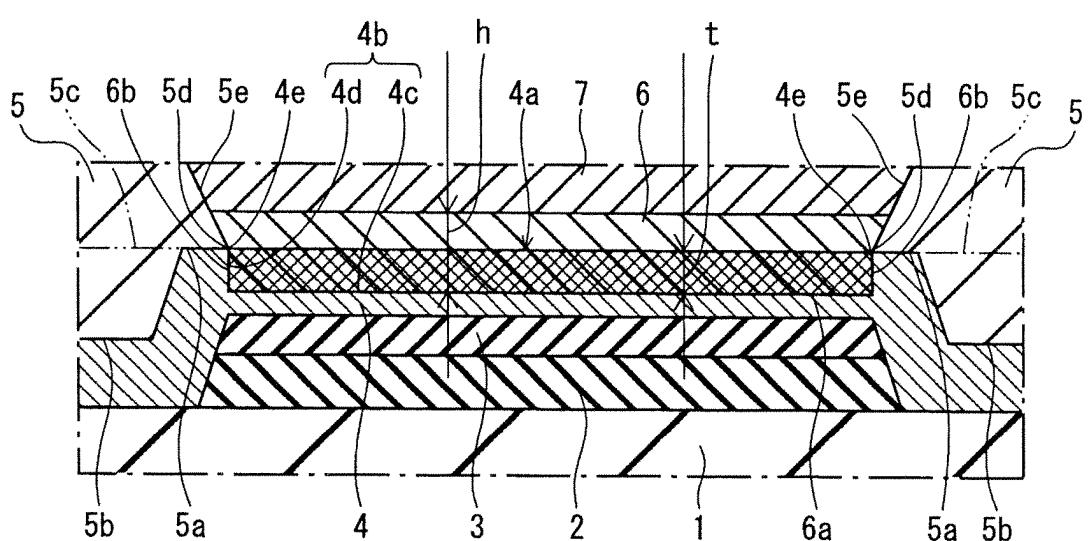


FIG. 3A

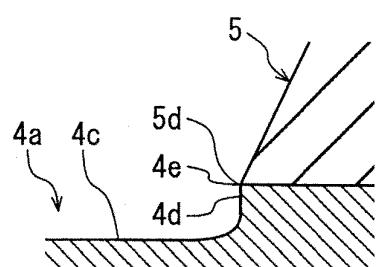


FIG. 3B

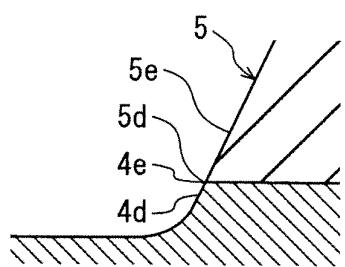


FIG. 3C

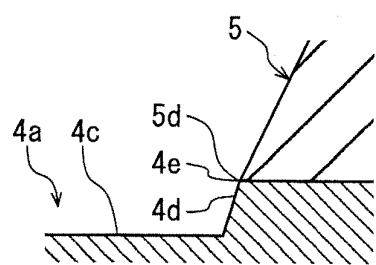


FIG. 3D

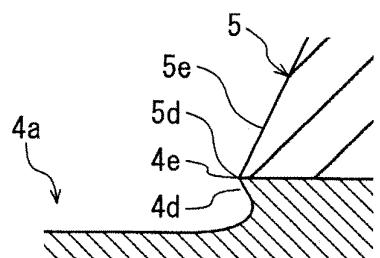


FIG. 4

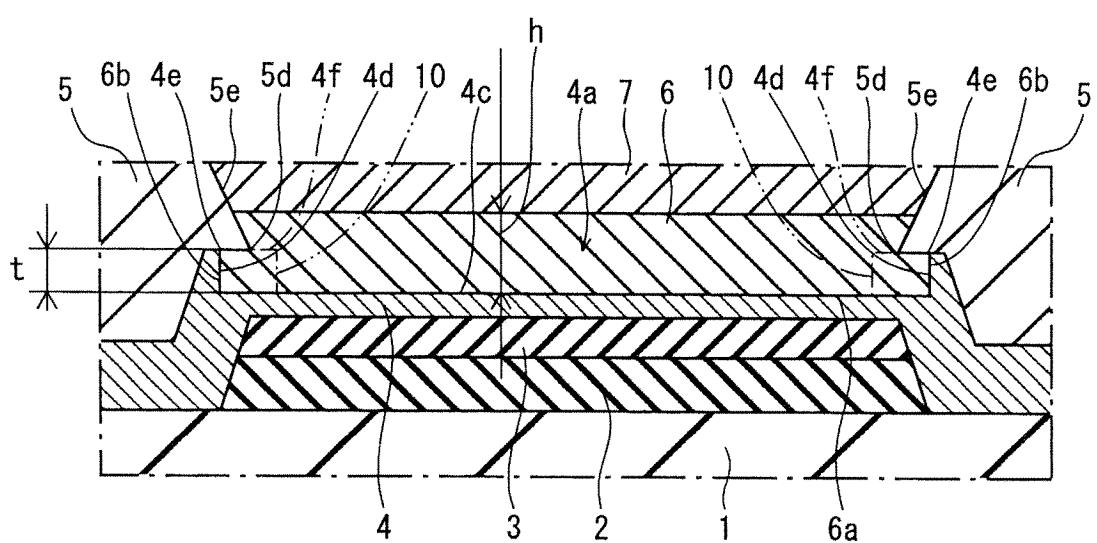


FIG. 5A

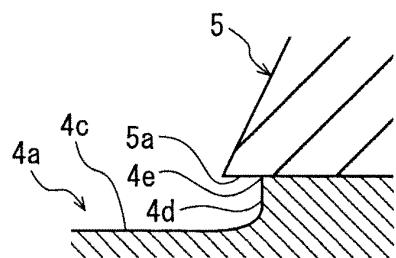


FIG. 5B

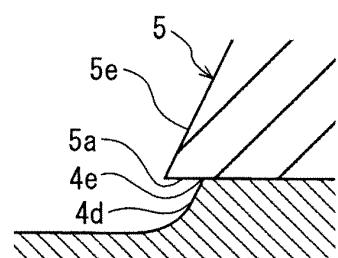


FIG. 5C

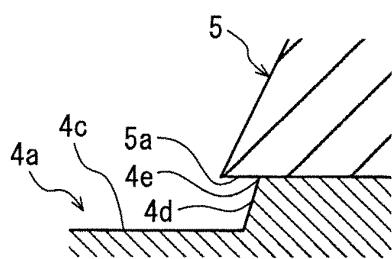


FIG. 5D

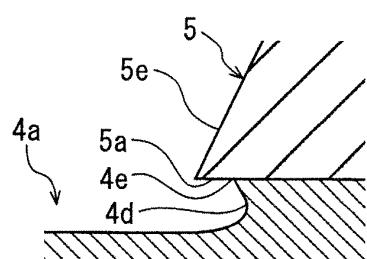


FIG. 6A

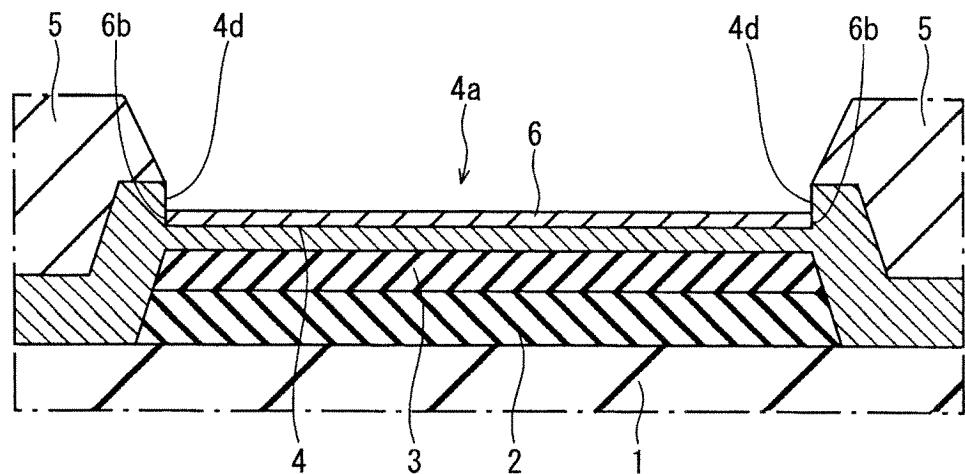


FIG. 6B

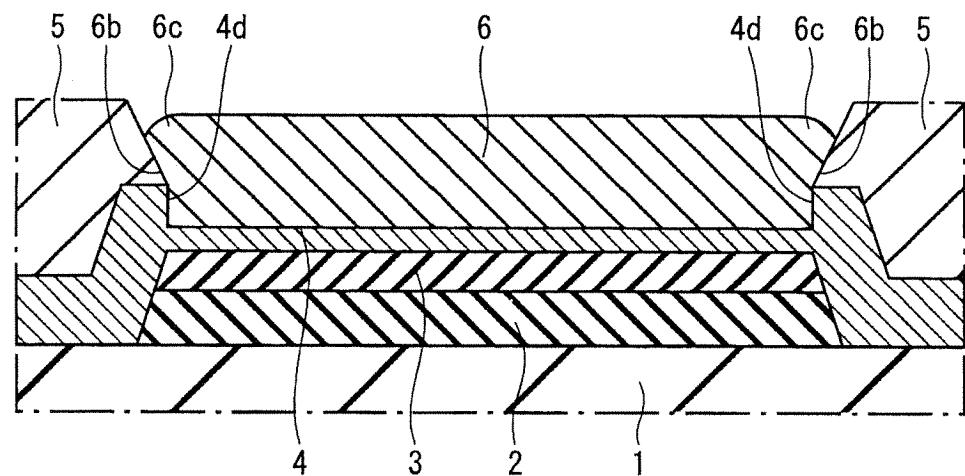


FIG. 7

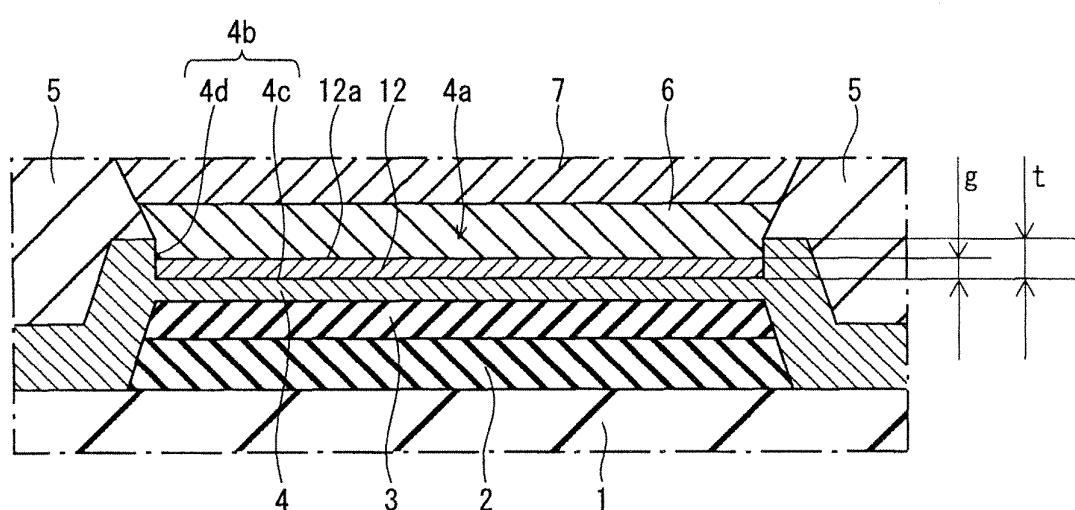


FIG. 8A

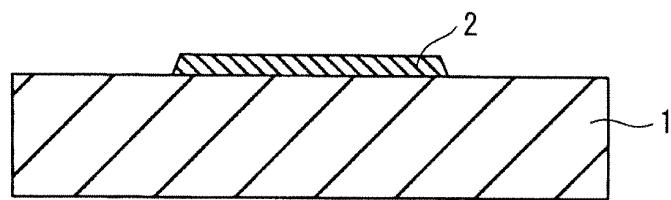


FIG. 8B

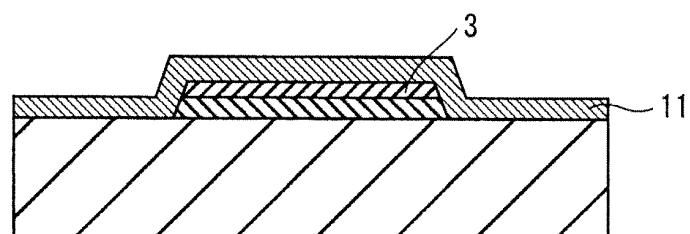


FIG. 8C

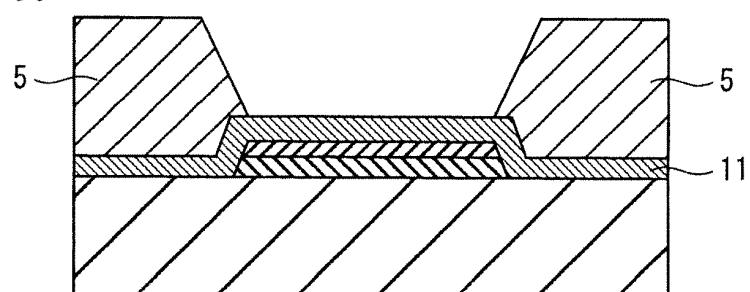


FIG. 8D

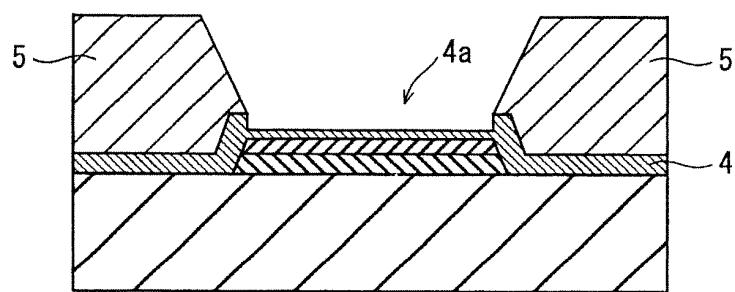


FIG. 9E

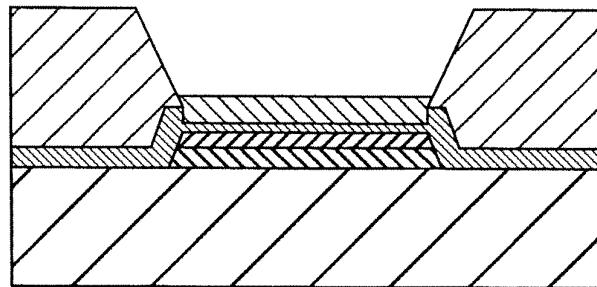


FIG. 9F

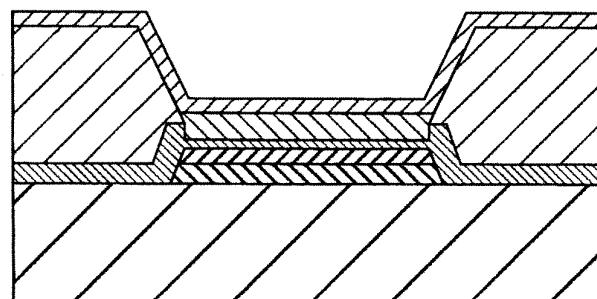


FIG. 9G

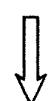
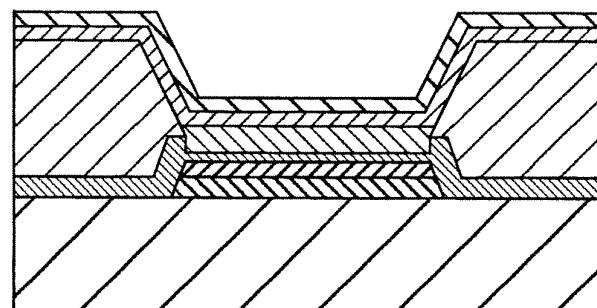


FIG. 9H

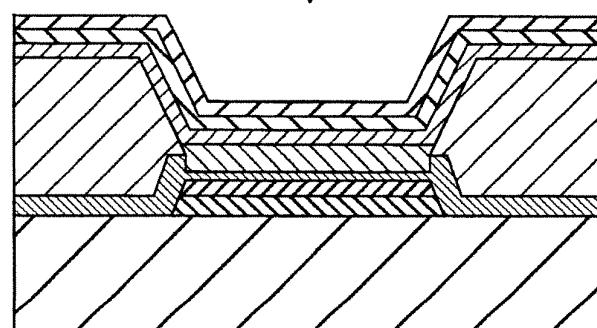


FIG. 10

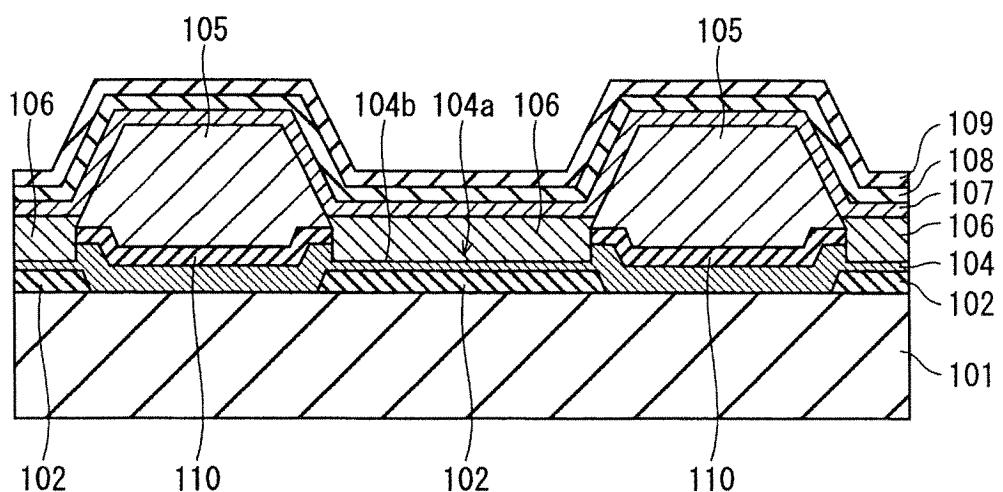


FIG. 11A

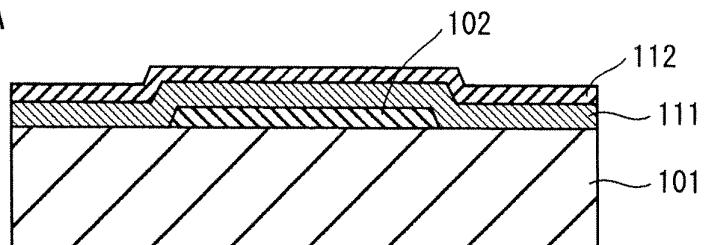


FIG. 11B

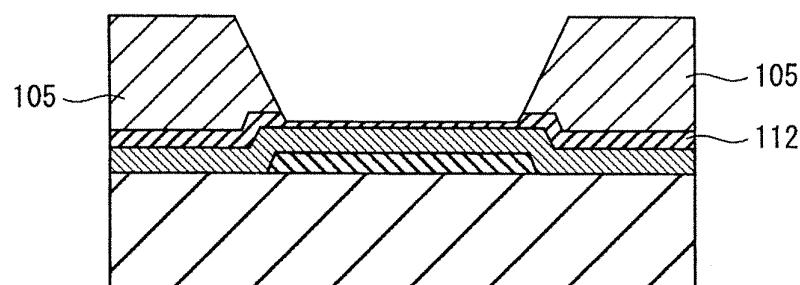


FIG. 11C

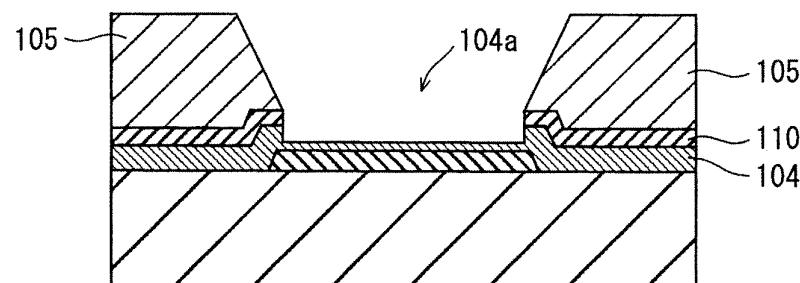


FIG. 11D

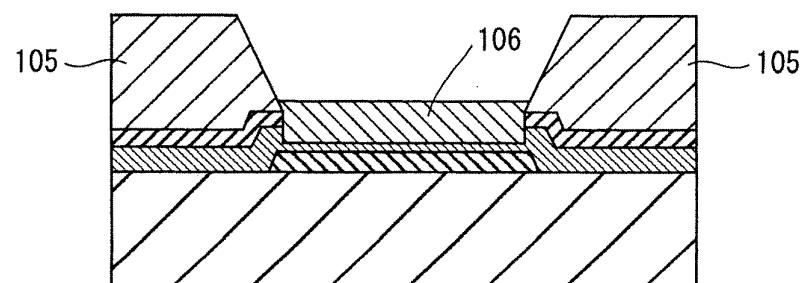


FIG. 12

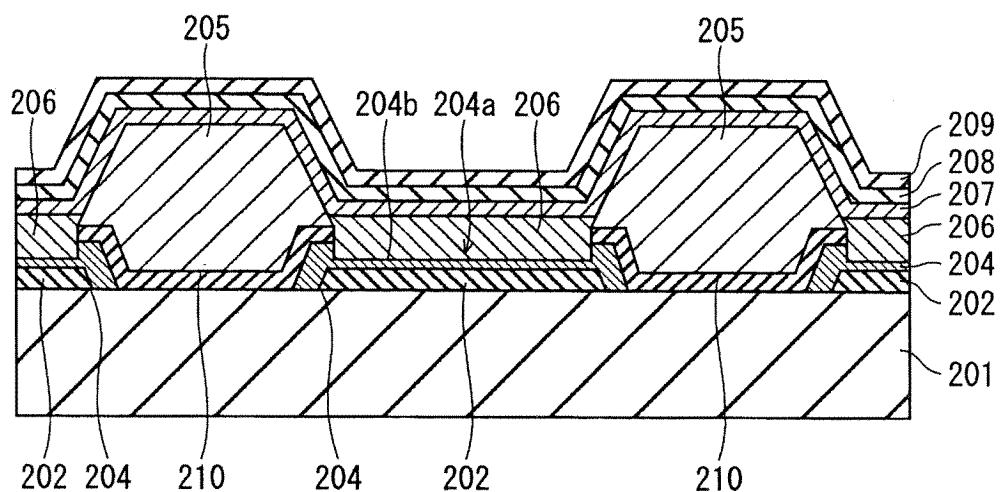


FIG. 13A

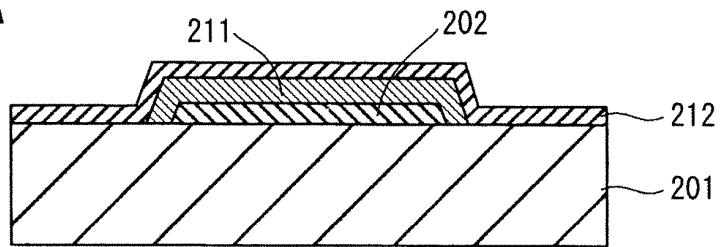


FIG. 13B

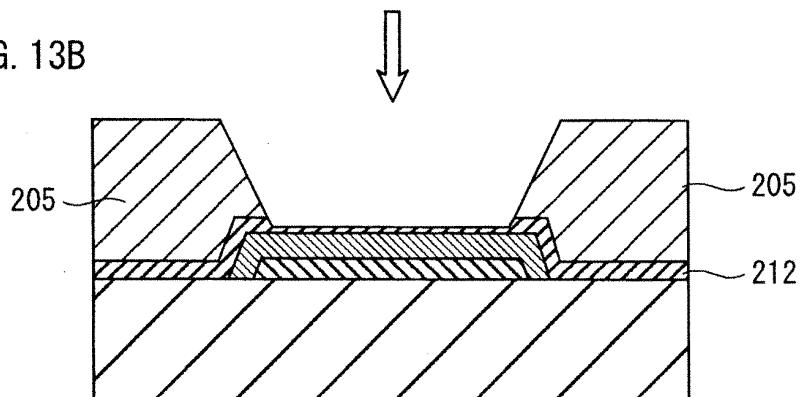


FIG. 13C

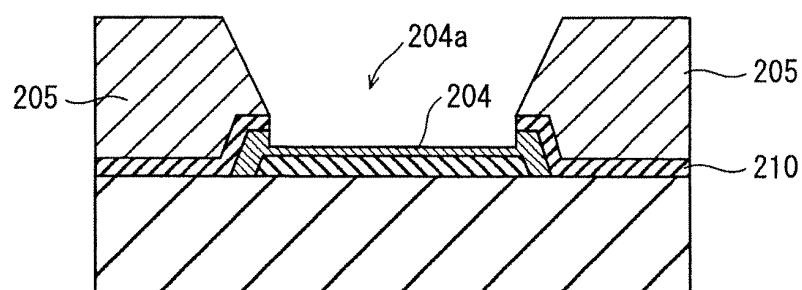


FIG. 13D

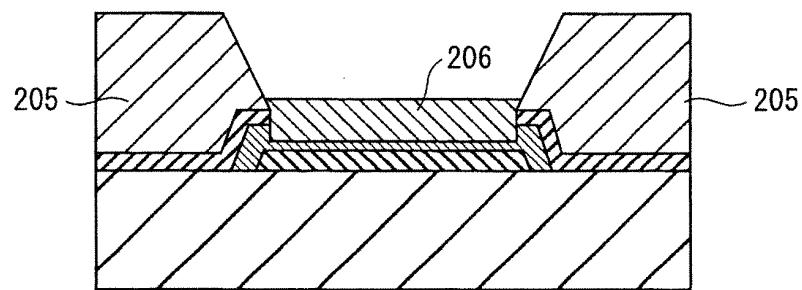
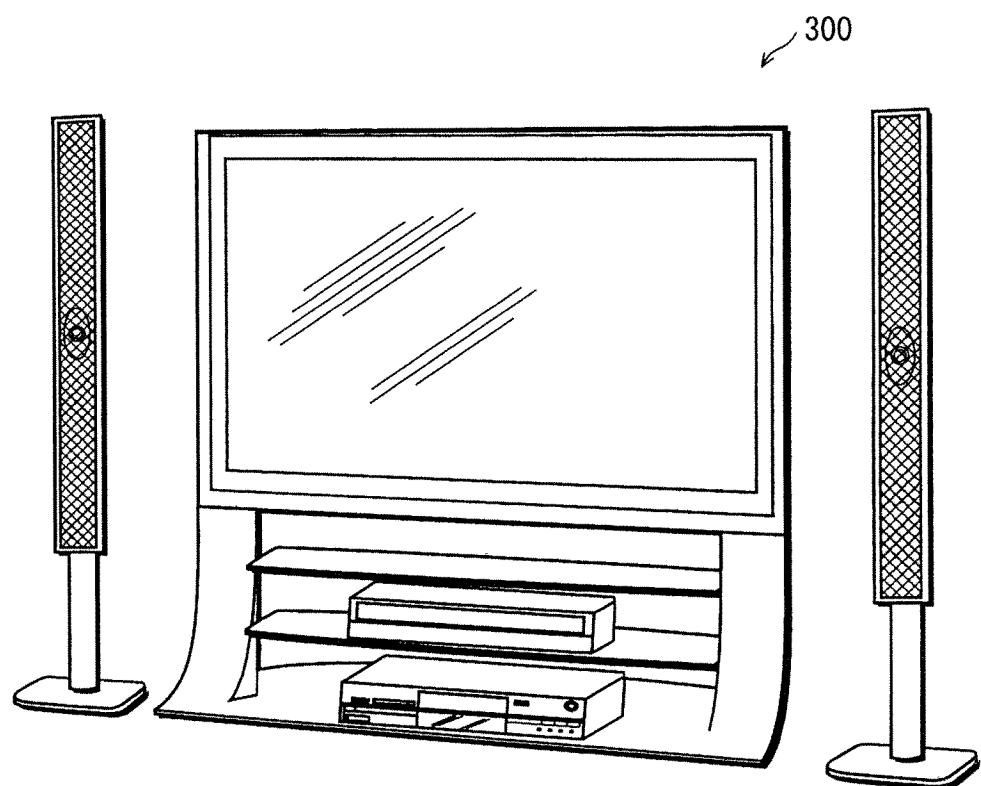


FIG. 14



INTERNATIONAL SEARCH REPORT		International application No. PCT/JP2010/000781
<b>A. CLASSIFICATION OF SUBJECT MATTER</b> <i>H01L51/50(2006.01)i, H05B33/10(2006.01)i, H05B33/12(2006.01)i, H05B33/22(2006.01)i</i>		
According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) <i>H01L51/50-51/56, H01L27/32</i>		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched <i>Jitsuyo Shinan Koho 1922-1996 Jitsuyo Shinan Toroku Koho 1996-2010  Kokai Jitsuyo Shinan Koho 1971-2010 Toroku Jitsuyo Shinan Koho 1994-2010</i>		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	WO 2008/149498 A1 (Panasonic Corp.), 11 December 2008 (11.12.2008), paragraphs [0078] to [0084]; fig. 4A & US 2009/0272999 A & EP 2077698 A1 & EP 2141964 A & KR 10-2009-0028513 A & CN 101543135 A	1-9
Y	WO 2008/120714 A1 (Dainippon Printing Co., Ltd.), 09 October 2008 (09.10.2008), paragraph [0036]; paragraph [0043] (organic luminescent layer 14 may be provided on the positive hole injection layer 12); paragraphs [0074], [0075]; fig. 4 (Family: none)	1-9
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: “ <b>A</b> ” document defining the general state of the art which is not considered to be of particular relevance “ <b>E</b> ” earlier application or patent but published on or after the international filing date “ <b>L</b> ” document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) “ <b>O</b> ” document referring to an oral disclosure, use, exhibition or other means “ <b>P</b> ” document published prior to the international filing date but later than the priority date claimed		
“ <b>T</b> ” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention “ <b>X</b> ” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone “ <b>Y</b> ” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art “ <b>&amp;</b> ” document member of the same patent family		
Date of the actual completion of the international search 24 March, 2010 (24.03.10)		Date of mailing of the international search report 06 April, 2010 (06.04.10)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.

Form PCT/ISA/210 (second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT		International application No. PCT/JP2010/000781
C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP 2004-234901 A (Seiko Epson Corp.), 19 August 2004 (19.08.2004), fig. 2 (Family: none)	1-9
A	WO 2008/149499 A1 (Panasonic Corp.), 11 December 2008 (11.12.2008), paragraphs [0025], [0042]; fig. 9 & US 2009/0284135 A & EP 2151867 A & KR 10-2009-0020644 A	1-9

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**REFERENCES CITED IN THE DESCRIPTION**

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**Patent documents cited in the description**

- JP 2003249375 A [0005]
- JP H5163488 B [0052]

专利名称(译)	发光元件，显示装置和发光元件的制造方法		
公开(公告)号	<a href="#">EP2398083A4</a>	公开(公告)日	2013-03-13
申请号	EP2010741075	申请日	2010-02-09
[标]申请(专利权)人(译)	松下电器产业株式会社		
申请(专利权)人(译)	松下电器产业株式会社		
当前申请(专利权)人(译)	松下电器产业株式会社		
[标]发明人	HARADA KENJI C/O PANASONIC CORPORATION IPROC TAKEUCHI TAKAYUKI C/O PANASONIC CORPORATION NISHIYAMA SEIJI C/O PANASONIC CORPORATION IPROC KOMATSU TAKAHIRO C/O PANASONIC CORPORATION IPROC		
发明人	HARADA, KENJI, C/O PANASONIC CORPORATION IPROC TAKEUCHI, TAKAYUKI, C/O PANASONIC CORPORATION NISHIYAMA, SEIJI, C/O PANASONIC CORPORATION IPROC KOMATSU, TAKAHIRO, C/O PANASONIC CORPORATION IPROC		
IPC分类号	H01L51/50 H05B33/10 H05B33/12 H05B33/22 H01L27/32 H01L51/00 H01L51/56		
CPC分类号	H01L27/3246 H01L51/0005 H01L51/0018 H01L51/5048 H01L51/56 H01L51/5296		
代理机构(译)	GRÜNECKER, KINKELDEY, STOCKMAIR & SCHWANHÄSSER		
优先权	2009028971 2009-02-10 JP		
其他公开文献	EP2398083A1 EP2398083B1		
外部链接	<a href="#">Espacenet</a>		

#### 摘要(译)

本发明提供一种发光体，其中图案化高清晰度发光层，并且可以低成本制造。为了实现该目的，发光体包括依次层叠的第一电极2，电荷注入传输层4，发光层6和第二电极8，并且至少发光层6是由堤5限定，堤5至少在其表面上是液体排斥的，并且电荷注入传输层4主要由比堤5的表面更亲液的金属化合物构成。而且，电荷注入传输层4具有凹陷结构，使得在由堤5限定的区域中，电荷注入传输层4低于堤5的底表面5a。