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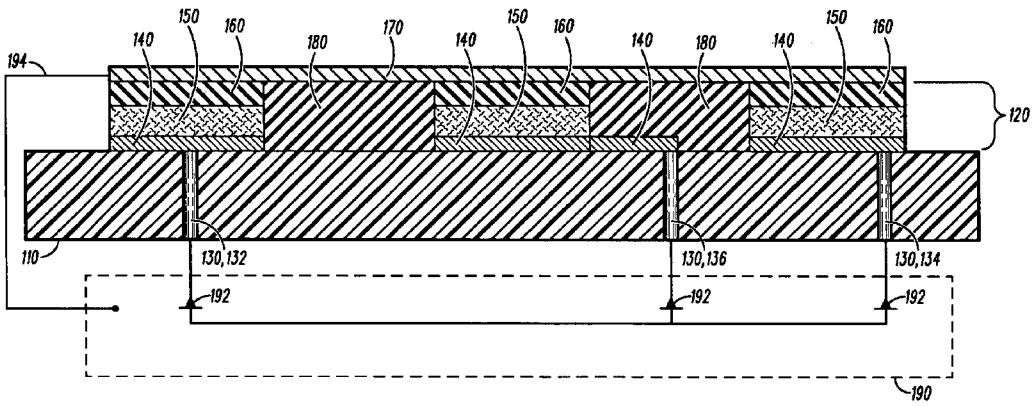
(54) **Title:** ELECTROLUMINESCENT DISPLAY HAVING A PIXEL ARRAY



(57) **Abstract:** An electroluminescent display contains an array of dynamically addressable pixels. The pixels are arranged on one side of a carrier substrate. Conductive vias in the substrate are electrically connected to each of the pixels. Each pixel consists of a bottom electrode that is coupled to a via, an electroluminescent material, and a dielectric material. A common top electrode is disposed on the dielectric material. A driver circuit conductor or connector is situated on the other side of the substrate and is electrically coupled to each of the conductive vias and to the common top electrode, so that each pixel can be individually addressed to illuminate the electroluminescent material on individual pixels.

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ELECTROLUMINESCENT DISPLAY HAVING A PIXEL ARRAY

FIELD OF THE INVENTION

This invention relates generally to luminescent displays. More particularly, this invention relates to electroluminescent displays arranged in pixel arrays.

BACKGROUND

Electroluminescent panels, lamps, and displays are light-emitting media for use in many applications. Electroluminescent (EL) panels are essentially a capacitor structure with an inorganic phosphor sandwiched between two electrodes. The resistance between the two electrodes is almost infinite and thus direct current (DC) will not pass through it. When an alternating voltage is applied, the build-up of a charge on the two surfaces effectively produces an increasing field (called an electric field) energizing the phosphors and resulting in the emission of light. The increase in voltage in one direction increases the field and this causes a current to flow. The voltage then decreases and rises in the opposite direction. This also causes a current to flow. The net result is that current flows into the electroluminescent panel and thus energy is delivered to the panel. This energy is converted to visible light by the inorganic phosphor, with little or no heat produced in the process. Application of an alternating current (AC) voltage across the electrodes generates a changing electric field within the phosphor particles, causing them to emit visible light. By making one or both of the electrodes so thin, transparent or translucent that light is able to pass through and be emitted to the environment, an optically transmissive path is available.

One particular area in which electroluminescent panels can be useful is in lighted advertising displays at the point of product purchase. In today's competitive global environment, local customization of the advertising display is often desirable to accommodate language nuances, local regulations, and cultural mores. Prior art displays are fabricated to depict a predetermined graphic or text, and thus are not amenable to situations where dynamic messages need to be displayed. This makes local customization very costly and/or impractical, with long lead times when changes in the message are needed. Additionally, producing small volumes of a display containing a fixed message can be costly, due to the cost of tooling.

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying figures, where like reference numerals refer to identical or functionally similar elements throughout the separate views and which together with the detailed description below are incorporated in and form part of the specification, serve to further illustrate various embodiments and to explain various principles and advantages all in accordance with the present invention.

FIGs. 1-4 are partial cross-sectional views of an electroluminescent display in accordance with certain embodiments of the present invention.

FIG. 5 is a partially exploded isometric view of an electroluminescent display in accordance with certain embodiments of the present invention.

DETAILED DESCRIPTION

As required, detailed embodiments of the present invention are disclosed herein; however, it is to be understood that the disclosed embodiments are merely

exemplary of the invention, which can be embodied in various forms. Therefore, specific structural and functional details disclosed herein are not to be interpreted as limiting, but merely as a basis for the claims and as a representative basis for teaching one skilled in the art to variously employ the present invention in virtually any appropriately detailed structure. Further, the terms and phrases used herein are not intended to be limiting; but rather, to provide an understandable description of the invention. The terms a or an, as used herein, are defined as one or more than one. The term plurality, as used herein, is defined as two or more than two. The term another, as used herein, is defined as at least a second or more. The terms including and/or having, as used herein, are defined as comprising (i.e., open language). The term coupled, as used herein, is defined as connected, although not necessarily directly, and not necessarily mechanically.

An electroluminescent display contains an array of dynamically addressable pixels. The pixels are arranged on one side of a carrier substrate. Conductive vias in the substrate are electrically connected to each of the pixels. Each pixel consists of a bottom electrode that is coupled to a via, an electroluminescent material, and a dielectric material. A common top electrode is disposed on the dielectric material. Driver circuit conductors are situated on the other side of the substrate, electrically coupled to each of the conductive vias and to the common top electrode, so that each pixel can be individually addressed to illuminate the electroluminescent material on individual pixels. Referring now to FIGs. 1 and 5, a pixelated electroluminescent (EL) display 100 is formed on a substrate 110 that has an array of pixel elements 120 disposed on an upper surface thereof. In one embodiment, the pixel elements are arranged in a regular array, but can also be arranged in a variety of layouts. Although

depicted as squares in FIG. 5, the pixel elements can be any of a number of shapes, such as, but not limited to, diamond, triangular, square, rectangular, pentagonal, hexagonal, octagonal, round, elliptical and polygonal. Compared to prior art EL displays, the pixel elements are generally smaller in size, closer together, and more numerous. Each of the pixels is electrically coupled to a conductive via 130 in the substrate, such that each pixel can be electrically routed to a location on the opposite side of the substrate. In one embodiment, the pixels are situated on top of a via, so that the via is contained within the perimeter of the pixel outline. In an alternate embodiment, the pixels are arranged so that the via is outside the perimeter of the pixel, and the pixel is then electrically coupled to the via by means of a conductive circuit trace on the upper surface of the substrate. Some of the various configurations of via and pixel are shown in FIG. 1, where the via is in the center of the pixel 132, the via is located off-center near the edge of the pixel 134, and the via is located remote from the pixel 136. These various arrangements can be used solely or in mixed fashion, as suits the needs of the designer in laying out the pixels and the electrical connections. The conductive vias 130 are formed in conventional fashion, such as, for example, plated through holes in a printed circuit board, and optionally filled with an electrically conductive or non-conductive material. Any of numerous methods familiar to those of ordinary skill in the art will suffice, as long as there is an electrical pathway from the pixel element to the other side of the substrate.

Each pixel element 120 contains a bottom electrode 140 that is disposed on the top surface of the substrate 110. The bottom electrode is typically an electrically conductive material such as copper, carbon, silver, platinum, titanium, indium-tin oxide, conductive alloys, etc. that is mechanically affixed to the surface of the

substrate. These electrodes may be formed in conventional fashion, such as electroless plating, electroplating, screen printing, vacuum deposition, etc. Overlying each electrode is an EL material 150 containing a phosphor. In one embodiment shown in FIG. 1, the EL material 150 is arranged such that it substantially corresponds to the size and shape of the bottom electrode 140. However, the invention is not so limited, as shown in FIG. 2 where the EL material 150 is larger than the corresponding bottom electrode. Configuring the display so that the EL material is the same size and shape as the bottom electrode will provide the sharpest image when the EL material is energized, since the resolution of the pixel is defined in this case by having all of the EL material energized. In the case where the EL material is larger than the bottom electrode, only those portions of the EL material that lie directly above the electrode will be energized, and those portions that do not overlie an electrode will not be energized. However, due to field effects, there will be some "drop-off" experienced at the boundary defined by the electrode, and the image may not be as sharp. Overlying each portion of the EL material 150 is a transparent or translucent dielectric material 160 that insulates the EL material from the other electrode. In one embodiment shown in FIG. 1, the dielectric material 160 is arranged such that it also substantially corresponds to the size and shape of the bottom electrode 140 and intervening EL material. However, the invention is not so limited, as shown in FIG. 2 where the dielectric material 160 is larger than the corresponding bottom electrode, and can actually span two or more pixels. In this configuration, portions of the dielectric material may also touch the surface of the substrate. The dielectric material 160 can, in one embodiment, be continuous over the entire pixel array, facilitating manufacture of the display.

Overlying the dielectric material 160 is a common top, or second, electrode 170. The second electrode is transparent or translucent so as to enable the EL material 150 to transmit the emitted visible light when energized. The top and bottom electrodes are electrically separated by EL material 150 and dielectric material 160. The top electrode 170 acts in concert with the bottom electrode 140 to form a capacitor-like structure that causes the phosphors in the EL material 150 that is sandwiched between the electrodes to fluoresce when the two electrodes are electrically energized. Since each bottom electrode in the array is individually addressable, the top electrode does not need to be individually addressable, but can instead be electrically common to all the bottom electrodes. Optionally, a second dielectric material 180 can be placed between the individual pixel elements 120 to fill in the space between the elements. This facilitates the formation of the top electrode 170, allowing it to be a single continuous layer over the pixels 120 and the second dielectric 180.

A driver circuit 190 is electrically coupled to each of the pixel elements 120 and to the top electrode 170. Driver circuits are commonly known and used in devices such as liquid crystal displays (LCD) to selectively address the various segments of the LCD. In simplistic form, driver circuits contain a plurality of switches (typically transistors) that can be turned on and off to address the various pixels as desired. Each of the switches 192 is coupled to a single conductive via, and in turn, to a single pixel, so as to make each pixel individually addressable. Another portion 194 of the driver circuit 190 is coupled to the common top electrode 170. When any one or more of the individual switches 192 is enabled, an electrical circuit is completed from the bottom electrode through the EL material to the top electrode, causing that individual

segment of EL material to fluoresce and emit visible light. By selectively energizing the various pixels, the array of pixels can be caused to form a dynamic display that can be rapidly changed, much in the manner of an LCD.

Driver circuit conductors are situated on the bottom of the substrate 110, opposite the side that contains the pixels 120. The driver circuit can be located anywhere, on a separate module or even on the top surface, as long as the conductors leading to the driver circuits are electrically coupled to the vias. This facilitates the routing of the various electrical connections required to couple the individual pixels in the array to the driver circuit. In one embodiment, the circuitry on the back side of the substrate is routed in multiple layers. In another embodiment, the driver circuit 190 is laminated to the back side of the substrate to form a monolithic package.

Having now described our invention, we now present additional embodiments. Referring now to FIGs. 3 and 4, a pixelated EL display 300 has the arrangement of the EL material and the dielectric material reversed from that shown in FIGs. 1 and 2. That is, a layer of dielectric material 160 is first disposed on each of the bottom electrodes 140, then a layer of EL material 150 is disposed on top of the dielectric material. The common top electrode 170 is then formed on top of the EL material. All other portions of the structure are similar to that described for FIGs. 1 and 2.

In summary, without intending to limit the scope of the invention, operation of a pixelated electroluminescent display according to certain embodiments of the invention can be carried out by coupling an array of dynamically addressable pixels on one side of a carrier substrate to a driver circuit situated on the other side of the substrate. Conductive vias in the substrate electrically connect each of the pixels to the driver. Each pixel consists of a bottom electrode that is coupled to a via, an

electroluminescent material, a dielectric material, and a common top electrode. The driver circuit makes each pixel individually addressable to illuminate the electroluminescent material in individual pixels.

While the invention has been described in conjunction with specific embodiments, it is evident that many alternatives, modifications, permutations and variations will become apparent to those of ordinary skill in the art in light of the foregoing description. Accordingly, it is intended that the present invention embrace all such alternatives, modifications and variations as fall within the scope of the appended claims.

What is claimed is:

1. An electroluminescent display, comprising:

a substrate having an array of conductive vias;

an array of pixel elements arranged on a first face of the substrate, each pixel element comprising;

a first electrode electrically coupled to one of the conductive vias;

electroluminescent material disposed on the first electrode; and

a transparent or translucent dielectric material disposed on the electroluminescent material;

a transparent or translucent common second electrode disposed on the dielectric material; and

a driver circuit situated on an opposing second face of the substrate and electrically coupled to each of the conductive vias and to the common second electrode, such that each pixel is individually addressable sufficient to illuminate the electroluminescent material on individual pixels.

2. The apparatus as described in claim 1, wherein the electroluminescent material covers the first electrodes and portions of the substrate.

3. The apparatus as described in claim 1, wherein the dielectric material covers the electroluminescent material and portions of the substrate.

4. The apparatus as described in claim 1, wherein the array is a regular array.

5. The apparatus as described in claim 1, wherein the first electrode is situated on the conductive via.

6. The apparatus as described in claim 1, wherein the first electrode is electrically coupled to the conductive via by means of a conductive circuit trace.

7. The apparatus as described in claim 1, wherein the driver circuit is laminated to the second face of the substrate.

8. The apparatus as described in claim 1, further comprising a second dielectric material disposed on the first face of the substrate between the individual pixel elements.

9. An electroluminescent display, comprising:

a substrate having an array of conductive vias;

a plurality of first electrodes disposed on a first face of the substrate, and corresponding to the array of conductive vias, each of the first electrodes electrically coupled to a respective one of the conductive vias;

electroluminescent pixels corresponding to the plurality of first electrodes, wherein one electroluminescent pixel is disposed on each of the first electrodes;

a transparent or translucent dielectric material disposed on each of the plurality of electroluminescent pixels;

a transparent or translucent common second electrode disposed on the dielectric material; and

a driver circuit conductor situated on a second face of the substrate and electrically coupled to each of the conductive vias and to the common second electrode, such that each of the plurality of first electrodes is individually addressable to illuminate individual electroluminescent pixels.

10. The apparatus as described in claim 9, wherein the array is a regular array.

11. The apparatus as described in claim 9, wherein the first electrode is situated on the conductive via.

12. The apparatus as described in claim 9, wherein the first electrode is electrically coupled to the conductive via by means of a conductive circuit trace.

13. The apparatus as described in claim 9, wherein the driver circuit is laminated to the second face of the substrate.

14. The apparatus as described in claim 9, further comprising a second dielectric material situated on the first face of the substrate and surrounding the first electrodes.

15. An electroluminescent display, comprising:

a substrate having an array of conductive vias;

an array of pixel elements arranged on a first face of the substrate, each pixel element comprising;

a first electrode electrically coupled to one of the conductive vias;

a common dielectric material disposed on the first electrode; and

electroluminescent material disposed on the dielectric material;

a transparent or translucent common second electrode disposed on the electroluminescent material; and

a driver circuit situated on an opposing second face of the substrate and electrically coupled to each of the conductive vias and to the common second electrode, such that each pixel is individually addressable sufficient to illuminate the electroluminescent material on individual pixels.

16. The apparatus as described in claim 15, wherein the dielectric material covers the plurality of first electrodes and portions of the substrate.

17. The apparatus as described in claim 15, wherein the electroluminescent material covers the dielectric material and portions of the substrate.

18. The apparatus as described in claim 15, wherein the array is a regular array.

19. The apparatus as described in claim 15, wherein the first electrode is situated on the conductive via.

20. The apparatus as described in claim 15, wherein the first electrode is electrically coupled to the conductive via by means of a conductive circuit trace.

21. The apparatus as described in claim 15, wherein the driver circuit is laminated to the second face of the substrate.

22. An electroluminescent display, comprising:

a substrate having an array of conductive vias;

a plurality of first electrodes disposed on a first face of the substrate, and corresponding to the array of conductive vias, each of the first electrodes electrically coupled to a respective one of the conductive vias;

a common dielectric material disposed on each of the plurality of first electrodes;

an electroluminescent material disposed on the dielectric material and corresponding to each of the plurality of first electrodes;

a transparent or translucent common second electrode disposed on the electroluminescent material; and

a driver circuit conductor situated on a second face of the substrate and electrically coupled to each of the conductive vias and to the common second electrode, such that each of the plurality of first electrodes is individually addressable to illuminate individual electroluminescent pixels.

23. The apparatus as described in claim 22, wherein the array is a regular array.

24. The apparatus as described in claim 22, wherein the first electrode is situated on the conductive via.

25. The apparatus as described in claim 22, wherein the first electrode is electrically coupled to the conductive via by means of a conductive circuit trace.

26. The apparatus as described in claim 22, wherein the driver circuit is laminated to the second face of the substrate.

27. The apparatus as described in claim 22, further comprising a second dielectric material situated on the first face of the substrate and surrounding the first electrodes.

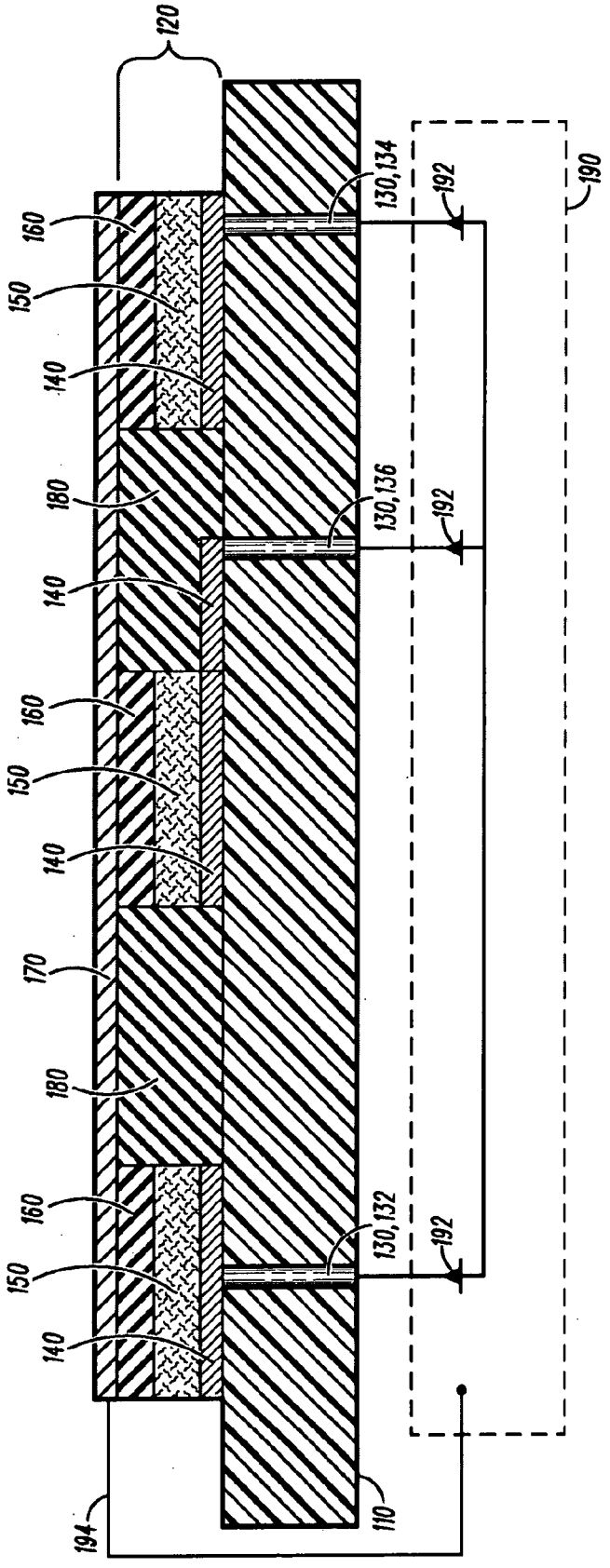
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FIG. 1

100

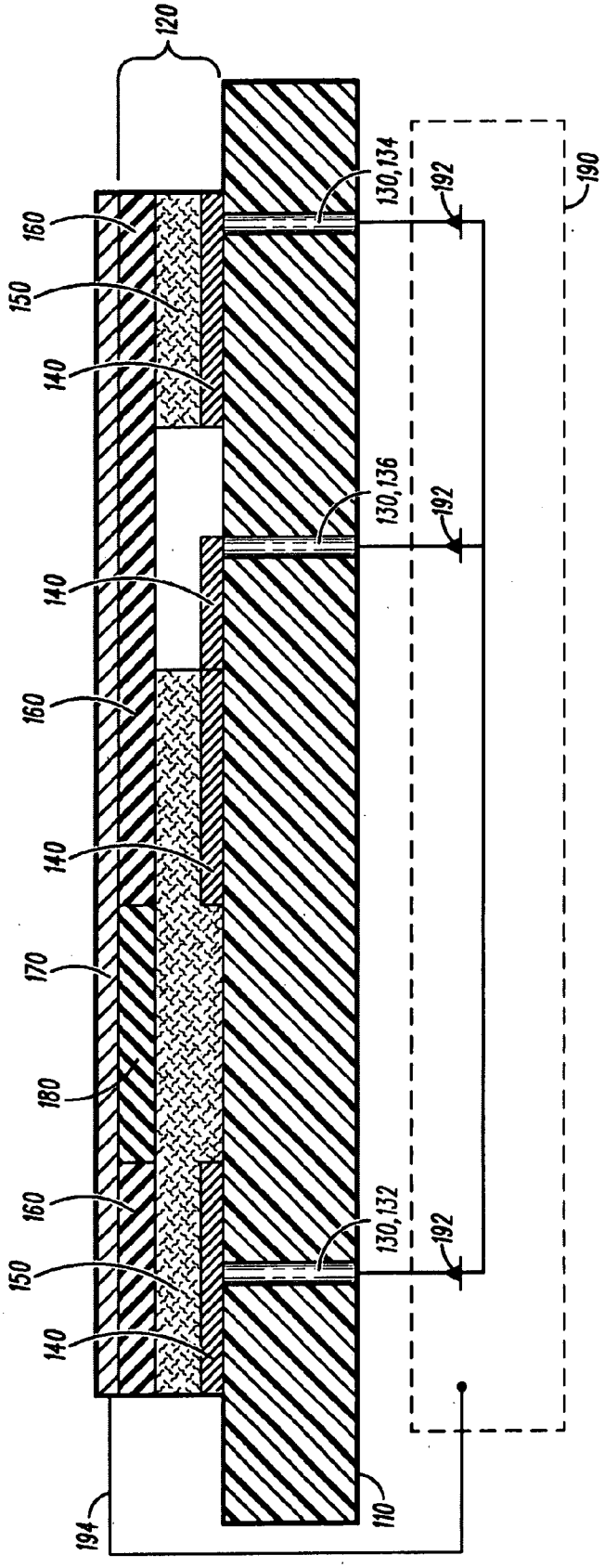
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FIG. 2

200

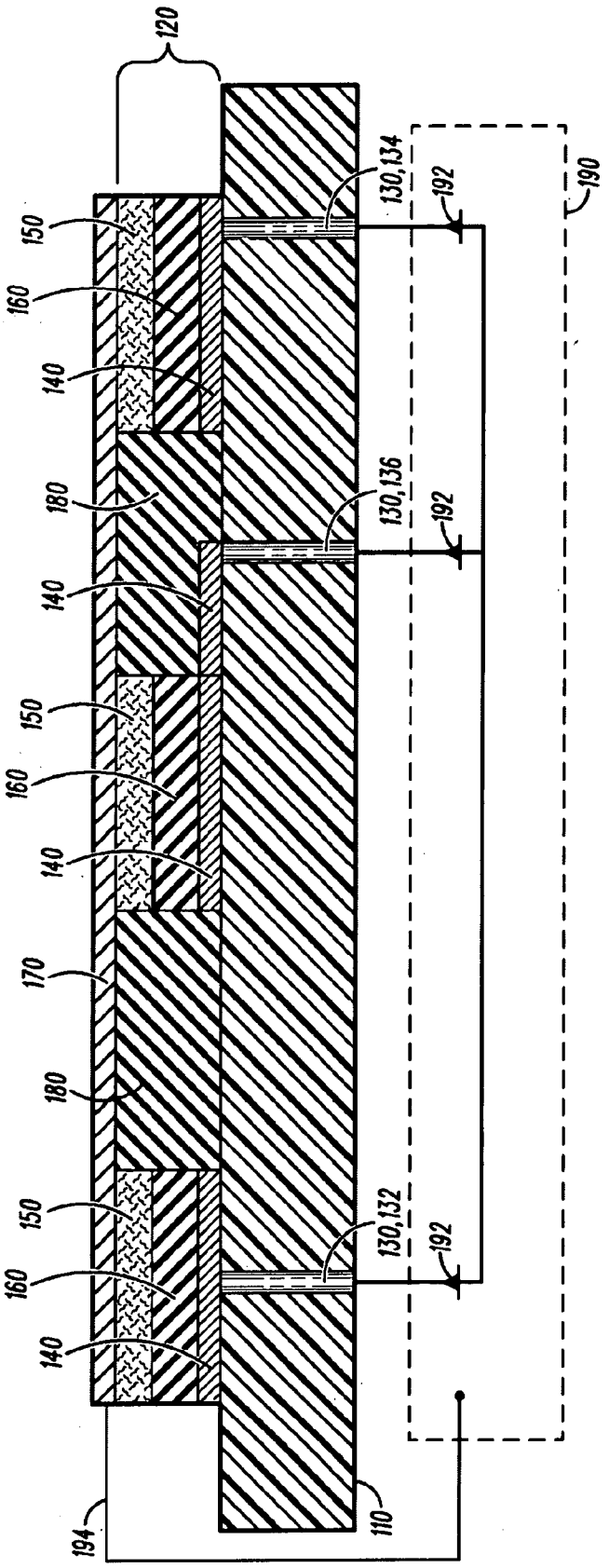
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FIG. 3

300

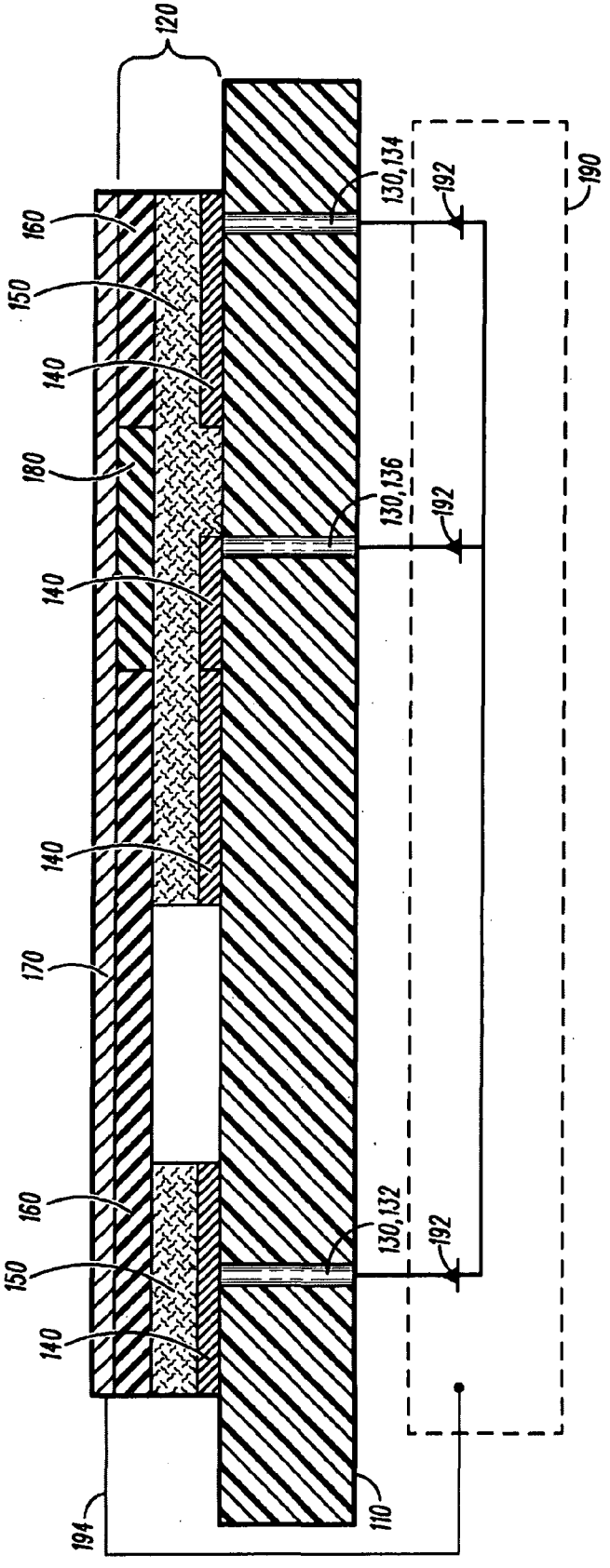
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FIG. 4

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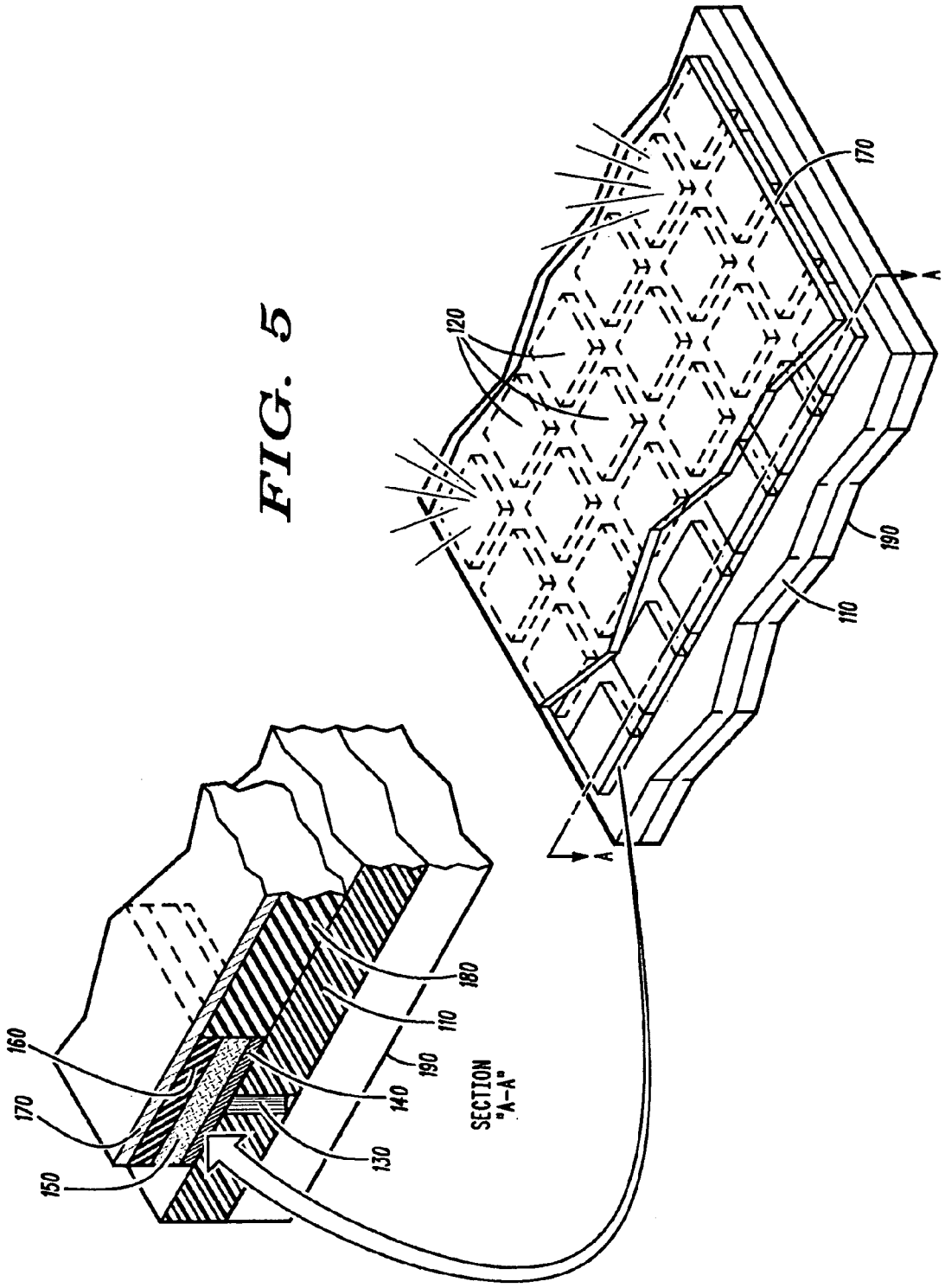
FIG. 5



专利名称(译)	具有像素阵列的电致发光显示器		
公开(公告)号	EP2050090A2	公开(公告)日	2009-04-22
申请号	EP2007812141	申请日	2007-06-14
[标]申请(专利权)人(译)	摩托罗拉公司		
申请(专利权)人(译)	MOTOROLA，INC.		
当前申请(专利权)人(译)	MOTOROLA MOBILITY LLC		
[标]发明人	JONNALAGADDA KRISHNA D CHASON MARC K GAMOTA DANIEL R ZHANG JIE		
发明人	JONNALAGADDA, KRISHNA, D. CHASON, MARC, K. GAMOTA, DANIEL, R. ZHANG, JIE		
IPC分类号	G09G3/30 G09G3/06 G09G3/12		
CPC分类号	H05B33/06		
优先权	11/461056 2006-07-31 US		
其他公开文献	EP2050090A4 EP2050090B1		
外部链接	Espacenet		

摘要(译)

电致发光显示器包含可动态寻址的像素阵列。像素布置在载体基板的一侧。衬底中的导电通孔电连接到每个像素。每个像素包括耦合到通孔的底部电极，电致发光材料和电介质材料。公共顶部电极设置在介电材料上。驱动器电路导体或连接器位于基板的另一侧并且电耦合到每个导电通孔和公共顶部电极，使得每个像素可以被单独寻址以照射各个像素上的电致发光材料。