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(54) **PIXEL CIRCUITS FOR AMOLED DISPLAYS**

(57) The OLED voltage of a selected pixel is extracted from the pixel produced when the pixel is programmed so that the pixel current is a function of the OLED voltage. One method for extracting the OLED voltage is to first program the pixel in a way that the current is not a function of OLED voltage, and then in a way that the current is a function of OLED voltage. During the latter stage, the programming voltage is changed so that the pixel current is the same as the pixel current when the pixel was programmed in a way that the current was not a function of OLED voltage. The difference in the two programming voltages is then used to extract the OLED voltage.

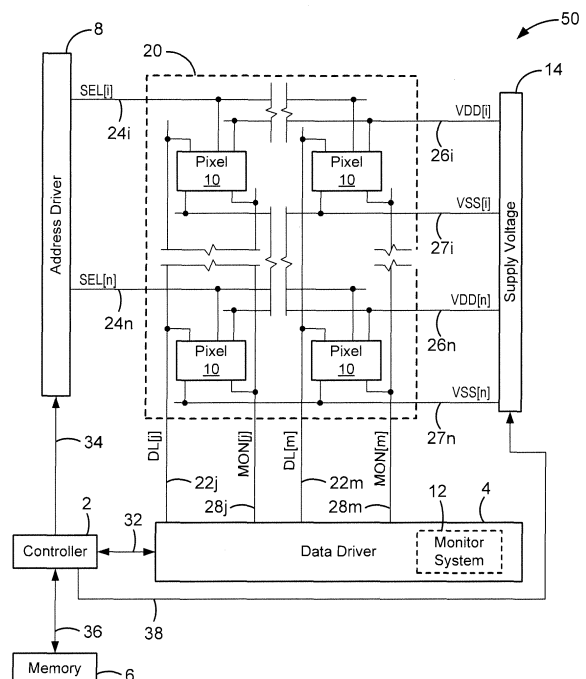


FIG. 1

Description

FIELD OF THE INVENTION

[0001] The present disclosure generally relates to circuits for use in displays, and methods of driving, calibrating, and programming displays, particularly displays such as active matrix organic light emitting diode displays.

BACKGROUND

[0002] Displays can be created from an array of light emitting devices each controlled by individual circuits (i.e., pixel circuits) having transistors for selectively controlling the circuits to be programmed with display information and to emit light according to the display information. Thin film transistors ("TFTs") fabricated on a substrate can be incorporated into such displays. TFTs tend to demonstrate non-uniform behavior across display panels and over time as the displays age. Compensation techniques can be applied to such displays to achieve image uniformity across the displays and to account for degradation in the displays as the displays age.

[0003] Some schemes for providing compensation to displays to account for variations across the display panel and over time utilize monitoring systems to measure time dependent parameters associated with the aging (i.e., degradation) of the pixel circuits. The measured information can then be used to inform subsequent programming of the pixel circuits so as to ensure that any measured degradation is accounted for by adjustments made to the programming. Such monitored pixel circuits may require the use of additional transistors and/or lines to selectively couple the pixel circuits to the monitoring systems and provide for reading out information. The incorporation of additional transistors and/or lines may undesirably decrease pixel-pitch (i.e., "pixel density").

SUMMARY

[0004] In accordance with one embodiment, the OLED voltage of a selected pixel is extracted from the pixel produced when the pixel is programmed so that the pixel current is a function of the OLED voltage. One method for extracting the OLED voltage is to first program the pixel in a way that the current is not a function of OLED voltage, and then in a way that the current is a function of OLED voltage. During the latter stage, the programming voltage is changed so that the pixel current is the same as the pixel current when the pixel was programmed in a way that the current was not a function of OLED voltage. The difference in the two programming voltages is then used to extract the OLED voltage.

[0005] Another method for extracting the OLED voltage is to measure the difference between the current of the pixel when it is programmed with a fixed voltage in both methods (being affected by OLED voltage and not

being affected by OLED voltage). This measured difference and the current-voltage characteristics of the pixel are then used to extract the OLED voltage.

[0006] A further method for extracting the shift in the OLED voltage is to program the pixel for a given current at time zero (before usage) in a way that the pixel current is a function of OLED voltage, and save the programming voltage. To extract the OLED voltage shift after some usage time, the pixel is programmed for the given current as was done at time zero. To get the same current as time zero, the programming voltage needs to change. The difference in the two programming voltages is then used to extract the shift in the OLED voltage. Here one needs to remove the effect of TFT aging from the second programming voltage first; this is done by programming the pixel without OLED effect for a given current at time zero and after usage. The difference in the programming voltages in this case is the TFT aging, which is subtracted from the calculated different in the aforementioned case.

[0007] In one implementation, the current effective voltage V_{OLED} of a light-emitting device in a selected pixel is determined by supplying a programming voltage to the drive transistor in the selected pixel to supply a first current to the light-emitting device (the first current being independent of the effective voltage V_{OLED} of the light-emitting device), measuring the first current, supplying a second programming voltage to the drive transistor in the selected pixel to supply a second current to the light-emitting device, the second current being a function of the current effective voltage V_{OLED} of the light-emitting device, measuring the second current and comparing the first and second current measurements, adjusting the second programming voltage to make the second current substantially the same as the first current, and extracting the value of the current effective voltage V_{OLED} of the light-emitting device from the difference between the first and second programming voltages.

[0008] In another implementation, the current effective voltage V_{OLED} of a light-emitting device in a selected pixel is determined by supplying a first programming voltage to the drive transistor in the selected pixel to supply a first current to the light-emitting device in the selected pixel (the first current being independent of the effective voltage V_{OLED} of the light-emitting device), measuring the first current, supplying a second programming voltage to the drive transistor in the selected pixel to supply a second current to the light-emitting device in the selected pixel (the second current being a function of the current effective voltage V_{OLED} of the light-emitting device), measuring the second current, and extracting the value of the current effective voltage V_{OLED} of the light-emitting device from the difference between the first and second current measurements.

[0009] In a modified implementation, the current effective voltage V_{OLED} of a light-emitting device in a selected pixel is determined by supplying a first programming voltage to the drive transistor in the selected pixel to supply a predetermined current to the light-emitting device at a

first time (the first current being a function of the effective voltage V_{OLED} of the light-emitting device), supplying a second programming voltage to the drive transistor in the selected pixel to supply the predetermined current to the light-emitting device at a second time following substantial usage of the display, and extracting the value of the current effective voltage V_{OLED} of the light-emitting device from the difference between the first and second programming voltages.

[0010] In another modified implementation, the current effective voltage V_{OLED} of a light-emitting device in a selected pixel is determined by supplying a predetermined programming voltage to the drive transistor in the selected pixel to supply a first current to the light-emitting device (the first current being independent of the effective voltage V_{OLED} of the light-emitting device), measuring the first current, supplying the predetermined programming voltage to the drive transistor in the selected pixel to supply a second current to the light-emitting device (the second current being a function of the current effective voltage V_{OLED} of the light-emitting device), measuring the second current, and extracting the value of the current effective voltage V_{OLED} of the light-emitting device from the difference between the first and second currents and current-voltage characteristics of the selected pixel.

[0011] In a preferred implementation, a system is provided for controlling an array of pixels in a display in which each pixel includes a light-emitting device. Each pixel includes a pixel circuit that comprises the light-emitting device, which emits light when supplied with a voltage V_{OLED} ; a drive transistor for driving current through the light-emitting device according to a driving voltage across the drive transistor during an emission cycle, the drive transistor having a gate, a source and a drain and characterized by a threshold voltage; and a storage capacitor coupled across the source and gate of the drive transistor for providing the driving voltage to the drive transistor. A supply voltage source is coupled to the drive transistor for supplying current to the light-emitting device via the drive transistor, the current being controlled by the driving voltage. A monitor line is coupled to a read transistor that controls the coupling of the monitor line to a first node that is common to the source side of the storage capacitor, the source of the drive transistor, and the light-emitting device. A data line is coupled to a switching transistor that controls the coupling of the data line to a second node that is common to the gate side of the storage capacitor and the gate of the drive transistor. A controller coupled to the data and monitor lines and to the switching and read transistors is adapted to:

- (1) during a first cycle, turn on the switching and read transistors while delivering a voltage V_b to the monitor line and a voltage V_{d1} to the data line, to supply the first node with a voltage that is independent of the voltage across the light-emitting device,
- (2) during a second cycle, turn on the read transistor and turn off the switching transistor while delivering

a voltage V_{ref} to the monitor line, and read a first sample of the drive current at the first node via the read transistor and the monitor line,

(3) during a third cycle, turn off the read transistor and turn on the switching transistor while delivering a voltage V_{d2} to the data line, so that the voltage at the second node is a function of V_{OLED} , and

(4) during a fourth cycle, turn on said read transistor and turn off said switching transistor while delivering a voltage V_{ref} to said monitor line, and read a second sample the drive current at said first node via said read transistor and said monitor line. The first and second samples of the drive current are compared and, if they are different, the first through fourth cycles are repeated using an adjusted value of at least one of the voltages V_{d1} and V_{d2} , until the first and second samples are substantially the same.

[0012] The foregoing and additional aspects and embodiments of the present invention will be apparent to those of ordinary skill in the art in view of the detailed description of various embodiments and/or aspects, which is made with reference to the drawings, a brief description of which is provided next.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] The foregoing and other advantages of the invention will become apparent upon reading the following detailed description and upon reference to the drawings.

FIG. 1 is a block diagram of an exemplary configuration of a system for driving an OLED display while monitoring the degradation of the individual pixels and providing compensation therefor.

FIG. 2A is a circuit diagram of an exemplary pixel circuit configuration.

FIG. 2B is a timing diagram of first exemplary operation cycles for the pixel shown in FIG. 2A.

FIG. 2C is a timing diagram of second exemplary operation cycles for the pixel shown in FIG. 2A.

FIG. 3 is a circuit diagram of another exemplary pixel circuit configuration.

FIG. 4 is a block diagram of a modified configuration of a system for driving an OLED display using a shared readout circuit, while monitoring the degradation of the individual pixels and providing compensation therefor.

[0014] While the invention is susceptible to various modifications and alternative forms, specific embodiments have been shown by way of example in the drawings and will be described in detail herein. It should be understood, however, that the invention is not intended to be limited to the particular forms disclosed. Rather, the invention is to cover all modifications, equivalents, and alternatives falling within the spirit and scope of the invention as defined by the appended claims.

DETAILED DESCRIPTION

[0015] FIG. 1 is a diagram of an exemplary display system 50. The display system 50 includes an address driver 8, a data driver 4, a controller 2, a memory storage 6, and display panel 20. The display panel 20 includes an array of pixels 10 arranged in rows and columns. Each of the pixels 10 is individually programmable to emit light with individually programmable luminance values. The controller 2 receives digital data indicative of information to be displayed on the display panel 20. The controller 2 sends signals 32 to the data driver 4 and scheduling signals 34 to the address driver 8 to drive the pixels 10 in the display panel 20 to display the information indicated. The plurality of pixels 10 associated with the display panel 20 thus comprise a display array ("display screen") adapted to dynamically display information according to the input digital data received by the controller 2. The display screen can display, for example, video information from a stream of video data received by the controller 2. The supply voltage 14 can provide a constant power voltage or can be an adjustable voltage supply that is controlled by signals from the controller 2. The display system 50 can also incorporate features from a current source or sink (not shown) to provide biasing currents to the pixels 10 in the display panel 20 to thereby decrease programming time for the pixels 10.

[0016] For illustrative purposes, the display system 50 in FIG. 1 is illustrated with only four pixels 10 in the display panel 20. It is understood that the display system 50 can be implemented with a display screen that includes an array of similar pixels, such as the pixels 10, and that the display screen is not limited to a particular number of rows and columns of pixels. For example, the display system 50 can be implemented with a display screen with a number of rows and columns of pixels commonly available in displays for mobile devices, monitor-based devices, and/or projection-devices.

[0017] The pixel 10 is operated by a driving circuit ("pixel circuit") that generally includes a driving transistor and a light emitting device. Hereinafter the pixel 10 may refer to the pixel circuit. The light emitting device can optionally be an organic light emitting diode, but implementations of the present disclosure apply to pixel circuits having other electroluminescence devices, including current-driven light emitting devices. The driving transistor in the pixel 10 can optionally be an n-type or p-type amorphous silicon thin-film transistor, but implementations of the present disclosure are not limited to pixel circuits having a particular polarity of transistor or only to pixel circuits having thin-film transistors. The pixel circuit 10 can also include a storage capacitor for storing programming information and allowing the pixel circuit 10 to drive the light emitting device after being addressed. Thus, the display panel 20 can be an active matrix display array.

[0018] As illustrated in FIG. 1, the pixel 10 illustrated as the top-left pixel in the display panel 20 is coupled to a select line 24i, a supply line 26i, a data line 22j, and a

monitor line 28j. A read line may also be included for controlling connections to the monitor line. In one implementation, the supply voltage 14 can also provide a second supply line to the pixel 10. For example, each pixel can be coupled to a first supply line 26 charged with V_{dd} and a second supply line 27 coupled with V_{ss}, and the pixel circuits 10 can be situated between the first and second supply lines to facilitate driving current between the two supply lines during an emission phase of the pixel circuit. The top-left pixel 10 in the display panel 20 can correspond a pixel in the display panel in a "ith" row and "jth" column of the display panel 20. Similarly, the top-right pixel 10 in the display panel 20 represents a "jth" row and "mth" column; the bottom-left pixel 10 represents an "nth" row and "jth" column; and the bottom-right pixel 10 represents an "nth" row and "mth" column. Each of the pixels 10 is coupled to appropriate select lines (e.g., the select lines 24i and 24n), supply lines (e.g., the supply lines 26i and 26n), data lines (e.g., the data lines 22j and 22m), and monitor lines (e.g., the monitor lines 28j and 28m). It is noted that aspects of the present disclosure apply to pixels having additional connections, such as connections to additional select lines, and to pixels having fewer connections, such as pixels lacking a connection to a monitoring line.

[0019] With reference to the top-left pixel 10 shown in the display panel 20, the select line 24i is provided by the address driver 8, and can be utilized to enable, for example, a programming operation of the pixel 10 by activating a switch or transistor to allow the data line 22j to program the pixel 10. The data line 22j conveys programming information from the data driver 4 to the pixel 10. For example, the data line 22j can be utilized to apply a programming voltage or a programming current to the pixel 10 in order to program the pixel 10 to emit a desired amount of luminance. The programming voltage (or programming current) supplied by the data driver 4 via the data line 22j is a voltage (or current) appropriate to cause the pixel 10 to emit light with a desired amount of luminance according to the digital data received by the controller 2. The programming voltage (or programming current) can be applied to the pixel 10 during a programming operation of the pixel 10 so as to charge a storage device within the pixel 10, such as a storage capacitor, thereby enabling the pixel 10 to emit light with the desired amount of luminance during an emission operation following the programming operation. For example, the storage device in the pixel 10 can be charged during a programming operation to apply a voltage to one or more of a gate or a source terminal of the driving transistor during the emission operation, thereby causing the driving transistor to convey the driving current through the light emitting device according to the voltage stored on the storage device.

[0020] Generally, in the pixel 10, the driving current that is conveyed through the light emitting device by the driving transistor during the emission operation of the pixel 10 is a current that is supplied by the first supply

line 26i and is drained to a second supply line 27i. The first supply line 26i and the second supply line 27i are coupled to the voltage supply 14. The first supply line 26i can provide a positive supply voltage (e.g., the voltage commonly referred to in circuit design as "Vdd") and the second supply line 27i can provide a negative supply voltage (e.g., the voltage commonly referred to in circuit design as "Vss"). Implementations of the present disclosure can be realized where one or the other of the supply lines (e.g., the supply line 27i) is fixed at a ground voltage or at another reference voltage.

[0021] The display system 50 also includes a monitoring system 12. With reference again to the top left pixel 10 in the display panel 20, the monitor line 28j connects the pixel 10 to the monitoring system 12. The monitoring system 12 can be integrated with the data driver 4, or can be a separate stand-alone system. In particular, the monitoring system 12 can optionally be implemented by monitoring the current and/or voltage of the data line 22j during a monitoring operation of the pixel 10, and the monitor line 28j can be entirely omitted. Additionally, the display system 50 can be implemented without the monitoring system 12 or the monitor line 28j. The monitor line 28j allows the monitoring system 12 to measure a current or voltage associated with the pixel 10 and thereby extract information indicative of a degradation of the pixel 10. For example, the monitoring system 12 can extract, via the monitor line 28j, a current flowing through the driving transistor within the pixel 10 and thereby determine, based on the measured current and based on the voltages applied to the driving transistor during the measurement, a threshold voltage of the driving transistor or a shift thereof.

[0022] The monitoring system 12 can also extract an operating voltage of the light emitting device (e.g., a voltage drop across the light emitting device while the light emitting device is operating to emit light). The monitoring system 12 can then communicate signals 32 to the controller 2 and/or the memory 6 to allow the display system 50 to store the extracted degradation information in the memory 6. During subsequent programming and/or emission operations of the pixel 10, the degradation information is retrieved from the memory 6 by the controller 2 via memory signals 36, and the controller 2 then compensates for the extracted degradation information in subsequent programming and/or emission operations of the pixel 10. For example, once the degradation information is extracted, the programming information conveyed to the pixel 10 via the data line 22j can be appropriately adjusted during a subsequent programming operation of the pixel 10 such that the pixel 10 emits light with a desired amount of luminance that is independent of the degradation of the pixel 10. In an example, an increase in the threshold voltage of the driving transistor within the pixel 10 can be compensated for by appropriately increasing the programming voltage applied to the pixel 10.

[0023] FIG. 2A is a circuit diagram of an exemplary driving circuit for a pixel 110. The driving circuit shown

in FIG. 2A is utilized to calibrate, program and drive the pixel 110 and includes a drive transistor 112 for conveying a driving current through an organic light emitting diode ("OLED") 114. The OLED 114 emits light according to the current passing through the OLED 114, and can be replaced by any current-driven light emitting device. The OLED 114 has an inherent capacitance C_{OLED} . The pixel 110 can be utilized in the display panel 20 of the display system 50 described in connection with FIG. 1.

[0024] The driving circuit for the pixel 110 also includes a storage capacitor 116 and a switching transistor 118. The pixel 110 is coupled to a select line SEL, a voltage supply line Vdd, a data line Vdata, and a monitor line MON. The driving transistor 112 draws a current from the voltage supply line Vdd according to a gate-source voltage (V_{gs}) across the gate and source terminals of the drive transistor 112. For example, in a saturation mode of the drive transistor 112, the current passing through the drive transistor 112 can be given by $I_{\text{ds}} = \beta (V_{\text{gs}} - V_{\text{t}})^2$, where β is a parameter that depends on device characteristics of the drive transistor 112, I_{ds} is the current from the drain terminal to the source terminal of the drive transistor 112, and V_{t} is the threshold voltage of the drive transistor 112.

[0025] In the pixel 110, the storage capacitor 116 is coupled across the gate and source terminals of the drive transistor 112. The storage capacitor 116 has a first terminal, which is referred to for convenience as a gate-side terminal, and a second terminal, which is referred to for convenience as a source-side terminal. The gate-side terminal of the storage capacitor 116 is electrically coupled to the gate terminal of the drive transistor 112. The source-side terminal 116s of the storage capacitor 116 is electrically coupled to the source terminal of the drive transistor 112. Thus, the gate-source voltage V_{gs} of the drive transistor 112 is also the voltage charged on the storage capacitor 116. As will be explained further below, the storage capacitor 116 can thereby maintain a driving voltage across the drive transistor 112 during an emission phase of the pixel 110.

[0026] The drain terminal of the drive transistor 112 is connected to the voltage supply line Vdd, and the source terminal of the drive transistor 112 is connected to (1) the anode terminal of the OLED 114 and (2) a monitor line MON via a read transistor 119. A cathode terminal of the OLED 114 can be connected to ground or can optionally be connected to a second voltage supply line, such as the supply line Vss shown in FIG. 1. Thus, the OLED 114 is connected in series with the current path of the drive transistor 112. The OLED 114 emits light according to the magnitude of the current passing through the OLED 114, once a voltage drop across the anode and cathode terminals of the OLED achieves an operating voltage (V_{OLED}) of the OLED 114. That is, when the difference between the voltage on the anode terminal and the voltage on the cathode terminal is greater than the operating voltage V_{OLED} , the OLED 114 turns on and emits light. When the anode-to-cathode voltage is less

than V_{OLED} , current does not pass through the OLED 114.

[0027] The switching transistor 118 is operated according to the select line SEL (e.g., when the voltage on the select line SEL is at a high level, the switching transistor 118 is turned on, and when the voltage SEL is at a low level, the switching transistor is turned off). When turned on, the switching transistor 118 electrically couples node A (the gate terminal of the driving transistor 112 and the gate-side terminal of the storage capacitor 116) to the data line Vdata.

[0028] The read transistor 119 is operated according to the read line RD (e.g., when the voltage on the read line RD is at a high level, the read transistor 119 is turned on, and when the voltage RD is at a low level, the read transistor 119 is turned off). When turned on, the read transistor 119 electrically couples node B (the source terminal of the driving transistor 112, the source-side terminal of the storage capacitor 116, and the anode of the OLED 114) to the monitor line MON.

[0029] FIG. 2B is a timing diagram of exemplary operation cycles for the pixel 110 shown in FIG. 2A. During a first cycle 150, both the SEL line and the RD line are high, so the corresponding transistors 118 and 119 are turned on. The switching transistor 118 applies a voltage V_{d1} , which is at a level sufficient to turn on the drive transistor 112, from the data line Vdata to node A. The read transistor 119 applies a monitor-line voltage V_b , which is at a level that turns the OLED 114 off, from the monitor line MON to node B. As a result, the gate-source voltage V_{gs} is independent of V_{OLED} ($V_{d1} - V_b - V_{ds3}$, where V_{ds3} is the voltage drop across the read transistor 119). The SEL and RD lines go low at the end of the cycle 150, turning off the transistors 118 and 119.

[0030] During the second cycle 154, the SEL line is low to turn off the switching transistor 118, and the drive transistor 112 is turned on by the charge on the capacitor 116 at node A. The voltage on the read line RD goes high to turn on the read transistor 119 and thereby permit a first sample of the drive transistor current to be taken via the monitor line MON, while the OLED 114 is off. The voltage on the monitor line MON is V_{ref} , which may be at the same level as the voltage V_b in the previous cycle.

[0031] During the third cycle 158, the voltage on the select line SEL is high to turn on the switching transistor 118, and the voltage on the read line RD is low to turn off the read transistor 119. Thus, the gate of the drive transistor 112 is charged to the voltage V_{d2} of the data line Vdata, and the source of the drive transistor 112 is set to V_{OLED} by the OLED 114. Consequently, the gate-source voltage V_{gs} of the drive transistor 112 is a function of V_{OLED} ($V_{gs} = V_{d2} - V_{\text{OLED}}$).

[0032] During the fourth cycle 162, the voltage on the select line SEL is low to turn off the switching transistor, and the drive transistor 112 is turned on by the charge on the capacitor 116 at node A. The voltage on the read line RD is high to turn on the read transistor 119, and a second sample of the current of the drive transistor 112

is taken via the monitor line MON.

[0033] If the first and second samples of the drive current are not the same, the voltage V_{d2} on the Vdata line is adjusted, the programming voltage V_{d2} is changed, and the sampling and adjustment operations are repeated until the second sample of the drive current is the same as the first sample. When the two samples of the drive current are the same, the two gate-source voltages should also be the same, which means that:

$$\begin{aligned} V_{\text{OLED}} &= V_{d2} - V_{gs} \\ &= V_{d2} - (V_{d1} - V_b - V_{ds3}) \\ &= V_{d2} - V_{d1} + V_b + V_{ds3}. \end{aligned}$$

[0034] After some operation time (t), the change in V_{OLED} between time 0 and time t is $\Delta V_{\text{OLED}} = V_{\text{OLED}}(t) - V_{\text{OLED}}(0) = V_{d2}(t) - V_{d2}(0)$. Thus, the difference between the two programming voltages $V_{d2}(t)$ and $V_{d2}(0)$ can be used to extract the OLED voltage.

[0035] FIG. 2C is a modified schematic timing diagram of another set of exemplary operation cycles for the pixel 110 shown in FIG. 2A, for taking only a single reading of the drive current and comparing that value with a known reference value. For example, the reference value can be the desired value of the drive current derived by the controller to compensate for degradation of the drive transistor 112 as it ages. The OLED voltage V_{OLED} can be extracted by measuring the difference between the pixel currents when the pixel is programmed with fixed voltages in both methods (being affected by V_{OLED} and not being affected by V_{OLED}). This difference and the current-voltage characteristics of the pixel can then be used to extract V_{OLED} .

[0036] During the first cycle 200 of the exemplary timing diagram in FIG. 2C, the select line SEL is high to turn on the switching transistor 118, and the read line RD is low to turn off the read transistor 118. The data line Vdata supplies a voltage V_{d2} to node A via the switching transistor 118. During the second cycle 201, SEL is low to turn off the switching transistor 118, and RD is high to turn on the read transistor 119. The monitor line MON supplies a voltage V_{ref} to the node B via the read transistor 118, while a reading of the value of the drive current is taken via the read transistor 119 and the monitor line MON. This read value is compared with the known reference value of the drive current and, if the read value and the reference value of the drive current are different, the cycles 200 and 201 are repeated using an adjusted value of the voltage V_{d2} . This process is repeated until the read value and the reference value of the drive current are substantially the same, and then the adjusted value of V_{d2} can be used to determine V_{OLED} .

[0037] FIG. 3 is a circuit diagram of two of the pixels 110a and 110b like those shown in FIG. 2A but modified to share a common monitor line MON, while still permitting independent measurement of the driving current and

OLED voltage separately for each pixel. The two pixels 110a and 110b are in the same row but in different columns, and the two columns share the same monitor line MON. Only the pixel selected for measurement is programmed with valid voltages, while the other pixel is programmed to turn off the drive transistor 12 during the measurement cycle. Thus, the drive transistor of one pixel will have no effect on the current measurement in the other pixel.

[0038] FIG. 4 illustrates a modified drive system that utilizes a readout circuit 300 that is shared by multiple columns of pixels while still permitting the measurement of the driving current and OLED voltage independently for each of the individual pixels 10. Although only four columns are illustrated in FIG. 4, it will be understood that a typical display contains a much larger number of columns, and they can all use the same readout circuit. Alternatively, multiple readout circuits can be utilized, with each readout circuit still sharing multiple columns, so that the number of readout circuits is significantly less than the number of columns. Only the pixel selected for measurement at any given time is programmed with valid voltages, while all the other pixels sharing the same gate signals are programmed with voltages that cause the respective drive transistors to be off. Consequently, the drive transistors of the other pixels will have no effect on the current measurement being taken of the selected pixel. Also, when the driving current in the selected pixel is used to measure the OLED voltage, the measurement of the OLED voltage is also independent of the drive transistors of the other pixels.

[0039] While particular embodiments and applications of the present invention have been illustrated and described, it is to be understood that the invention is not limited to the precise construction and compositions disclosed herein and that various modifications, changes, and variations can be apparent from the foregoing descriptions without departing from the spirit and scope of the invention as defined in the appended claims.

[0040] A first example is method of determining the current effective voltage V_{OLED} of a light-emitting device in a selected pixel in an array of pixels in a display in which each pixel includes a drive transistor for supplying current to said light-emitting device, said method comprising supplying current to said light-emitting device via said drive transistor in said selected pixel, said current being a function of the current effective voltage V_{OLED} of said light-emitting device, measuring said current, and extracting the value of the current effective voltage V_{OLED} of said light-emitting device from said current measurement.

[0041] A second example is the method of the first example in which said light-emitting devices are OLEDs.

[0042] A third example is the method of the first example in which said current is supplied to said light-emitting device via a drive transistor in each pixel in each pixel.

[0043] A fourth example is the method of the first example in which said current is measured via a read tran-

sistor in each pixel.

[0044] A fifth example is the method of the first example in which said supplying step includes supplying a first programming voltage to said drive transistor in said selected pixel to supply a first current to said light-emitting device in said selected pixel, said measuring step includes supplying a second programming voltage to said drive transistor in said selected pixel to supply a second current to said light-emitting device in said selected pixel, said second current being a function of the current effective voltage V_{OLED} of said light-emitting device, and measuring said second current and comparing said first and second current measurements, and said extracting step includes extracting the value of the current effective voltage V_{OLED} of said light-emitting device from the difference between said first and second programming voltages.

[0045] A sixth example is the method of fifth example which includes adjusting said second programming voltage to make said second current substantially the same as said first current before the extracting step.

[0046] A seventh example is the method of the fifth example in which said first current is supplied at a first time and is independent of the effective voltage V_{OLED} of said light-emitting device, and said second current is supplied at a second time following substantial usage of said display.

[0047] An eighth example is the method of the fifth example in which said first current is independent of, or a function of, the effective voltage V_{OLED} of said light-emitting device.

[0048] A ninth example is a system for determining the current effective voltage V_{OLED} of a pixel in an array of pixels in a display in which each pixel includes a light-emitting device that emits light when supplied with a voltage V_{OLED} , and a drive transistor for supplying current to said light-emitting device, said system comprising a controller adapted to supply current to said light-emitting device via said drive transistor in said selected pixel, said current being a function of the current effective voltage V_{OLED} of said light-emitting device, measure said current, and extract the value of the current effective voltage V_{OLED} of said light-emitting device from said current measurement.

[0049] A tenth example is the system of the ninth example in which each pixel includes a pixel circuit in which said drive transistor drives current through the light-emitting device according to a driving voltage across the drive transistor during an emission cycle, and which includes a storage capacitor coupled across the source and gate of said drive transistor for providing said driving voltage to said drive transistor, a monitor line coupled to a read transistor that controls the coupling of said monitor line to a node that is common to the source of said drive transistor and said light-emitting device, and a data line coupled to a switching transistor that controls the coupling of said data line to the gate of said drive transistor, and wherein said controller is coupled to said data and

monitor lines and to said switching and read transistors, and adapted to turn on said switching and read transistors of a selected pixel while supplying said first node of said selected pixel with a voltage that is sufficient to turn on said light-emitting device of said selected pixel, the current supplied to said light-emitting device being a function of the voltage V_{OLED} of said device, measure the current flowing through said drive transistor and said light-emitting device of the selected pixel, and extract from said current measurement the effective voltage V_{OLED} .

[0050] An eleventh example is the system of the tenth example in which said first node of said selected pixel is supplied with a first voltage that is not sufficient to turn on said light-emitting device of said selected pixel so that the current supplied to said light-emitting device is not a function of the voltage V_{OLED} of said device, prior to supplying said first node of said selected pixel with a second voltage that is sufficient to turn on said light-emitting device of the selected pixel so that the current supplied to said light-emitting device is a function of the voltage V_{OLED} of said device.

[0051] A twelfth example is the system of the eleventh example in which said voltage V_{OLED} is determined from the difference between said first and second voltages.

[0052] A thirteenth example is the system of the tenth example in which said light-emitting device emits light when supplied with a voltage V_{OLED} , said drive transistor drives current through the light-emitting device according to a driving voltage across the drive transistor during an emission cycle, said drive transistor having a gate, a source and a drain and characterized by a threshold voltage, and said controller is coupled to said data and monitor lines and to said switching and read transistors, and adapted to during a first cycle, turn on said switching and read transistors while delivering a voltage V_b to said monitor line and a voltage V_{d1} to said data line, to supply said first node with a voltage that is independent of the voltage across said light-emitting device, during a second cycle, turn on said read transistor and turn off said switching transistor while delivering a voltage V_{ref} to said monitor line, and read a first sample of the drive current at said first node via said read transistor and said monitor line, during a third cycle, turn off said read transistor and turn on said switching transistor while delivering a voltage V_{d2} to said data line, so that the voltage at said second node is a function of V_{OLED} , during a fourth cycle, turn on said read transistor and turn off said switching transistor while delivering a voltage V_{ref} to said monitor line, and read a second sample of the drive current at said first node via said read transistor and said monitor line, and compare said first and second samples and, if said first and second samples are different, repeating said first through fourth cycles using an adjusted value of at least one of said voltages V_{d1} and V_{d2} , until said first and second samples are substantially the same.

[0053] A fourteenth example is the system of the thirteenth example in which said pixels are arranged in rows and columns, and said pixel circuits in a plurality of col-

umns share a common monitor line.

[0054] A fifteenth example is the system of the fourteenth example in which said pixel circuits that share a common monitor line are in adjacent columns.

[0055] A sixteenth example is the system of the fourteenth example in which, during said second and fourth cycles, said controller is adapted to turn off all the drive transistor in all of said pixel circuits sharing a common monitor line, except the pixel circuit in which said drive current is being read.

[0056] A seventeenth example is the system of the sixteenth example in which said controller is adapted to determine the current value of V_{OLED} when it has been determined that said first and second samples are substantially the same.

Claims

1. A method of determining the operating voltage V_{OLED} of a light-emitting device in a selected pixel in an array of pixels in a display, the selected pixel including a drive transistor for supplying current to said light-emitting device, said method comprising:

programming the selected pixel with use of the light-emitting device and with at least one first programming voltage such that a programming of the selected pixel is a function of the operating voltage V_{OLED} of the light-emitting device; measuring a current provided by the drive transistor according to said programming and adjusting the at least one programming voltage to a final at least one programming voltage to make the current measured substantially the same as a reference current; and extracting the value of the operating voltage V_{OLED} of the light-emitting device with use of said final at least one programming voltage.

2. The method of claim 1 wherein programming the selected pixel with use of the light-emitting device comprises allowing a voltage of the light-emitting device to achieve the operating voltage V_{OLED} and affect the programming of the selected pixel.

3. The method of claim 2, wherein said programming of the selected pixel comprises storing voltages with use of a storage capacitor of the selected pixel, and wherein allowing a voltage of the light-emitting device to achieve the operating voltage V_{OLED} and affect the programming of the selected pixel comprises allowing the achieved operating voltage V_{OLED} to affect a voltage of the storage capacitor such that the programming of the selected pixel is a function of the operating voltage V_{OLED} .

4. The method of claim 3, wherein the reference current

corresponds to a desired drive current derived by the controller to compensate for degradation of the drive transistor.

5. The method of claim 3, wherein the reference current corresponds to a previously measured current provided by the drive transistor according to a previous programming of the selected pixel, the previous programming of the pixel independent of the operating voltage V_{OLED} of the light-emitting device. 5
6. The method of claim 6, wherein extracting the value of the operating voltage V_{OLED} comprises determining a difference between a voltage of the at least one programming voltage and a voltage of at least one previous programming voltage of the previous programming. 10
7. A method of determining the operating voltage V_{OLED} of a light-emitting device in a selected pixel in an array of pixels in a display, the selected pixel including a drive transistor for supplying current to said light-emitting device, said method comprising: 15
- programming the selected pixel with use of the light-emitting device and with at least one programming voltage at a first time such that a programming of the selected pixel at the first time is a function of the operating voltage V_{OLED} of the light-emitting device; 20
- programming the selected pixel with at least one programming voltage at a second time different from the first time and wherein one of the first time and the second time is later than the other, such that a programming of the selected pixel at the second time is independent of the operating voltage V_{OLED} of the light-emitting device; 25
- measuring a first current provided by the drive transistor according to said programming at the first time; 30
- measuring a second current provided by the drive transistor according to said programming at the second time; 35
- adjusting the at least one programming voltage of the first or second time, whichever is later, to a final at least one programming voltage to make the first current measured substantially the same as the second current measured; and 40
- extracting the value of the operating voltage V_{OLED} of the light-emitting device with use of said final at least one programming voltage. 45
8. The method of claim 7 wherein programming the selected pixel with use of the light-emitting device comprises allowing a voltage of the light-emitting device to achieve the operating voltage V_{OLED} and affect the programming of the selected pixel. 50

9. The method of claim 8, wherein said programming of the selected pixel comprises storing voltages with use of a storage capacitor of the selected pixel, and wherein allowing a voltage of the light-emitting device to achieve the operating voltage V_{OLED} and affect the programming of the selected pixel comprises allowing the achieved operating voltage V_{OLED} to affect a voltage of the storage capacitor such that the programming of the selected pixel is a function of the operating voltage V_{OLED} . 55
10. The method of claim 9, wherein extracting the value of the operating voltage V_{OLED} comprises determining a difference between a voltage of the at least one programming voltage at the first time and a voltage of the at least one programming voltage at the second time. 60

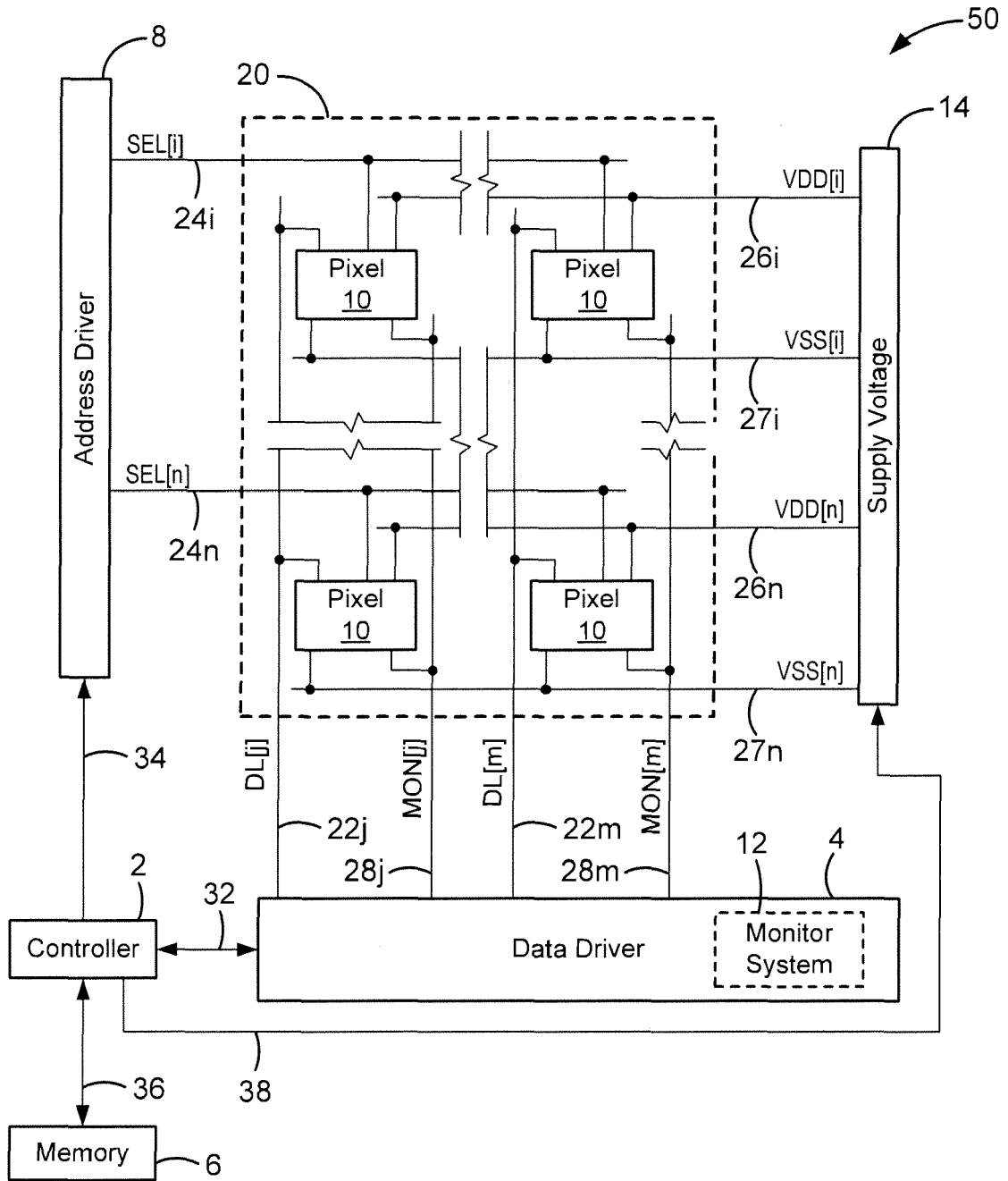


FIG. 1

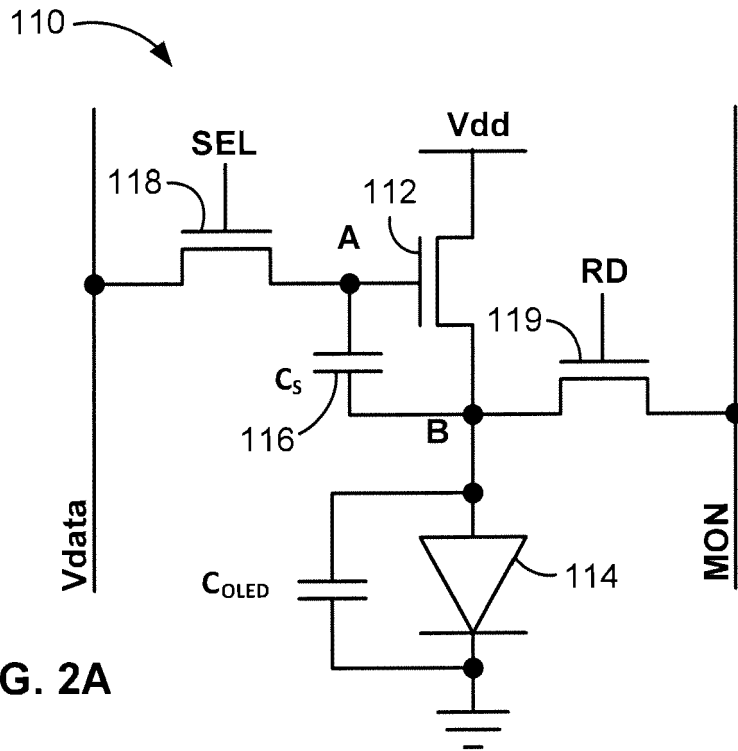


FIG. 2A

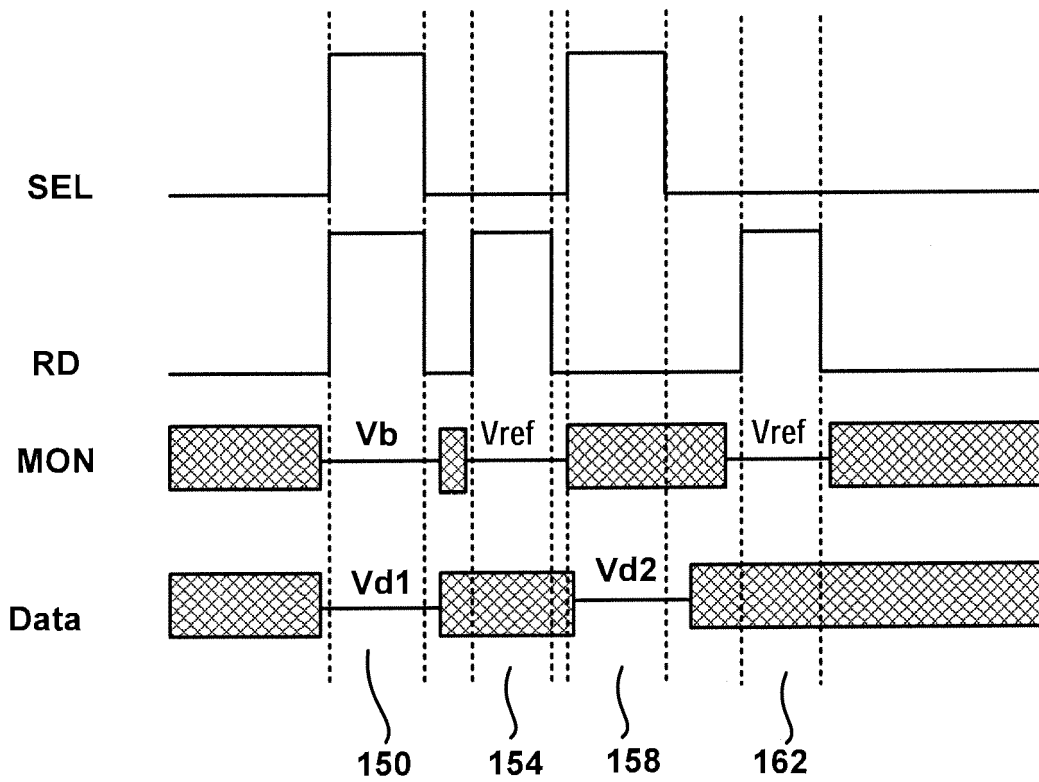


FIG. 2B

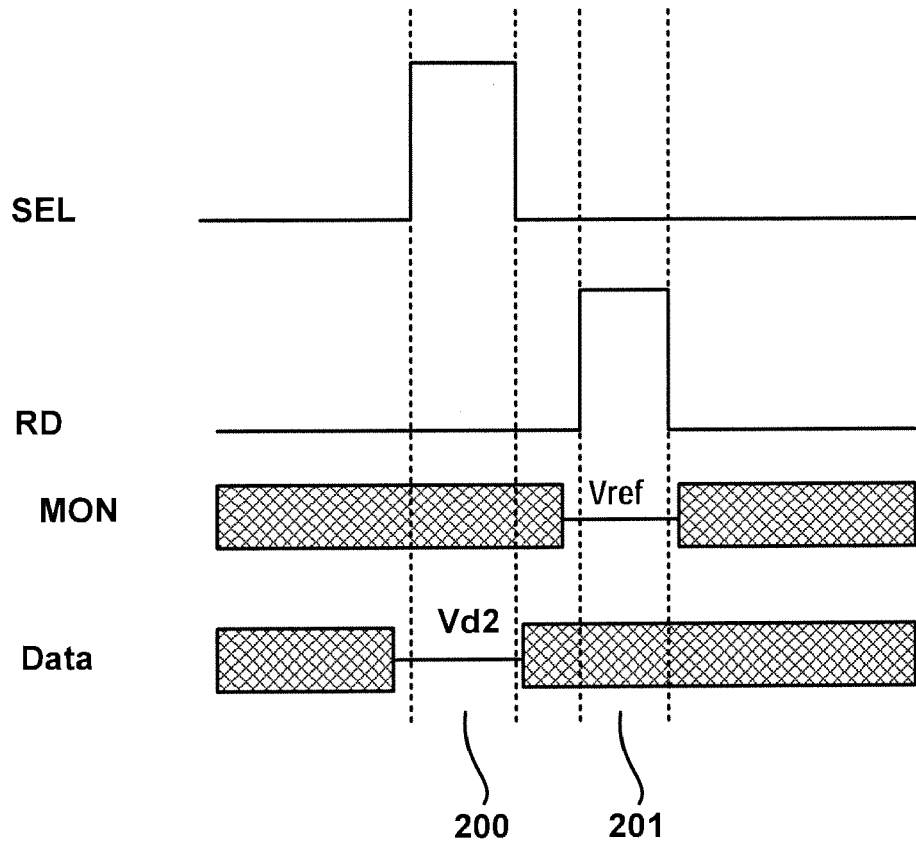


FIG. 2C

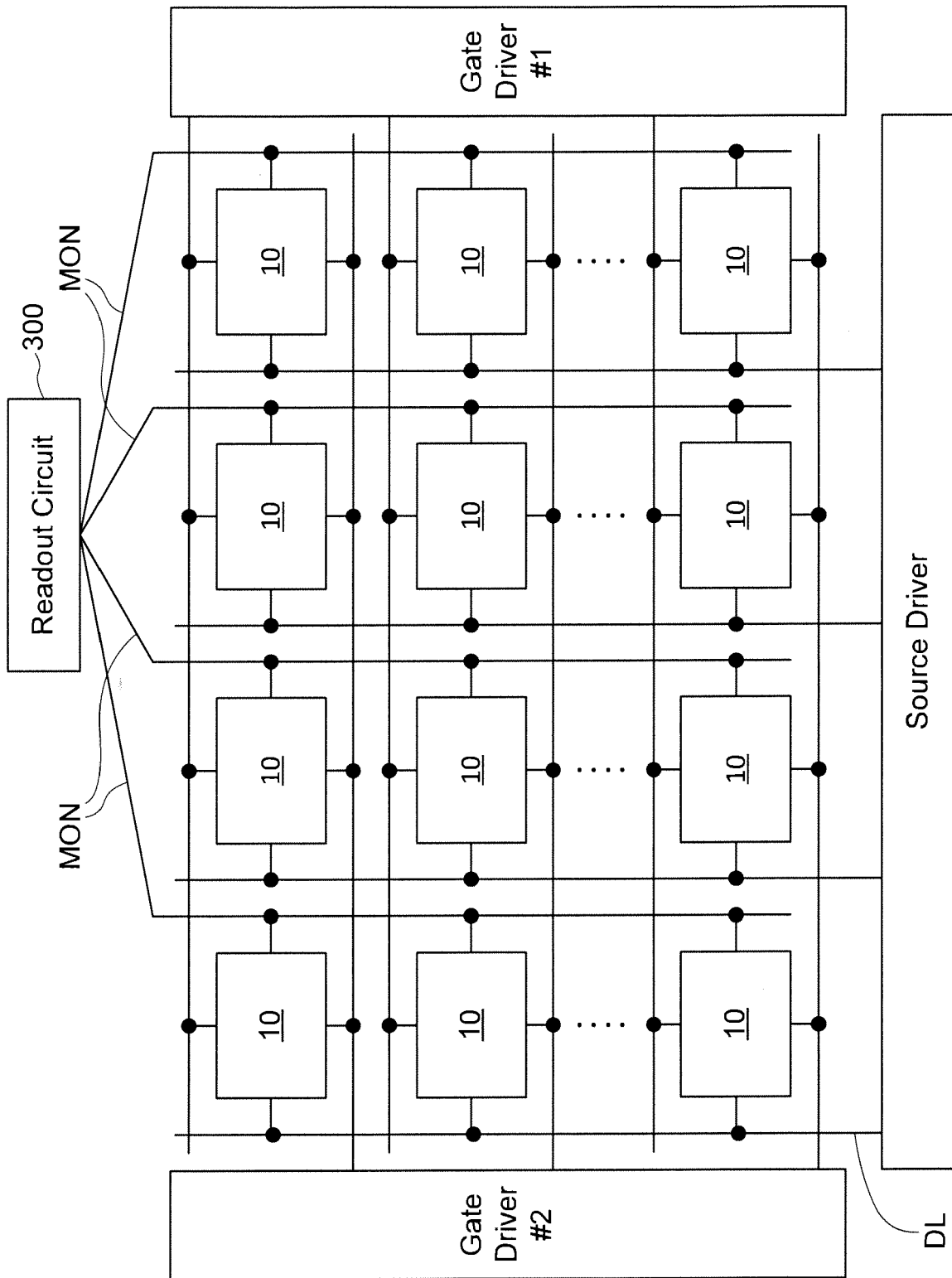


FIG. 4



EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT				
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)	
X	US 2012/299978 A1 (CHAJI GHOLAMREZA [CA]) 29 November 2012 (2012-11-29) * paragraphs [0085], [0086], [0099], [0100]; figures 5A-6D *	1-10	INV. G09G3/32	
X	EP 2 133 860 A1 (SAMSUNG MOBILE DISPLAY CO LTD [KR]) 16 December 2009 (2009-12-16) * paragraphs [0073] - [0082]; figures 3, 7A-7B, 8A-8C *	1-10		
X	US 2009/295423 A1 (LEVEY CHARLES I [US]) 3 December 2009 (2009-12-03) * paragraphs [0042] - [0046]; figures 1-4 *	1-10		
X	US 2011/205250 A1 (YOO MYOUNG-HWAN [KR] ET AL) 25 August 2011 (2011-08-25) * paragraphs [0131] - [0185]; figures 3-9 *	1-10		
X	EP 2 383 720 A2 (IGNIS INNOVATION INC [CA]) 2 November 2011 (2011-11-02) * paragraphs [0106] - [0162]; figures 8, 12, 13, 14 *	1-10		TECHNICAL FIELDS SEARCHED (IPC)
X	US 2010/103159 A1 (LEON FELIPE A [US]) 29 April 2010 (2010-04-29) * paragraphs [0044] - [0049]; figures 3, 5 *	1-10		G09G
X	US 2010/103082 A1 (LEVEY CHARLES I [US] ET AL) 29 April 2010 (2010-04-29) * paragraphs [0042] - [0052]; figures 2, 4 *	1-10		
X	US 2008/315788 A1 (LEVEY CHARLES I [US] ET AL) 25 December 2008 (2008-12-25) * paragraphs [0039] - [0047]; figures 2, 4 *	1-10		
The present search report has been drawn up for all claims				
Place of search The Hague		Date of completion of the search 11 March 2019	Examiner Bellatalla, Filippo	
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document				

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ON EUROPEAN PATENT APPLICATION NO.

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11-03-2019

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2012299978 A1	29-11-2012	CN 103562989 A	05-02-2014
		CN 106910464 A	30-06-2017
		EP 2715710 A2	09-04-2014
		EP 3293726 A1	14-03-2018
		JP 2014517940 A	24-07-2014
		US 2012299978 A1	29-11-2012
		US 2017358251 A1	14-12-2017
		US 2018240386 A1	23-08-2018
		WO 2012164475 A2	06-12-2012
EP 2133860 A1	16-12-2009	CN 101615379 A	30-12-2009
		EP 2133860 A1	16-12-2009
		JP 2009301037 A	24-12-2009
		KR 20090129336 A	16-12-2009
		US 2009309818 A1	17-12-2009
US 2009295423 A1	03-12-2009	NONE	
US 2011205250 A1	25-08-2011	CN 102163402 A	24-08-2011
		JP 5761776 B2	12-08-2015
		JP 2011175226 A	08-09-2011
		KR 20110096877 A	31-08-2011
		TW 201133449 A	01-10-2011
		US 2011205250 A1	25-08-2011
EP 2383720 A2	02-11-2011	EP 1836697 A1	26-09-2007
		EP 2383720 A2	02-11-2011
		EP 2688058 A2	22-01-2014
		JP 5128287 B2	23-01-2013
		JP 5822759 B2	24-11-2015
		JP 6086893 B2	01-03-2017
		JP 2008523448 A	03-07-2008
		JP 2012150490 A	09-08-2012
		JP 2015084105 A	30-04-2015
		KR 20070101275 A	16-10-2007
		TW 1402790 B	21-07-2013
		US 2006158402 A1	20-07-2006
		US 2010033469 A1	11-02-2010
		US 2013027381 A1	31-01-2013
		US 2014159996 A1	12-06-2014
		US 2014168203 A1	19-06-2014
US 2015192621 A1	09-07-2015		
WO 2006063448 A1	22-06-2006		
US 2010103159 A1	29-04-2010	CN 102197420 A	21-09-2011
		EP 2351010 A1	03-08-2011

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55

ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

EP 18 20 5133

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11-03-2019

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		JP 5347029 B2	20-11-2013
		JP 2012507746 A	29-03-2012
		KR 20110074999 A	05-07-2011
		KR 20130075789 A	05-07-2013
		TW 201027492 A	16-07-2010
		US 2010103159 A1	29-04-2010
		WO 2010053514 A1	14-05-2010

US 2010103082 A1	29-04-2010	CN 102203846 A	28-09-2011
		EP 2351009 A1	03-08-2011
		JP 2012507041 A	22-03-2012
		KR 20110074986 A	05-07-2011
		TW 201216245 A	16-04-2012
		US 2010103082 A1	29-04-2010
		WO 2010047791 A1	29-04-2010

US 2008315788 A1	25-12-2008	CN 101689349 A	31-03-2010
		EP 2160728 A1	10-03-2010
		JP 5209709 B2	12-06-2013
		JP 2010530993 A	16-09-2010
		KR 20100021482 A	24-02-2010
		TW 200912852 A	16-03-2009
		US 2008315788 A1	25-12-2008
		WO 2009002406 A1	31-12-2008

EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	用于amoled显示器的像素电路		
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申请号	EP2018205133	申请日	2014-02-28
[标]申请(专利权)人(译)	伊格尼斯创新公司		
申请(专利权)人(译)	IGNIS创新INC.		
当前申请(专利权)人(译)	IGNIS创新INC.		
[标]发明人	AZIZI YASER CHAJI GHOLAMREZA		
发明人	AZIZI, YASER CHAJI, GHOLAMREZA		
IPC分类号	G09G3/32		
CPC分类号	G09G3/3233 G09G2300/0842 G09G2320/0295 G09G2320/045 G09G2320/0693 G09G3/3258 G09G2300/0819 G09G2320/048 G09G2320/10 H05B45/14		
优先权	13/789978 2013-03-08 US		
外部链接	Espacenet		

摘要(译)

从像素被编程时产生的像素中提取所选像素的OLED电压，使得像素电流是OLED电压的函数。用于提取OLED电压的一种方法是首先以电流不是OLED电压的函数的方式对像素进行编程，然后以电流是OLED电压的函数的方式对像素进行编程。在后一阶段期间，改变编程电压，使得像素电流与像素被编程的像素电流相同，使得电流不是OLED电压的函数。然后使用两个编程电压的差异来提取OLED电压。

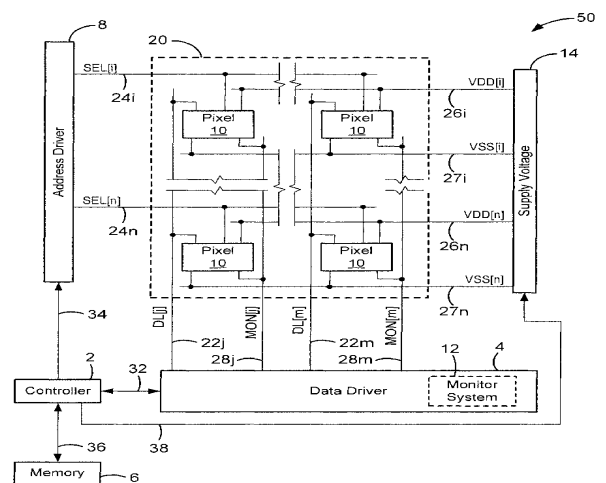


FIG. 1