

(19)



(11)

EP 2 333 758 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:

15.06.2011 Bulletin 2011/24

(51) Int Cl.:

G09G 3/32^(2006.01)

(21) Application number: **10175789.6**

(22) Date of filing: **08.09.2010**

(84) Designated Contracting States:

**AL AT BE BG CH CY CZ DE DK EE ES FI FR GB
GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO
PL PT RO SE SI SK SM TR**

Designated Extension States:

BA ME RS

(30) Priority: **01.12.2009 KR 20090117879**

(71) Applicant: **Samsung Mobile Display Co., Ltd.
Yongin-city, Gyunggi-do 446-711 (KR)**

(72) Inventors:

- **Kim, Hyung-Soo
446-711, Gyunggi-Do (KR)**
- **Ahn, Soon-Sung
446-711, Gyunggi-Do (KR)**

(74) Representative: **Walaski, Jan Filip et al**

**Venner Shipley LLP
20 Little Britain
London
EC1A 7DH (GB)**

(54) **Organic light emitting display**

(57) An organic light emitting display includes an image display unit having a plurality of pixels, and first and second pixel power lines that receive pixel-driving voltages from first and second power supplies, respectively. The second pixel power lines are interleaved with the first pixel power lines. Adjacent pixels of the same colour

from among the plurality of pixels provided in the image display unit are coupled to pixel power lines corresponding to different power supplies. This is to solve or mitigate the problem of non-uniform brightness of the entire image display unit due to voltage drops in the pixel power lines.

EP 2 333 758 A2

Description

[0001] The present invention relates to a flat panel display, and more particularly, to an organic light emitting display.

[0002] Recently, various flat panel displays (FPDs), with reduced weight and volume compared to that of cathode ray tubes (CRTs), have been developed. The FPDs include liquid crystal displays (LCDs), field emission displays (FEDs), plasma display panels (PDPs), and organic light emitting displays.

[0003] Among the FPDs, the organic light emitting display has self-emission elements that emit light from phosphors by re-combination of electrons and holes. The organic light emitting display is divided into two categories, a passive matrix organic light emitting display and an active matrix organic light emitting display.

[0004] In the active matrix organic light emitting display, a plurality of pixels are arranged in a matrix. Emission of the pixels is controlled using switch elements such as thin film transistors (TFTs) in the pixels. Display is performed through scan lines that select the pixels, data lines for controlling the emission of the pixels, and pixel power lines VDD for supplying voltage (or current) to the pixels.

[0005] Ideally, the pixel power lines VDD supply uniform voltage to the plurality of pixels coupled to them. Voltage values applied through the pixel power lines should be equal to each other in terms of the coupled pixels. However, this can be difficult to achieve due to an IR drop (e.g., a voltage drop) generated by the pixel power lines. That is, the voltages supplied to the pixels are reduced due to the IR drop as the distance that the pixels are from an external power supply increases. This is more clearly described with reference to FIG. 1.

[0006] FIG. 1 is a block diagram illustrating the structure of a conventional organic light emitting display.

[0007] Referring to FIG. 1, the organic light emitting display includes an image display unit 10 for displaying an image, a data driver 20 for transmitting data signals, and a scan driver 30 for transmitting scan signals. In addition, the image display unit 10 includes a plurality of pixels 11 including light emitting elements and pixel circuits, a plurality of scan lines S1, S2, ..., Sn-1, and Sn arranged in rows, a plurality of data lines D1, D2, ..., Dm-1, and Dm arranged in columns, a plurality of pixel power lines VDD for supplying power to the plurality of pixels, and a power supply 12 for providing voltages (e.g., predetermined voltages) to the pixel power lines VDD.

[0008] The image display unit 10 applies the scan signals transmitted from the scan lines S1, S2, ..., Sn-1, and Sn and the data signals transmitted from the data lines D1, D2, ..., Dm-1, and Dm to the pixels. The pixel circuits included in the pixels generate currents corresponding to the data signals and transmit the generated currents to the light emitting elements to display an image.

[0009] In this case, a positive pixel-driving voltage from the power supply 12 (in the lower portion of the display,

as illustrated in FIG. 1) having a uniform level is applied to the pixels 11 through the pixel power lines VDD. As illustrated in FIG. 1, some of the pixels 10 are further from the power supply 12 (for example, pixels in the upper portion of the display) for applying pixel-driving voltages than others (for instance, pixels in the lower portion). Therefore, due to a non-uniformity of line resistance in accordance with lengths of the pixel power lines VDD commonly coupled to the power supply 12, the magnitudes of the IR drops of the pixel-driving voltages supplied to the pixels 11 are different from each other.

[0010] That is, the magnitude of the IR drop of the pixel power lines VDD is smaller when the pixel is closer to the power supply 12, while the magnitude of the voltage drop of the pixel power line VDD increases the further the pixel is from the power supply 12.

[0011] Therefore, the conventional organic light emitting display has a shortcoming in that the amount of current delivered to a pixel 11 for the same data signal varies in accordance with the position of the pixel 11 due to the non-uniformity of the IR drop of the pixel power lines VDD. Consequently, emission brightness becomes non-uniform. Such a problem becomes serious as the size of a panel increases.

[0012] Accordingly, embodiments of the present invention provide an organic light emitting display that includes first pixel power lines that receive pixel-driving voltages from a first power supply and second pixel power lines arranged between the first pixel power lines to receive pixel-driving voltages from a second power supply. Adjacent pixels of the same colour among the plurality of pixels provided in an image display unit are coupled to pixel power lines that are coupled to different power supplies. This prevents or reduces the problem of non-uniform brightness of the entire image display unit due to the IR drop of pixel power lines.

[0013] According to an exemplary embodiment of the present invention, an organic light emitting display is provided. The organic light emitting display includes an image display unit, a plurality of pixel power lines, and first and second power supplies. The image display unit includes a plurality of pixels. The plurality of pixel power lines includes first pixel power lines and second pixel power lines for transmitting pixel-driving voltages to the plurality of pixels. The first and second power supplies are for providing the pixel-driving voltages to the first pixel power lines and the second pixel power lines, respectively. Adjacent pixels of the same colour from among the plurality of pixels included in the image display unit are coupled to different ones of the power supplies via respective ones of the pixel power lines.

[0014] The first pixel power lines and the second pixel power lines may extend in opposite directions.

[0015] The first pixel power lines may extend from a first side of the image display unit to a second side of the image display unit that is opposite the first side of the image display unit. The second pixel power lines may extend from the second side of the image display unit to

the first side of the image display unit and are interleaved with the first pixel power lines.

[0016] The first pixel power lines may correspond to odd columns of the pixels of the image display unit. The second pixel power lines may correspond to even columns of the pixels of the image display unit.

[0017] Pixels in an odd row from among the plurality of pixels may be coupled to respective ones of the plurality of pixel power lines on a first side of the pixels in the odd row. Pixels in an even row from among the plurality of pixels may be coupled to respective ones of the plurality of pixel power lines on a second side of the pixels in the even row. The second side of the pixels is opposite the first side of the pixels.

[0018] The pixels may be arranged in unit pixels, each including adjacent red (R), green (G), and blue (B) pixels of the plurality of pixels, such that the pixels of each of the unit pixels are coupled to adjacent ones of the plurality of pixel power lines coupled to the same one of the power supplies, and that two of the pixels of each of the unit pixels are coupled to the same one of the plurality of pixel power lines.

[0019] The unit pixels may be arranged in rows, and the red pixel of each of the unit pixels in an odd row among the plurality of rows is coupled to a respective one of the plurality of pixel power lines, and the green and blue pixels of each of the unit pixels in the odd row are coupled to a respective another one of the plurality of pixel power lines.

[0020] The red and green pixels of each of the unit pixels in an even row among the plurality of rows may be coupled to a respective one of the plurality of pixel power lines, and the blue pixel of each of the unit pixels in the even row is coupled to a respective another one of the plurality of pixel power lines.

[0021] The pixel-driving voltages provided by the first and second power supplies may be positive voltages of the same level.

[0022] The pixels may be arranged in rows and columns, and the adjacent pixels of the same colour include two pixels of the same colour in the same row or column from among the plurality of pixels with no intervening pixels of the same colour.

[0023] The colours may include red (R), green (G), and blue (B), and each of the pixels in the same column are of the same colour.

[0024] The organic light emitting display may further include a plurality of first and second auxiliary pixel power lines crossing the first and second pixel power lines.

[0025] The first auxiliary pixel power lines may be coupled to the first pixel power lines at crossing points of the first auxiliary pixel power lines and the first pixel power lines, the first auxiliary pixel power lines and the first pixel power lines being configured in a mesh type arrangement. The second auxiliary pixel power lines may be interleaved with the first auxiliary pixel power lines and are coupled to the second pixel power lines at crossing points of the second auxiliary pixel power lines and the second

pixel power lines, the second auxiliary pixel power lines and the second pixel power lines being configured in a mesh type arrangement.

[0026] The first and second auxiliary pixel power lines may include a metal material having a low resistivity and may be located in different layers from the first and second pixel power lines.

[0027] Contact holes may be located at crossing points of the first auxiliary pixel power lines and the first pixel power lines and at crossing points of the second auxiliary pixel power lines and the second pixel power lines so that the first and second auxiliary pixel power lines are electrically coupled to the first and second pixel power lines, respectively.

[0028] As described above, according to aspects of the present invention, the total distance of respective pixel power lines coupled to adjacent pixels of the same colour is made uniform. This helps prevent or reduce the problem of the brightness of the entire image display unit becoming non-uniform due to the IR drop of the pixel power lines.

[0029] The accompanying drawings, together with the specification, illustrate exemplary embodiments of the present invention and, together with the description, serve to explain the principles of aspects of the present invention.

FIG. 1 is a block diagram illustrating the structure of a conventional organic light emitting display;

FIG. 2 is a block diagram illustrating the structure of an organic light emitting display according to an embodiment of the present invention;

FIG. 3 is a circuit diagram illustrating the structure of an embodiment of the pixel of FIG. 2;

FIGs. 4A and 4B are views illustrating a coupling relationship between first and second pixel power lines and the pixels according to the embodiment illustrated in FIG. 2 in detail;

FIG. 5 is a block diagram illustrating the structure of an organic light emitting display according to another embodiment of the present invention; and

FIG. 6 is a view illustrating a coupling relationship among the first and second pixel power lines, first and second auxiliary pixel power lines, and the pixels according to the embodiment of FIG. 5.

[0030] In the following detailed description, only certain exemplary embodiments of the present invention have been shown and described, simply by way of illustration. As those skilled in the art would realize, the described embodiments may be modified in various different ways, all without departing from the spirit or scope of the present invention. Accordingly, the drawings and de-

scription are to be regarded as illustrative in nature and not restrictive. In addition, when an element is referred to as being "on" another element, it can be directly on another element or be indirectly on another element with one or more intervening elements interposed therebetween. Also, when an element is referred to as being "coupled to" another element, it can be directly connected to another element or be indirectly connected to another element with one or more intervening elements interposed therebetween. Hereinafter, like reference numerals refer to like elements.

[0031] Hereinafter, the embodiments of the present invention will be described in detail with reference to the accompanying drawings.

[0032] FIG. 2 is a block diagram illustrating the structure of an organic light emitting display according to an embodiment of the present invention.

[0033] Referring to FIG. 2, the organic light emitting display includes an image display unit 100 for displaying an image, a data driver 200 for transmitting data signals, and a scan driver 300 for transmitting scan signals.

[0034] In addition, the image display unit 100 includes a plurality of pixels 110 including light emitting elements and pixel circuits; a plurality of scan lines S1, S2, ..., Sn-1, and Sn arranged in rows; a plurality of data lines D1, D2, ..., Dm-1, and Dm arranged in columns; first and second pixel power lines VDD1 and VDD2 for transmitting pixel-driving voltages to the plurality of pixels and extending from the upper and lower portions of the display, respectively; and first and second power supplies 122 and 124 for providing the pixel-driving voltages to the first and second pixel power lines VDD1 and VDD2, respectively.

[0035] In particular, according to the embodiment shown in FIG. 2, pixel power lines for transmitting the pixel-driving voltages to the pixels are divided into the first pixel power lines VDD1 and the second pixel power lines VDD2 arranged to extend (deliver power) in opposite directions toward the opposite side of the image display unit 100.

[0036] That is, as illustrated in FIG. 2, the first pixel power lines VDD1 extend from the upper side of the image display unit 100 to the lower side of the image display unit 100 while the second pixel power lines VDD2 extend from the lower side of the image display unit 100 to the upper side of the image display unit 100. The first pixel power lines VDD1 and the second pixel power lines VDD2 alternate, such that, except at the edges, each first power line VDD1 is between a pair of second power lines VDD2 and each second power line VDD2 is between a pair of first power lines VDD1. That is, the first and second power pixel lines VDD1 and VDD2 are interleaved.

[0037] In addition, the first pixel power lines VDD1 receive the pixel-driving voltages from the first power supply 122 to transmit the received pixel-driving voltages to the pixels 110 provided in the image display unit 100 while the second pixel power lines VDD2 receive the pixel-driving voltages from the second power supply 124 to transmit the received pixel-driving voltages to the pixels

110 included in the image display unit 100.

[0038] Thus, the magnitude of the IR drop of the first pixel power lines VDD1 increases toward the lower region of the image display unit 100, which is further from the first power supply 122 than the upper region of the image display unit 100. On the contrary, the magnitude of the IR drop of the second pixel power VDD2 increases toward the upper region of the image display unit 100, which is further from the second power supply 124 than the lower region of the image display unit 100.

[0039] Therefore, it is possible to prevent or reduce the variation in brightness of the image display unit that takes place due to a difference in the IR drops of the pixel power lines when the pixel-driving voltages are supplied to the pixels through pixel power lines extending only in the same direction.

[0040] The pixel-driving voltages provided by the first and second power supplies 122 and 124 are positive voltages of the same level so that the first and second power supplies 122 and 124 may be realized by one device.

[0041] In addition, each of the pixels 110 included in the image display unit 100 is coupled to either a first pixel power line VDD1 or a second pixel power line VDD2. According to some embodiments of the present invention, adjacent pixels of the same colour (for example, in the same column and next to each other, or in the same row with only intervening pixels of different colours between them) among the plurality of pixels 110 included in the image display unit 100 are coupled to different pixel power lines VDD1 and VDD2.

[0042] That is, continuing to refer to FIG. 2, in the case where one each of red (R), green (G), and blue (B) pixels realize one unit pixel, when it is assumed that a first red pixel 110a included in a first unit pixel is coupled to the first pixel power line VDD1, second red pixels 110b and 110c included in second unit pixels adjacent to the top, bottom, left, and right of the first unit pixel are not coupled to the first pixel power lines VDD1 but rather to the second pixel power lines VDD2.

[0043] Through such coupling, when the magnitude of the IR drop of the first pixel power line VDD1 coupled to the first red pixel 110a is small, the magnitude of the IR drop of the second pixel power lines VDD2 coupled to the second red pixels 110b and 110c is relatively large. As a result, since the distance from the pixel power lines to adjacent pixels of the same colour, that is, the magnitude of line resistance, is relatively uniform, it is possible to prevent or reduce the brightness of the image display unit from being non-uniform due to the IR drop of the pixel power lines.

[0044] FIG. 3 is a circuit diagram illustrating the structure of an embodiment of the pixel of FIG. 2.

[0045] FIG. 3 is one embodiment of the structure of the pixel according to the embodiment of FIG. 2, but the structure of the pixel is not limited to the embodiment of FIG. 3.

[0046] Referring to FIG. 3, the pixel includes a pixel

circuit and a light emitting element. The pixel circuit includes a first transistor M1, a second transistor M2, and a capacitor C1. The first transistor M1 and the second transistor M2 include sources, drains, and gates. The capacitor C1 includes a first electrode and a second electrode.

[0047] The source of the first transistor M1 is coupled to the first pixel power line VDD1 or the second pixel power line VDD2. The drain of the first transistor M1 is coupled to an anode electrode of an organic light emitting diode (OLED). The gate of the first transistor M1 is coupled to a first node N1. In addition, the first node N1 is coupled to the drain of the second transistor M2. The first transistor M1 supplies the current corresponding to a data signal to the OLED. A base power VSS is coupled to a cathode electrode of the OLED.

[0048] In addition, the source of the second transistor M2 is coupled to a data line D, the drain of the second transistor M2 is coupled to the first node N1, and the gate of the second transistor M2 is coupled to a scan line S. In accordance with the scan signal applied to the gate, the data signal is transmitted to the first node N1.

[0049] The first electrode of the capacitor C1 is coupled to the first power supply line VDD1 or the second power supply line VDD2 and the second electrode is coupled to the first node N1. Therefore, the capacitor C1 charges a charge in accordance with the data signal applied to the pixel and applies a gate signal of the first transistor M1 in a frame in accordance with the charge so that the operation of the first transistor M1 is maintained in the frame.

[0050] FIGs. 4A and 4B are views illustrating a coupling relationship between first and second pixel power lines and the pixels according to the embodiment illustrated in FIG. 2 in detail.

[0051] In FIGs. 4A and 4B, for ease of illustration, the arrangement of scan lines and data lines is omitted.

[0052] Referring to FIGs. 4A and 4B, the first pixel power lines VDD1 positioned on the upper side of the image display unit 100 extend downward and the second pixel power lines VDD2 positioned on the lower side of the image display unit 100 extend upward and interleave the first pixel power lines VDD1. That is, the first pixel power lines VDD1 are arranged to correspond to odd columns of the image display unit 100 and the second pixel power lines VDD2 are arranged to correspond to even columns of the image display unit 100.

[0053] According to the embodiment of FIG. 4A, for the plurality of pixels 110 included in the image display unit 100, the pixels arranged in an odd row are coupled to the pixel power lines arranged on a first side, that is, on the left side of the respective pixels while the pixels arranged in an even row are coupled to the pixel power lines arranged on a second side, that is, on the right side of the respective pixels. For example, in one embodiment, adjacent sets of one each of red (R), green (G), and blue (B) pixels 110 realize one unit pixel (for example, unit pixels 400, 410, and 420). Thus, in the embodiment

of FIG. 4A, no two pixels in the same unit pixel share the same pixel power line. In addition, pixel power lines from both the first pixel power lines VDD1 and the second pixel power lines VDD2 supply power to the three pixels of each of the unit pixels.

[0054] By comparison, according to the embodiment of FIG. 4B, two pixels among the three pixels that constitute each unit pixel are arranged to share an adjacent pixel power line. In addition, pixel power lines from only one of the first pixel power lines VDD1 or the second pixel power lines VDD2 supply power to the three pixels of each of the unit pixels.

[0055] That is, referring to FIG. 4B, the odd unit pixels (for example, unit pixel 400) arranged in the odd rows are coupled to the first pixel power lines VDD1. For instance, if the red pixel of a unit pixel is coupled to the i th (i is an integer, $i \leq m/2 - 1$) first pixel power line VDD1, then the green and blue pixels of the same unit pixel are coupled to share the $(i+1)$ th first pixel power line VDD1. In addition, the even unit pixels (for example, unit pixel 410) arranged in the odd rows are coupled to the second pixel power lines VDD2. For instance, if the red pixel of a unit pixel is coupled to the i th second pixel power line VDD2, then the green and blue pixels of the same unit pixel are coupled to share the $(i+1)$ th second pixel power line VDD2.

[0056] Furthermore, the odd unit pixels (for example, unit pixel 420) arranged in the even rows are coupled to the second pixel power lines VDD2. For instance, if the red and green pixels of a unit pixel are coupled to share the j th (j is an integer, $j \leq m/2 - 1$) second pixel power line VDD2, then the blue pixel of the same unit pixel is coupled to the $(j+1)$ th second power line VDD2. In addition, the even unit pixels arranged in the even rows are coupled to the first pixel power lines VDD1 in a similar fashion.

[0057] As described in the above embodiments, the pixels included in the image display unit 100 are coupled to the first pixel power lines VDD1 (supplied by the first power supply 122) or the second pixel power lines VDD2 (supplied by the second power supply 124) so that the adjacent pixels of the same colour among the plurality of pixels 110 included in the image display unit 100 are coupled to pixel power lines that are coupled to different power supplies.

[0058] Referring to FIGs. 4A and 4B, when a first unit pixel 400 positioned on the uppermost side and on the right side of the image display unit is taken as an example, the first red pixel a included in the first unit pixel 400 is coupled to a first pixel power line VDD1, the second red pixel b included in a second unit pixel 410 adjacent to the first unit pixel 400 in a row direction is coupled to a second pixel power line VDD2, and a third red pixel c included in a third unit pixel 420 adjacent to the first unit pixel 400 in a column direction is coupled to a second pixel power line VDD2 (different from the second pixel power line VDD2 coupled to the second red pixel b).

[0059] In this case, the magnitude of the IR drop of the first pixel power line VDD1 coupled to the first red pixel

a is small and the magnitudes of the IR drops of the second pixel power lines VDD2 coupled to the second red pixel b and the third red pixel c are large. As a result, since the total distance from the power supplies via the pixel power lines to the adjacent pixels of the same colour, that is, the magnitude of the line resistance, is relatively uniform, it is possible to prevent or reduce the non-uniform brightness of the entire image display unit due to the IR drop of the pixel power lines.

[0060] FIG. 5 is a block diagram illustrating the structure of an organic light emitting display according to another embodiment of the present invention.

[0061] According to the embodiment of FIG. 5, in comparison with the embodiment of FIG. 2, in order to reduce or prevent the apparent brightness non-uniformity caused by the IR drop of the pixel power lines with respect to the first and second pixel power lines VDD1 and VDD2 arranged in a first direction, first and second auxiliary pixel power lines VDD1a and VDD2a arranged in a second direction crossing the first direction (for example, perpendicular to the first direction) are additionally formed or provided. Since the other elements and coupling relationships are the same as the embodiment of FIG. 2, like reference numerals refer to like elements and description of the elements will be omitted.

[0062] That is, the first auxiliary pixel power lines VDD1w are arranged in the second direction to be coupled to the first pixel power lines VDD1 in a mesh type arrangement. Likewise, the second auxiliary pixel power lines VDD2a are arranged between the first auxiliary pixel power lines VDD1a to be coupled to the second pixel power lines VDD2 in a similar mesh type arrangement.

[0063] At this time, the first and second auxiliary pixel power lines VDD1a and VDD2a may be made of a metal material having a low resistivity value. The first and second auxiliary pixel power lines VDD1a and VDD2a are electrically coupled (at crossing points) to the first and second pixel power lines VDD1 and VDD2, respectively, to reduce the line resistance of the first and second pixel power lines VDD1 and VDD2 and to reduce or prevent the apparent brightness non-uniformity due to the IR drop caused by the first and second pixel power lines VDD1 and VDD2.

[0064] To reduce or prevent problems like short circuits, the first and second auxiliary pixel power lines VDD1a and VDD2a are formed or located in different layers from the first and second pixel power lines VDD1 and VDD2, respectively. Thus, contact holes, for example, are formed at the intersections (crossing regions) between the first auxiliary pixel power lines VDD1a and the first pixel power lines VDD1 and at the intersections between the second auxiliary pixel power lines VDD2a and the second pixel power lines VDD2 so that the first and second auxiliary pixel power lines VDD1a and VDD2a are electrically coupled to the first and second pixel power lines VDD1 and VDD2, respectively.

[0065] FIG. 6 is a view illustrating a coupling relationship among the first and second pixel power lines, first

and second auxiliary pixel power lines, and the pixels according to the embodiment of FIG. 5.

[0066] In FIG. 6, for simplicity of illustration, the arrangement of the scan lines and the data lines is omitted. It should be noted that the first and second auxiliary pixel power lines VDD1a and VDD2a in FIG. 6 may be added to the embodiment of FIG. 4A and may also be applied to the embodiment of FIG. 4B.

[0067] Referring to FIG. 6, the first pixel power lines VDD1 positioned on the upper side of the image display unit 100 extend from the upper side to the lower side and the second pixel power lines VDD2 positioned on the lower side of the image display unit 100 extend from the lower side to the upper side and are arranged to be interleaved with the first pixel power lines VDD1. That is, the first pixel power lines VDD1 are arranged to correspond to the odd columns of the image display unit 100 and the second pixel power lines VDD2 are arranged to correspond to the even columns of the image display unit 100.

[0068] In addition, the first auxiliary pixel power lines VDD1a are arranged in a row direction to be coupled to the first pixel power lines VDD1 in a mesh type arrangement. The second auxiliary pixel power lines VDD2a are arranged between the first auxiliary pixel power lines VDD1a to be coupled to the second pixel power lines VDD2 in a similar mesh type arrangement.

[0069] That is, the first auxiliary pixel power lines VDD1a are arranged to correspond to the odd rows of the image display unit to be coupled to the first pixel power lines VDD1 to form a mesh or grid. In addition, the second auxiliary pixel power lines VDD2a are arranged to correspond to the even rows of the image display unit to be coupled to the second pixel power lines VDD2 to form a similar mesh or grid.

[0070] Here, since the first and second auxiliary pixel power lines VDD1a and VDD2a are formed in different layers from the first and second pixel power lines VDD1 and VDD2, respectively, contact holes are formed at the intersections between the first auxiliary pixel power lines VDD1a and the first pixel power lines VDD1 and at the intersections between the second auxiliary pixel power lines VDD2a and the second pixel power lines VDD2 as illustrated in FIG. 6 so that the first and second auxiliary pixel power lines VDD1a and VDD2a are electrically coupled to the first and second pixel power lines VDD1 and VDD2, respectively.

[0071] In addition, according to the embodiment of FIG. 6, like in the embodiment of FIG. 4A, for plurality of pixels 110 included in the image display unit 100, the pixels arranged in the odd rows are coupled to the pixel power lines arranged on a first side, that is, on the left side of the respective pixels and the pixels arranged in the even rows are coupled to the pixel power lines arranged on a second side, that is, on the right side of the respective pixels.

[0072] According to embodiments of the present invention, when the pixels 110 included in the image display

play unit 100 are coupled to the first pixel power lines VDD1 supplied by the first power supply 122 or the second pixel power lines VDD2 supplied by the second power supply 124, the adjacent pixels of the same colour among the plurality of pixels 110 included in the image display unit 100 are coupled to pixel power lines that are coupled to different power supplies.

[0073] While the present invention has been described in connection with certain exemplary embodiments, it is to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the scope of the appended claims.

Claims

1. An organic light emitting display comprising:

an image display unit comprising a plurality of pixels;
a plurality of pixel power lines comprising first pixel power lines and second pixel power lines for transmitting pixel-driving voltages to the plurality of pixels; and
a first power supply and a second power supply for providing the pixel-driving voltages to the first pixel power lines and the second pixel power lines, respectively,

wherein adjacent pixels of the same colour from among the plurality of pixels included in the image display unit are coupled to different ones of the power supplies via respective ones of the pixel power lines.

2. The organic light emitting display as claimed in claim 1, wherein the first pixel power lines and the second pixel power lines extend in opposite directions.

3. The organic light emitting display as claimed in claim 2, wherein the first pixel power lines extend from a first side of the image display unit to a second side of the image display unit that is opposite the first side of the image display unit, and wherein the second pixel power lines extend from the second side of the image display unit to the first side of the image display unit and are interleaved with the first pixel power lines.

4. The organic light emitting display as claimed in claim 3, wherein the first pixel power lines correspond to odd columns of the pixels of the image display unit, and wherein the second pixel power lines correspond to even columns of the pixels of the image display unit.

5. The organic light emitting display as claimed in claim

4, wherein pixels in an odd row from among the plurality of pixels are coupled to respective ones of the plurality of pixel power lines on a first side of the pixels in the odd row, and pixels in an even row from among the plurality of pixels are coupled to respective ones of the plurality of pixel power lines on a second side of the pixels in the even row, the second side of the pixels opposite the first side of the pixels.

6. The organic light emitting display as claimed in claim 4, wherein the pixels are arranged in unit pixels, each comprising adjacent red (R), green (G), and blue (B) pixels of the plurality of pixels, such that the pixels of each of the unit pixels are coupled to adjacent ones of the plurality of pixel power lines coupled to the same one of the power supplies, and that two of the pixels of each of the unit pixels are coupled to the same one of the plurality of pixel power lines.

7. The organic light emitting display as claimed in claim 6, wherein the unit pixels are arranged in rows, and the red pixel of each of the unit pixels in an odd row among the plurality of rows is coupled to a respective one of the plurality of pixel power lines, and the green and blue pixels of each of the unit pixels in the odd row are coupled to a respective another one of the plurality of pixel power lines.

8. The organic light emitting display as claimed in claim 7, wherein the red and green pixels of each of the unit pixels in an even row among the plurality of rows are coupled to a respective one of the plurality of pixel power lines, and the blue pixel of each of the unit pixels in the even row is coupled to a respective another one of the plurality of pixel power lines.

9. The organic light emitting display as claimed in any one of the preceding claims, wherein the pixel-driving voltages provided by the first and second power supplies are positive voltages of the same level.

10. The organic light emitting display as claimed in any one of the preceding claims, wherein the pixels are arranged in rows and columns, and the adjacent pixels of the same colour comprise two pixels of the same colour in the same row or column from among the plurality of pixels with no intervening pixels of the same colour.

11. The organic light emitting display as claimed in claim 10, wherein the colours comprise red (R), green (G), and blue (B), and each of the pixels in the same column are of the same colour.

12. The organic light emitting display as claimed in any one of the preceding claims, further comprising a plurality of first and second auxiliary pixel power lines crossing the first and second pixel power lines.

13. The organic light emitting display as claimed in claim 12,
wherein the first auxiliary pixel power lines are coupled to the first pixel power lines at crossing points of the first auxiliary pixel power lines and the first pixel power lines, the first auxiliary pixel power lines and the first pixel power lines being configured in a mesh type arrangement, and
wherein the second auxiliary pixel power lines are interleaved with the first auxiliary pixel power lines and are coupled to the second pixel power lines at crossing points of the second auxiliary pixel power lines and the second pixel power lines, the second auxiliary pixel power lines and the second pixel power lines being configured in a mesh type arrangement.
14. The organic light emitting display as claimed in claim 12 or 13, wherein the first and second auxiliary pixel power lines comprise a metal material having a low resistivity and are located in different layers from the first and second pixel power lines.
15. The organic light emitting display as claimed in claim 14, wherein contact holes are located at crossing points of the first auxiliary pixel power lines and the first pixel power lines and at crossing points of the second auxiliary pixel power lines and the second pixel power lines so that the first and second auxiliary pixel power lines are electrically coupled to the first and second pixel power lines, respectively.

5

10

15

20

25

30

FIG. 1

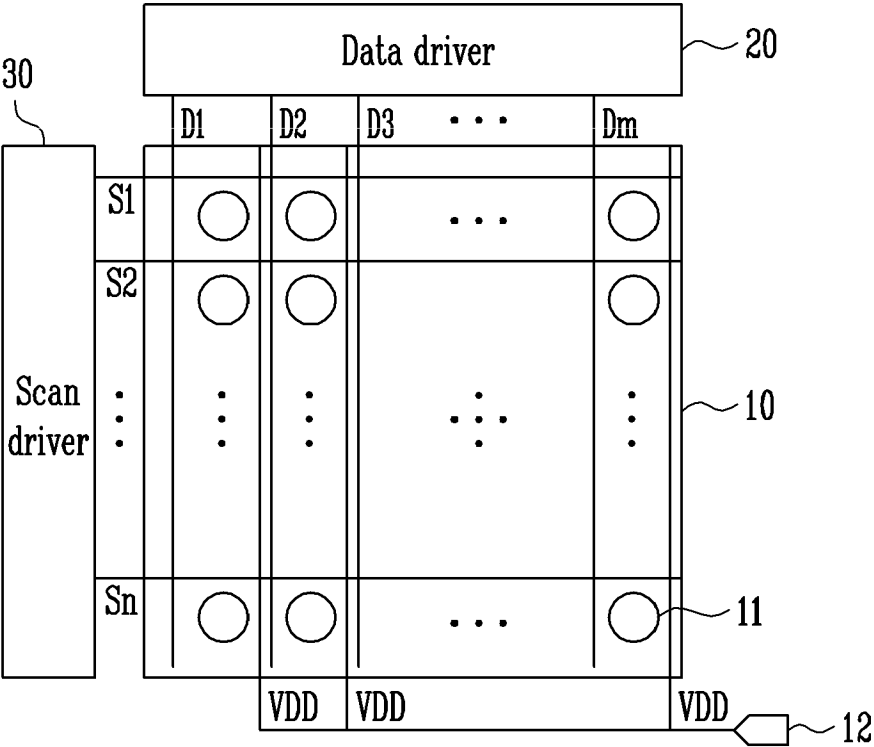


FIG. 2

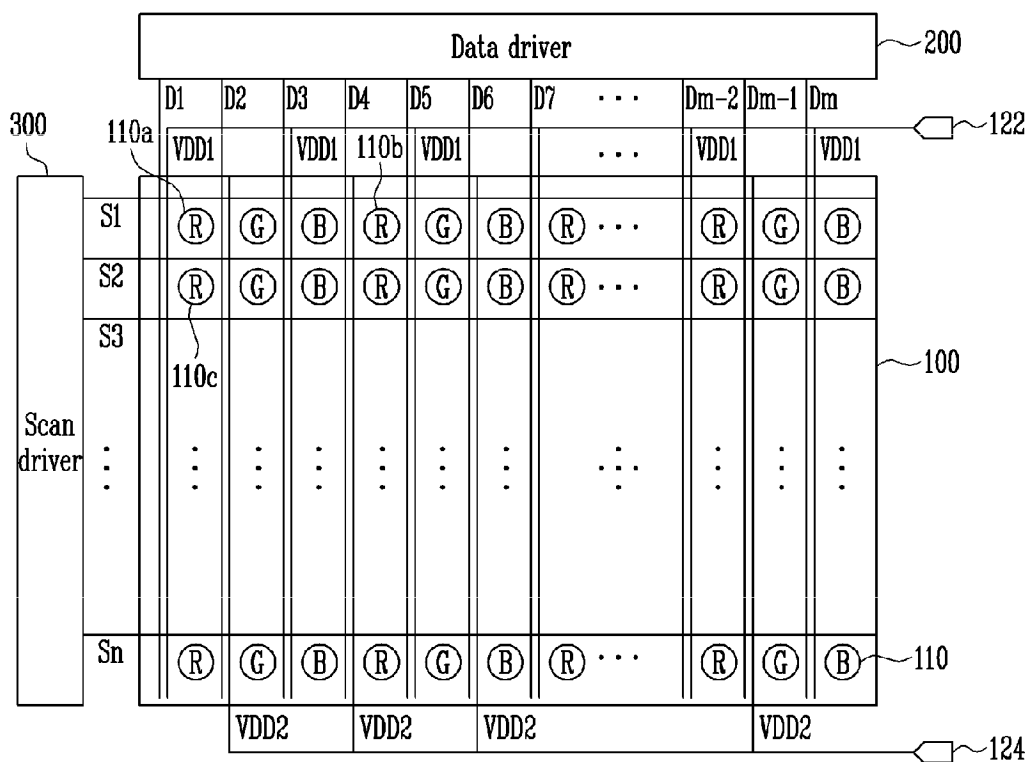


FIG. 3

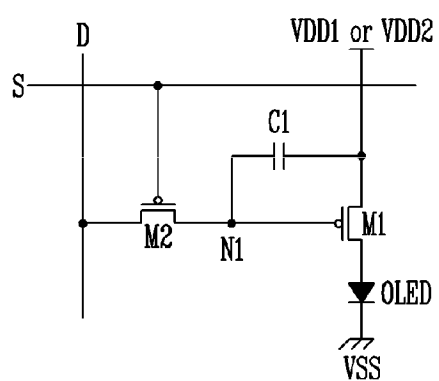


FIG. 4A

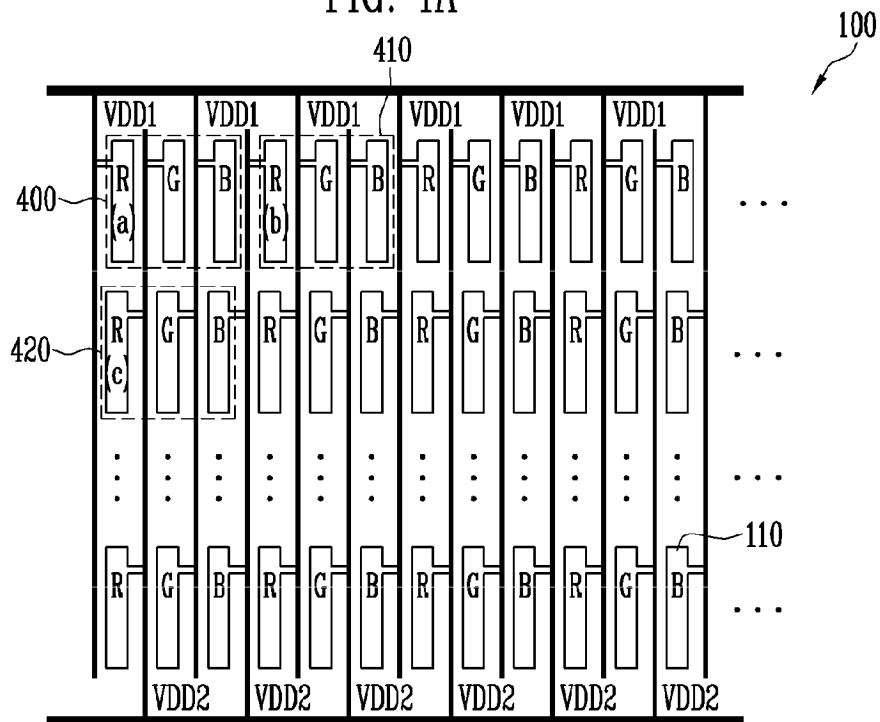


FIG. 4B

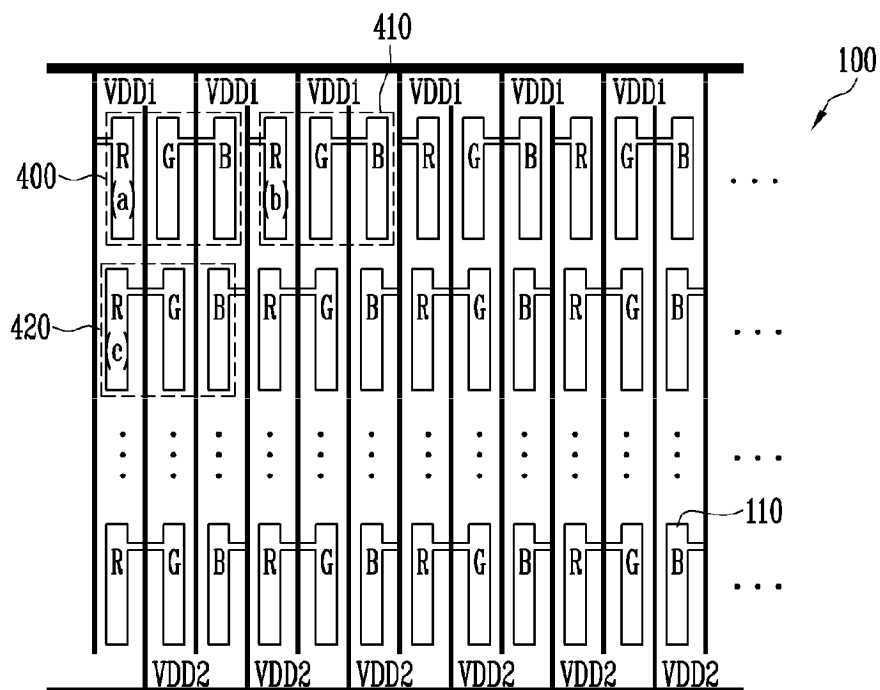


FIG. 5

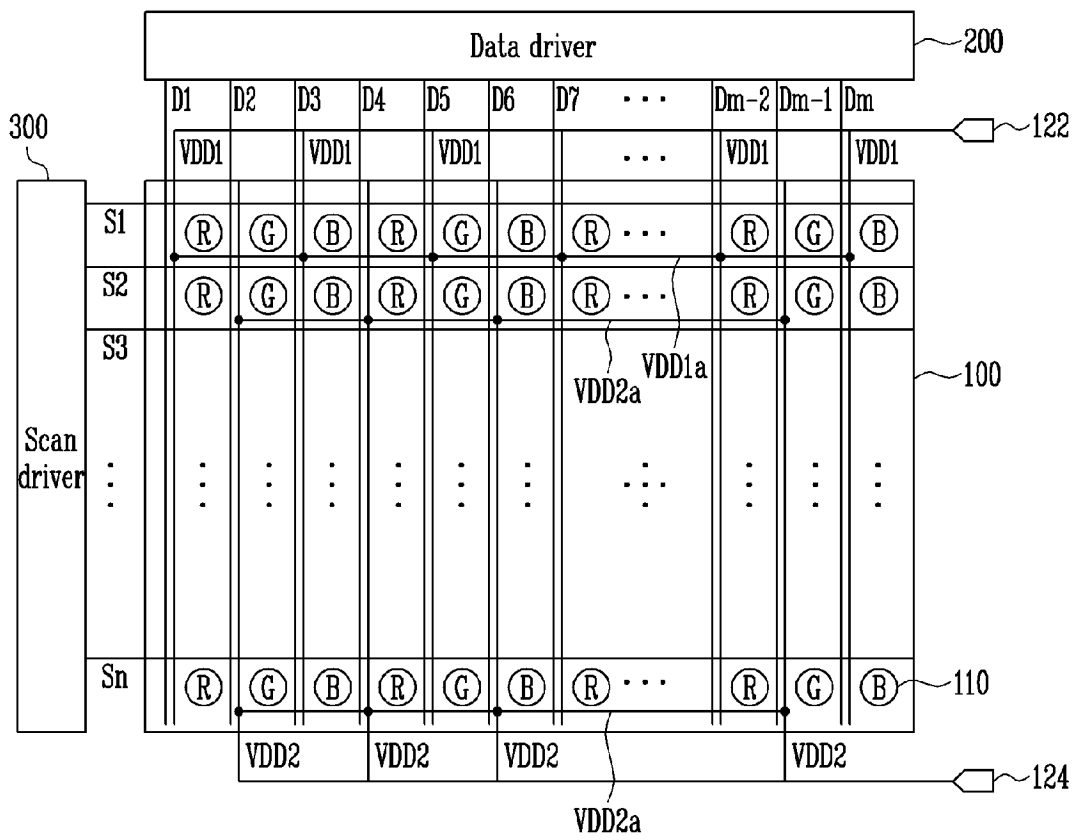
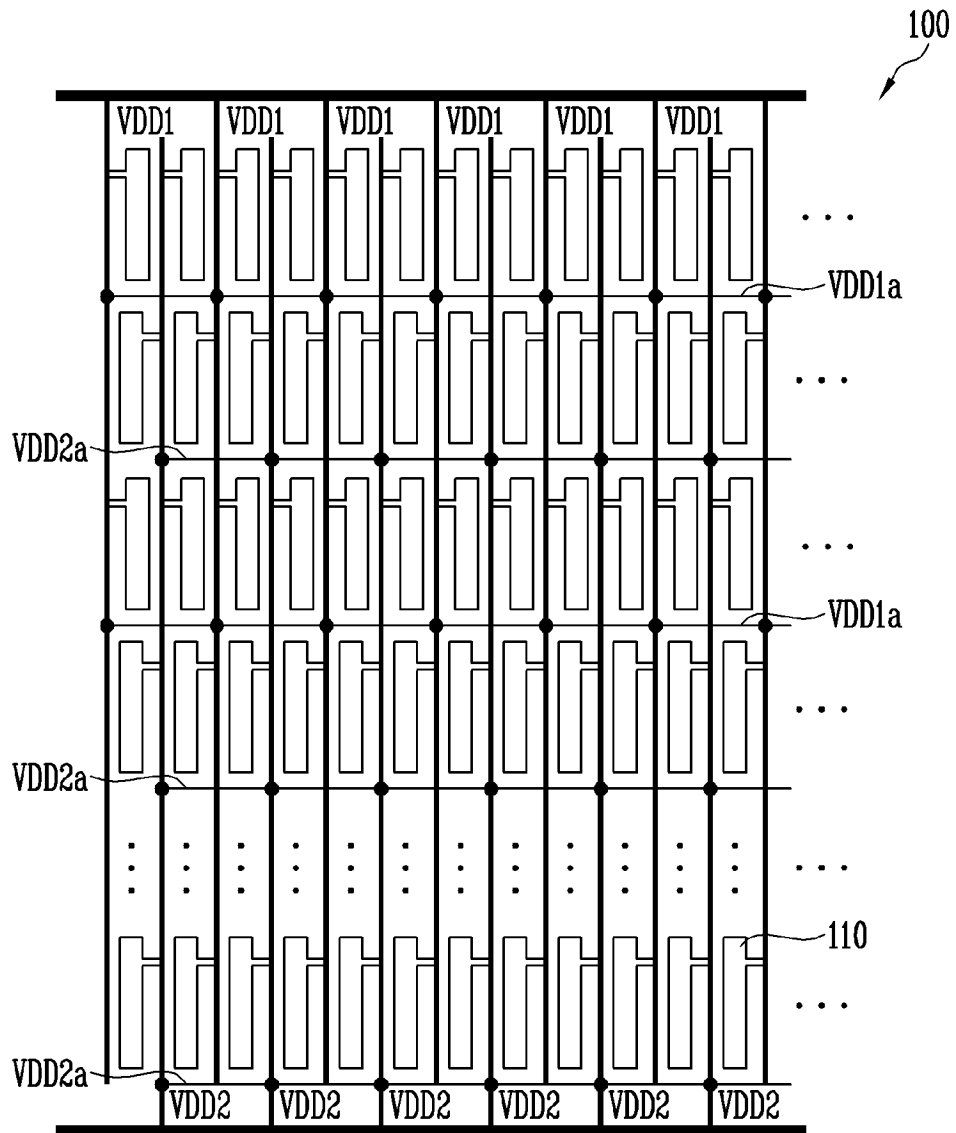


FIG. 6



专利名称(译)	有机发光显示器		
公开(公告)号	EP2333758A2	公开(公告)日	2011-06-15
申请号	EP2010175789	申请日	2010-09-08
[标]申请(专利权)人(译)	三星显示有限公司		
申请(专利权)人(译)	三星移动显示器有限公司.		
当前申请(专利权)人(译)	三星移动显示器有限公司.		
[标]发明人	KIM HYUNG SOO AHN SOON SUNG		
发明人	KIM, HYUNG-SOO AHN, SOON-SUNG		
IPC分类号	G09G3/32		
CPC分类号	G09G3/3225 G09G2300/0426 G09G2320/0223 G09G2320/0233 G09G2320/0242 G09G2330/02 G09G2330/06 H01L27/3211 H01L27/3279 H04N9/77		
优先权	1020090117879 2009-12-01 KR		
其他公开文献	EP2333758A3 EP2333758B1		
外部链接	Espacenet		

摘要(译)

有机发光显示器包括具有多个像素的图像显示单元，以及分别从第一和第二电源接收像素驱动电压的第一和第二像素电源线。第二像素电源线与第一像素电源线交错。设置在图像显示单元中的多个像素中的相同颜色的相邻像素耦合到对应于不同电源的像素电源线。这是为了解决或减轻由于像素电源线中的电压降引起的整个图像显示单元的不均匀亮度的问题。

FIG. 1

