



(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:
25.03.2020 Bulletin 2020/13

(51) Int Cl.:
G09G 3/3233 (2016.01)

(21) Application number: **19194871.0**

(22) Date of filing: **02.09.2019**

(84) Designated Contracting States:
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR
 Designated Extension States:
BA ME
 Designated Validation States:
KH MA MD TN

(72) Inventor: **BIAN, Qingfang**
Beijing, Beijing 100085 (CN)

(74) Representative: **dompatent von Kreisler Selting Werner - Partnerschaft von Patent- und Rechtsanwälten mbB**
Deichmannhaus am Dom Bahnhofsvorplatz 1 50667 Köln (DE)

(30) Priority: **21.09.2018 CN 201811109264**

(71) Applicant: **Beijing Xiaomi Mobile Software Co., Ltd.**
Beijing 100085 (CN)

(54) **DISPLAY STRUCTURE, DISPLAY PANEL USING THE SAME AND DISPLAY DEVICE USING THE SAME**

(57) The present disclosure relates to a display structure (X), a display panel including the display structure (X), and a display device including the display panel and an image acquisition device. The display structure (X) includes a plurality of pixels disposed in a first region (A) of the display structure (X), wherein each pixel of the plurality of pixels includes a plurality of sub-pixels of N number of colors, and each sub-pixel of the plurality of

sub-pixels includes an organic light emitting diode (OLED); and N number of driving circuits disposed in a second region (B) of the display structure (X), wherein an i^{th} driving circuit of the N number of driving circuits is configured to drive each sub-pixel of an i^{th} color of the plurality of sub-pixels, wherein $1 \leq i \leq N$ and N is an integer greater than 1.

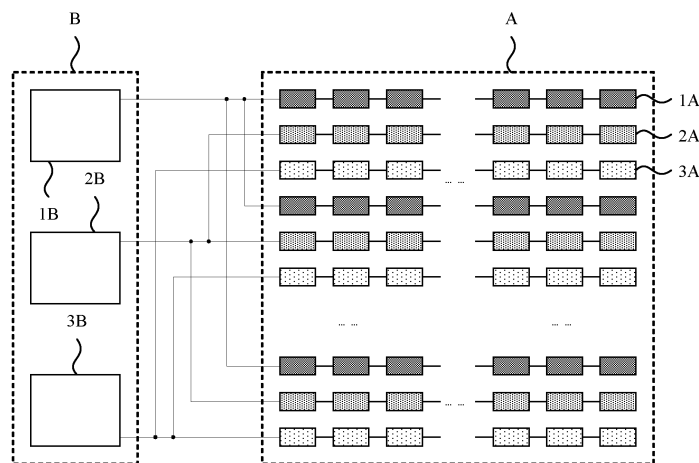


FIG. 1

Description

TECHNICAL FIELD

[0001] The present disclosure relates to the field of display technology, and more particularly to a display structure, a display panel using the display structure and a display device using the display panel.

BACKGROUND

[0002] In order to improve the intelligence of mobile terminals, more and more functions are integrated into smart terminals, such as the function of taking photos. In order to attain the function of taking photos, an image acquisition device must be integrated into the smart terminal. In order to enable the image acquisition device to acquire images from the front of the mobile terminal, an opening (e.g., hole) is made on the front of the mobile terminal for disposing the image acquisition device. This adversely affects the appearance of the mobile terminal and reduces the area ratio of the display region on the front of the mobile terminal.

SUMMARY

[0003] This Summary is provided to introduce a selection of aspects of the present disclosure in a simplified form that are further described below in the Detailed Description. This Summary is not intended to identify key features or essential features of the claimed subject matter, nor is it intended to be used to limit the scope of the claimed subject matter.

[0004] Aspects of the disclosure provide a display structure. The display structure includes a plurality of pixels disposed in a first region of the display structure, wherein each pixel of the plurality of pixels includes a plurality of sub-pixels of N number of colors, and each sub-pixel of the plurality of sub-pixels includes an organic light emitting diode; and N number of driving circuits disposed in a second region of the display structure, wherein an i^{th} driving circuit of the N number of driving circuits is configured to drive each sub-pixel of an i^{th} color of the plurality of sub-pixels, wherein $1 \leq i \leq N$ and N is an integer greater than 1.

[0005] According to an aspect, each of the N number of driving circuits includes a driving transistor, wherein the organic light emitting diode of each of the plurality of sub-pixels includes a first electrode, a second electrode, and an organic light emitting layer disposed between the first electrode and the second electrode, and wherein a driving transistor of the i^{th} driving circuit is connected to the first electrode of the organic light emitting diode of each respective sub-pixel of the plurality of sub-pixels of the i^{th} color.

[0006] According to another aspect, the first electrodes of adjacent same color sub-pixels are connected to each other.

[0007] According to yet another aspect, each driving circuit of the N number of driving circuits includes a switching transistor, wherein a first terminal of the switching transistor is connected to a scanning line, and a second terminal of the switching transistor is connected to a data line; a driving transistor, wherein a first terminal of the driving transistor is connected to a third terminal of the switching transistor, a second terminal of the driving transistor is connected to a preset voltage, and a third terminal of the driving transistor is connected to the organic light emitting diode; and a capacitor, wherein a first terminal of the capacitor is connected to the first terminal of the driving transistor, and a second terminal of the capacitor is connected to the second terminal of the driving transistor.

[0008] Aspects of the disclosure also provide a display panel. The display panel includes a display structure and an effective light emitting region. The display structure includes a plurality of pixels disposed in a first region of the display structure, wherein each pixel of the plurality of pixels includes a plurality of sub-pixels of N number of colors, and each sub-pixel of the plurality of sub-pixels includes an organic light emitting diode; and N number of driving circuits disposed in a second region of the display structure, wherein an i^{th} driving circuit of the N number of driving circuits is configured to drive each sub-pixel of an i^{th} color of the plurality of sub-pixels, wherein $1 \leq i \leq N$, and N is an integer greater than 1. The effective light emitting region includes a plurality of effective light emitting pixels and a plurality of driving circuits disposed in the effective light emitting region, wherein each of the plurality of effective light emitting pixels includes a plurality of sub-pixels, and each of the plurality of sub-pixels in the effective light emitting region corresponds to a respective one of the plurality of driving circuits.

[0009] According to an aspect, the display panel also includes a gate driving circuit configured to input a gate driving signal to each of the plurality of driving circuits; and a data signal circuit configured to input a data signal to each of the plurality of driving circuits.

[0010] According to an aspect, the display panel also includes an array substrate, wherein the gate driving circuit is disposed on the array substrate.

[0011] Aspects of the disclosure also provide a display device. The display device includes a display panel and an image acquisition device. The display panel includes a display structure and an effective light emitting region. The display structure includes a plurality of pixels disposed in a first region of the display structure, wherein each pixel of the plurality of pixels includes a plurality of sub-pixels of N number of colors, and each sub-pixel of the plurality of sub-pixels includes an organic light emitting diode; and N number of driving circuits disposed in a second region of the display structure, wherein an i^{th} driving circuit of the N number of driving circuits is configured to drive each sub-pixel of an i^{th} color of the plurality of sub-pixels, wherein $1 \leq i \leq N$, and N is an integer greater than 1. The effective light emitting region includes a plu-

rality of effective light emitting pixels and a plurality of driving circuits disposed in the effective light emitting region, wherein each of the plurality of effective light emitting pixels includes a plurality of sub-pixels, and each of the plurality of sub-pixels in the effective light emitting region corresponds to a respective one of the plurality of driving circuits. The image acquisition device is disposed in the first region and is located on a side of the display structure away from a light exiting direction.

[0012] According to an aspect, the display device also includes a sensor disposed in the first region and located on the side of the display structure away from the light exiting direction.

[0013] It is to be understood that both the foregoing general description and the following detailed description are illustrative and explanatory only and are not restrictive of the present disclosure.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014] The accompanying drawings, which are incorporated in and constitute a part of this specification, illustrate aspects consistent with the disclosure and, together with the description, serve to explain the principles of the disclosure.

FIG. 1 is a schematic diagram of a display structure shown in accordance with an exemplary aspect of the present disclosure.

FIG. 2 is a schematic diagram of a display structure shown in accordance with another exemplary aspect of the present disclosure.

FIG. 3 is a schematic cross-sectional view of a display structure shown in accordance with an exemplary aspect of the present disclosure.

FIG. 4 is a schematic cross-sectional view of a display structure shown in accordance with another exemplary aspect of the present disclosure.

FIG. 5 is a schematic diagram of a driving circuit shown in accordance with an exemplary aspect of the present disclosure.

FIG. 6 is a schematic diagram of a display panel shown in accordance with an exemplary aspect of the present disclosure.

FIG. 7 is a schematic block diagram of a device for display shown in accordance with an exemplary aspect of the present disclosure.

[0015] The specific aspects of the present disclosure, which have been illustrated by the accompanying drawings described above, will be described in detail below. These accompanying drawings and description are not intended to limit the scope of the present disclosure in any manner, but to explain the concept of the present disclosure to those skilled in the art via referencing specific aspects.

DETAILED DESCRIPTION

[0016] Reference will now be made in detail to exemplary aspects, examples of which are illustrated in the accompanying drawings. The following description refers to the accompanying drawings in which the same numbers in different drawings represent the same or similar elements unless otherwise represented. The implementations set forth in the following description of illustrative aspects do not represent all implementations consistent with the disclosure. Instead, they are merely examples of apparatuses and methods consistent with aspects related to the disclosure as recited in the appended claims.

[0017] FIG. 1 is a schematic diagram of a display structure shown in accordance with an embodiment of the present disclosure, and FIG. 2 is a schematic diagram of a display structure shown in accordance with another embodiment of the present disclosure. As shown in FIGS. 1 and 2, each of the display structures includes:

a plurality of pixels, disposed in a first region A of the display structure, wherein each pixel includes n colors of sub-pixels, and each of the sub-pixels includes an organic light emitting diode; and
n driving circuits, disposed in a second region B of the display structure, wherein the i^{th} driving circuit is configured to drive each sub-pixel of the i^{th} color, $1 \leq i \leq n$, and n is an integer greater than 1.

[0018] In one embodiment, $n=3$ is taken as an example, that is, each pixel includes three colors of sub-pixels, for example, the three colors are red, green and blue respectively, wherein the red sub-pixel 1A, green sub-pixel 2A and blue sub-pixel 3A may be arranged in a matrix as shown in FIG. 1, in this case, each pixel includes three sub-pixels respectively corresponding to the above three colors. Alternatively, the sub-pixels may be arranged in a crystal form as shown in FIG. 2, in which case, each pixel includes one red sub-pixel 1A, one blue sub-pixel 3A and two green sub-pixels 2A.

[0019] It should be noted that the sub-pixels included in each pixel are not limited to those described in the above embodiments, and the arrangement of the sub-pixels is not limited to the case described in the above embodiments either.

[0020] According to the embodiment of the present disclosure, since the driving circuit contains light blocking structures such as transistors, capacitors, or the like, while the organic light emitting diode is transparent, the display structure of the first region is nearly transparent by disposing the sub-pixels in the first region and disposing the driving circuits in the second region, and the sub-pixels of different colors are respectively driven by different driving circuits, so that the display structure of the first region can have a display function to some extent, for example, can display different colors.

[0021] According to the above, in a display device including the display structure, if it needs to dispose an

image acquisition device in the display device, the image acquisition device can be disposed under the first region of the display structure. Since the first region is nearly transparent, it can be ensured that the image acquisition device cannot be blocked, and the first region still has a certain display effect, so it belongs to a part of the display region as well, which is advantageous for increasing the proportion of display region on the front of the display device and making the display device appear more beautiful as a whole.

[0022] Optionally, the driving circuit includes a driving transistor, and the organic light emitting diode includes a first electrode, a second electrode and an organic light emitting layer disposed between the first electrode and the second electrode;

wherein the driving transistor in the i^{th} driving circuit is connected to the first electrode of each sub-pixel of the i^{th} color.

[0023] In one embodiment, the structure of the organic light emitting diode may be configured to have an organic light emitting layer disposed between the first electrode and the second electrode, wherein the first electrode is an anode and the second electrode is a cathode, or the first electrode is the cathode and the second electrode is the anode. The following mainly exemplifies the case where the first electrode is the anode and the second electrode is the cathode.

[0024] For different organic light emitting diodes, the first electrode is disposed on the same side of the organic light emitting diodes. Therefore, by connecting the driving transistor to the plurality of first electrodes to realize the driving of the plurality of sub-pixels, the structure is relatively simple in comparison with the structure that the driving transistor is connected to the first electrodes of a portion of the sub-pixels and to the second electrodes of another portion of the sub-pixels.

[0025] FIG. 3 is a schematic cross-sectional view of a display structure shown in accordance with an embodiment of the present disclosure, and FIG. 4 is a schematic cross-sectional view of a display structure shown in accordance with another embodiment of the present disclosure. As shown in FIGS. 3 and 4, the first electrodes 6 of adjacent same color sub-pixels are connected to each other.

[0026] In one embodiment, as shown in FIG. 3, the display structure, from bottom to top, may sequentially include a substrate 1, a buffer layer 2, a gate insulating layer 3, an interlayer dielectric layer 4, a flat layer 5, a first electrode 6, a pixel defining layer 7 and a second electrode 8. The driving transistor includes an active layer 10, a gate electrode 11, a source electrode 12 and a drain electrode 13. An organic material layer 9 is disposed between the first electrode 6 and the second electrode 8.

[0027] Each block of organic material layer 9 corresponds to one sub-pixel, and the first electrodes 6 of a plurality of sub-pixels of the same color are connected to each other. According to the above, when the first electrodes 6 are formed, the first electrodes 6 can be con-

nected to each other, and thus the plurality of same color sub-pixels are connected to each other. By connecting the driving transistor with the first electrode 6 of any one of the sub-pixels, a structure that the driving transistor is connected to the first electrode 6 of the sub-pixels can be formed, which is advantageous for simplifying the connection structure between the driving transistor and the first electrode 6.

[0028] The driving transistor may be connected to the plurality of same color sub-pixels in other manners. For example, in a case where the first electrodes of the same color sub-pixel are not connected to each other, a via-hole may be disposed at a position corresponding to the first electrode of each sub-pixel, and then the drain electrode of the driving transistor is extended to each via-hole, thereby realizing the electrical connection to the first electrode in each via-hole.

[0029] It should be noted that, in the embodiment shown in FIG. 3, the organic material layer 9 may be disposed above the driving transistor, that is, the second region B where the driving circuits are located may emit light as well. No organic material layer may be disposed above the driving transistor if such is needed, as shown in FIG. 4, so that the second region B where the driving circuits are located does not emit light.

[0030] FIG. 5 is a schematic diagram of a driving circuit shown in accordance with an embodiment of the present disclosure. As shown in FIG. 5, the driving circuit includes:

a switching transistor TFT1, a first terminal of the switching transistor TFT1 is connected to a scanning line SL, and a second terminal of the switching transistor TFT1 is connected to a data line DL;

a driving transistor TFT2, a first terminal of the driving transistor TFT2 is connected to a third terminal of the switching transistor TFT1, a second terminal of the driving transistor TFT2 is connected to a preset voltage Vdd, and a third terminal of the driving transistor TFT2 is connected to the organic light emitting diode OLED; and

a capacitor C, a first terminal of the capacitor C is connected to the first terminal of the driving transistor TFT2, and a second terminal of the capacitor C is connected to the second terminal of the driving transistor TFT2.

[0031] In one embodiment, the above described first terminal may refer to a gate electrode, the second terminal may refer to a source electrode, and the third terminal may refer to a drain electrode. The scanning line SL may input a signal to the gate electrode of the switching transistor TFT1 such that the switching transistor TFT1 is turned on, and then the signal on the data line DL is transmitted to the drain electrode of the switching transistor TFT1 through its source electrode, and is applied to the gate electrode of the driving transistor TFT2, so that the driving transistor TFT2 is turned on. The signal of the

preset voltage V_{dd} generates a current through the driving transistor TFT2 and input it to the organic light emitting diode OLED, such that the organic light emitting diode OLED emits light.

[0032] It should be noted that the structure of the driving circuit is not limited to the case in the embodiment shown in FIG. 5. For example, a structure or a process such as 6T1C, 7T1C or NMOS, CMOS may be employed as well.

[0033] FIG. 6 is a schematic diagram of a display panel shown in accordance with an embodiment of the present disclosure. As shown in FIG. 6, a display panel, includes the display structure X of any of the above embodiments, and further includes:

an effective light emitting region Y, wherein a plurality of effective light emitting pixels and a plurality of driving circuits are disposed in the effective light emitting region Y, each effective light emitting pixel includes a plurality of sub-pixels, and the sub-pixels in the effective light emitting region Y correspond to a driving circuits one by one.

[0034] In one embodiment, in the effective light emitting region of the display panel, since each sub-pixel corresponds to a driving circuit one by one, for example, the driving transistor is disposed under the organic light emitting layer of the sub-pixel, such that each sub-pixel can be separately controlled, thereby realizing that the pattern can be displayed in the effective light emitting region.

[0035] In this case, if it needs to dispose an image acquisition device under the display panel, the image acquisition device can be disposed under the first region of the display structure, and since the first region is nearly transparent, and has a certain display effect, the effective light emitting region and the first region of the display structure together can function as the display region, so that the region in which the image acquisition device is disposed can be used as the display region as well, which is advantageous for increasing the proportion of display region on the front of the display panel and making the display panel appear more beautiful as a whole.

[0036] It should be noted that, in the embodiment shown in FIG. 3, the organic material layer 9 may be disposed above the driving transistor, that is, the second region B where the driving circuits are located may emit light as well. In this case, the driving transistor in the display structure and the driving transistor of the driving circuit in the effective light emitting region can be used at the same time.

[0037] In the case of the embodiment shown in FIG. 4, no organic material layer is disposed above the driving transistor, and then the driving transistor in the display structure can be disposed in the frame region of the display panel, thereby avoiding that the driving transistor affects the transmittance of the display panel.

[0038] In one embodiment, based on the embodiment shown in FIG. 6, the display structure includes:

a plurality of pixels, disposed in a first region of the display structure, wherein each pixel includes n

colors of sub-pixels, and the sub-pixel includes an organic light emitting diode; and
n driving circuits, disposed in a second region of the display structure, wherein the ith driving circuit is configured to drive each sub-pixel of the ith color, $1 \leq i \leq n$, and n is an integer greater than 1.

[0039] In one embodiment, the driving circuit includes a driving transistor, and the organic light emitting diode includes a first electrode, a second electrode, and an organic light emitting layer disposed between the first electrode and the second electrode;

wherein the driving transistor in the ith driving circuit is connected to a first electrode of each sub-pixel of the ith color.

[0040] In one embodiment, the first electrodes of adjacent same color sub-pixels are connected to each other.

[0041] In one embodiment, the driving circuit includes:

a switching transistor, wherein a first terminal of the switching transistor is connected to a scanning line, and a second terminal of the switching transistor is connected to a data line;

a driving transistor, wherein a first terminal of the driving transistor is connected to a third terminal of the switching transistor, a second terminal of the driving transistor is connected to a preset voltage, and a third terminal of the driving transistor is connected to the organic light emitting diode; and

a capacitor, wherein a first terminal of the capacitor is connected to the first terminal of the driving transistor, and a second terminal of the capacitor is connected to the second terminal of the driving transistor.

[0042] Optionally, the display panel further includes:

a gate driving circuit, configured to input a gate driving signal to the driving circuit; and

a data signal circuit, configured to input a data signal to the drive circuit.

[0043] The driving circuit includes the driving circuit in the display structure, and includes the driving circuit in the effective light emitting region as well.

[0044] In one embodiment, the gate driving circuit can input a gate driving signal to the driving circuit through the scanning line. For the n driving circuits in the display structure, the gate driving signal can be input to the n driving circuits through one scanning line, or the gate driving signal can be input to the n driving circuits through n scanning lines in a one-to-one correspondence as well. In addition, the gate driving signal can be input to the a driving circuits of the n driving circuits through one scanning line, and the gate driving signal can be input to the n-a driving circuits of the n driving circuits through n-a scanning lines in a one-to-one correspondence, wherein

a is a positive integer smaller than n.

[0045] For the driving circuit in the effective light-emitting region of the above display panel, the gate driving signal may be input through the scanning line. The scanning line, through which the gate driving signal may be input to the driving circuit in the effective light-emitting region of the display panel, may be multiplexed to input the gate driving signal to the driving circuit in the above display structure.

[0046] The scanning line through which the gate driving signal may be input to the driving circuit in the effective light-emitting region of the display panel, and the scanning line through which the gate driving signal may be input to the driving circuit in the above display structure, may be different. For example, the gate driving signal is input to the driving circuit in the display structure through one scanning line, and the gate driving signal is input to the driving circuit in the effective light emitting region of the display panel through m scanning lines. The scanning line, through which the gate driving signal is input to the driving circuit in the above display structure, can be placed at any position in the m+1 scanning lines, for example, in the first row or in the (m+1)th row.

[0047] Optionally, the display panel further includes: an array substrate, wherein the gate driving circuit is disposed on the array substrate.

[0048] In one embodiment, the gate driving circuit (Gate IC) may be disposed on the array substrate to form a GOA (Gate IC On Array) structure.

[0049] The embodiment of the present disclosure further provides a display device that includes the display panel of any of the above embodiments and further includes:

an image acquisition device, disposed in the first region and located on a side of the display structure away from the light exiting direction.

[0050] In one embodiment, the image acquisition device can be disposed under the display panel, specifically in the first region of the display structure and on a side of the display structure away from the light exiting direction. Since the first region is nearly transparent, and has a certain display effect, the effective light emitting region and the first region of the display structure together can function as the display region, so that the region in which the image acquisition device is disposed can be used as the display region as well, which is advantageous for increasing the proportion of display region on the front of the display device and making the display device appear more beautiful as a whole.

[0051] Optionally, the display device further includes: a sensor, disposed in the first region and located on a side of the display structure away from the light exiting direction.

[0052] In one embodiment, if it needs to dispose the sensor in the display device, the sensor may be disposed in the first region of the display structure and located on the side of the display structure away from the light exiting direction as well, so that the region where the sensor is

disposed can be used as the display region as well, which is advantageous for increasing the proportion of display region on the front of the display device and making the display device appear more beautiful as a whole. Wherein, the sensor may include a distance sensor, an ambient light sensor, or the like.

[0053] It should be noted that the display device may be a mobile terminal, such as a mobile phone, a tablet computer, a smart wearable device, etc., or may be other types of devices, such as a television, a computer screen, or the like.

[0054] FIG. 7 is a schematic block diagram of a device 700 for display shown in accordance with an embodiment of the present disclosure. For example, device 700 can be a mobile phone, a computer, a digital broadcast terminal, a message transceiver, a gaming console, a tablet device, a medical device, a fitness device, a personal digital assistant, or the like.

[0055] Referring to FIG. 7, the device 700 may include one or more of the following components: a processing component 702, a memory 704, a power component 706, a multimedia component 708, an audio component 710, an input/output (I/O) interface 712, a sensor component 714, and a communication component 716. The device 700 further includes the display panel of any of the above embodiments.

[0056] The processing component 702 typically controls the overall operations of the device 700, such as the operations associated with display, telephone call, data communication, camera operation and recording operation. The processing component 702 can include one or more processors 720 to execute instructions to perform all or part of the steps in the above described methods. Moreover, the processing component 702 can include one or more modules to facilitate the interaction between the processing component 702 and other components. For example, the processing component 702 can include a multimedia module to facilitate the interaction between the multimedia component 708 and the processing component 702.

[0057] The memory 704 is configured to store various types of data to support the operation of the device 700. Examples of such data include instructions for any application or method operated on device 700, such as the contact data, the phone book data, messages, pictures, videos, and the like. The memory 704 can be implemented by any type of volatile or non-volatile storage device, or a combination thereof, such as a static random access memory (SRAM), an electrically erasable programmable read-only memory (EEPROM), an erasable programmable read-only memory (EPROM), a programmable read-only memory (PROM), a read-only memory (ROM), a magnetic memory, a flash memory, a magnetic disk or optical disk.

[0058] The power component 706 provides power to the respective components of the device 700. The power component 706 can include a power management system, one or more power sources, and other components

associated with the generation, management, and distribution of power in the device 700.

[0059] The multimedia component 708 includes a screen providing an output interface between the device 700 and the user. In some embodiments, the screen can include a liquid crystal display (LCD) and a touch panel (TP). If the screen includes the touch panel, the screen can be implemented as a touch screen to receive input signals from the user. The touch panel includes one or more touch sensors to sense touches, swipes, and gestures on the touch panel. The touch sensors may not only sense a boundary of a touch or swipe action, but further sense a period of time and a pressure associated with the touch or swipe action. In some embodiments, the multimedia component 708 includes a front camera and/or a rear camera. When the device 700 is in an operation mode, such as a photographing mode or a video mode, the front camera and/or the rear camera can receive external multimedia data. Each of the front camera and the rear camera may be a fixed optical lens system or have capability of focus and optical zoom.

[0060] The audio component 710 is configured to output and/or input an audio signal. For example, the audio component 710 includes a microphone (MIC) configured to receive an external audio signal when the device 700 is in an operation mode, such as a call mode, a recording mode and a voice recognition mode. The received audio signal may be further stored in the memory 704 or transmitted via the communication component 716. In some embodiments, the audio component 710 further includes a speaker for outputting the audio signal.

[0061] The I/O interface 712 provides an interface between the processing component 702 and peripheral interface modules such as a keyboard, a click wheel, buttons, and the like. These buttons may include, but are not limited to, a home button, a volume button, a starting button and a locking button.

[0062] The sensor component 714 includes one or more sensors for providing status assessments of various aspects of the device 700. For example, the sensor component 714 can detect an open/closed status of the device 700, relative positioning of components such as the display and the keypad of the device 700. The sensor component 714 can further detect a change in position of one component of the device 700 or of the device 700, the presence or absence of user contacting with the device 700, orientation, or acceleration/deceleration of the device 700, and a change in temperature of the device 700. The sensor component 714 can include a proximity sensor configured to detect the presence of nearby objects without any physical contact. The sensor component 714 can further include a light sensor, such as a CMOS or CCD image sensor, configured to be used in imaging applications. In some embodiments, the sensor component 714 can further include an accelerometer sensor, a gyroscope sensor, a magnetic sensor, a pressure sensor or a temperature sensor.

[0063] The communication component 716 is config-

ured to facilitate wired or wireless communication between the device 700 and other devices. The device 700 can access a wireless network based on a communication standard, such as WiFi, 2G or 3G, or a combination thereof. In an exemplary embodiment, the communication component 716 receives broadcast signals or broadcast associated information from an external broadcast management system via a broadcast channel. In an exemplary embodiment, the communication component 716 further includes a near field communication (NFC) module to facilitate short-range communications. For example, the NFC module can be implemented based on a radio frequency identification (RFID) technology, an infrared data association (IrDA) technology, an ultrawideband (UWB) technology, a Bluetooth (BT) technology and other technologies.

[0064] In an exemplary embodiment, the device 700 may be implemented with one or more application specific integrated circuit (ASIC), digital signal processor (DSP), digital signal processing device (DSPD), programmable logic device (PLD), field programmable Gate array (FPGA), controller, microcontroller, microprocessor or other electronic component.

[0065] In an exemplary embodiment, there is further provided a non-transitory computer-readable storage medium including instructions, such as a memory 704 including instructions executable by the processor 720 of the device 700 to perform the above described method. For example, the non-transitory computer readable storage medium may be a ROM, a random access memory (RAM), a CD-ROM, a magnetic tape, a floppy disc, and an optical data storage device.

[0066] It is noted that the various modules, sub-modules, units, and components in the present disclosure can be implemented using any suitable technology. For example, a module may be implemented using circuitry, such as an integrated circuit (IC). As another example, a module may be implemented as a processing circuit executing software instructions.

[0067] Other embodiments of the present disclosure will be apparent to those skilled in the art after consideration of the specification and practice of the present disclosure disclosed herein. The present application is intended to cover any variations, uses or adaptations of the present disclosure, which are in accordance with the general principle of the present disclosure and include common general knowledge or conventional technical means in the art that are not disclosed in the present disclosure. The specification and embodiments are only illustrative, and the real scope of the present disclosure is defined by the appended claims.

[0068] It should be understood that the present disclosure is not limited to the precise structures that have been described above and shown in the drawings, and various modifications and changes can be made without departing from the scope thereof. The scope of the present disclosure is limited only by the appended claims.

Claims**1.** A display structure (X), comprising:

a plurality of pixels, disposed in a first region (A) of the display structure (X), wherein each pixel of the plurality of pixels comprises a plurality of sub-pixels of N number of colors, and each sub-pixel of the plurality of sub-pixels comprises an organic light emitting diode (OLED); and N number of driving circuits, disposed in a second region (B) of the display structure (X), wherein an i^{th} driving circuit of the N number of driving circuits is configured to drive each sub-pixel of an i^{th} color of the plurality of sub-pixels, wherein $1 \leq i \leq N$ and N is an integer greater than 1.

2. The display structure (X) according to claim 1, wherein each of the N number of driving circuits comprises a driving transistor (TFT2),

wherein the organic light emitting diode (OLED) of each of the plurality of sub-pixels comprises a first electrode (6), a second electrode (8), and an organic light emitting layer (9) disposed between the first electrode (6) and the second electrode (8)" and wherein a driving transistor (TFT2) of the i^{th} driving circuit is connected to the first electrode (6) of the organic light emitting diode (OLED) of each respective sub-pixel of the plurality of sub-pixels of the i^{th} color.

3. The display structure (X) according to claim 2, wherein the first electrodes (6) of adjacent same color sub-pixels are connected to each other.**4.** The display structure (X) according to any one of claims 1 to 3, wherein each driving circuit of the N number of driving circuits comprises:

a switching transistor (TFT1), wherein a first terminal of the switching transistor (TFT1) is connected to a scanning line (SL), and a second terminal of the switching transistor (TFT1) is connected to a data line (DL);

a driving transistor (TFT2), wherein a first terminal of the driving transistor (TFT2) is connected to a third terminal of the switching transistor (TFT1), a second terminal of the driving transistor (TFT2) is connected to a preset voltage, and a third terminal of the driving transistor (TFT2) is connected to the organic light emitting diode (OLED); and

a capacitor (C), wherein a first terminal of the capacitor (C) is connected to the first terminal of the driving transistor (TFT2), and a second terminal of the capacitor (C) is connected to the second terminal of the driving transistor (TFT2).

5. A display panel, comprising:

a display structure (X) that comprises:

a plurality of pixels disposed in a first region (A) of the display structure (X), wherein each pixel of the plurality of pixels comprises a plurality of sub-pixels of N number of colors, and each sub-pixel of the plurality of sub-pixels comprises an organic light emitting diode (OLED); and N number of driving circuits disposed in a second region (B) of the display structure (X), wherein an i^{th} driving circuit of the N number of driving circuits is configured to drive each sub-pixel of an i^{th} color of the plurality of sub-pixels, wherein $1 \leq i \leq N$, and N is an integer greater than 1, and

wherein the display panel further comprises an effective light emitting region (Y) comprising a plurality of effective light emitting pixels and a plurality of driving circuits disposed in the effective light emitting region (Y), wherein each of the plurality of effective light emitting pixels includes a plurality of sub-pixels, and each of the plurality of sub-pixels in the effective light emitting region (Y) corresponds to a respective one of the plurality of driving circuits.

6. The display panel according to claim 5, further comprising:

a gate driving circuit, configured to input a gate driving signal to each of the plurality of driving circuits; and

a data signal circuit, configured to input a data signal to each of the plurality of driving circuits.

7. The display panel according to claim 6, further comprising:

an array substrate, wherein the gate driving circuit is disposed on the array substrate.

8. The display panel according to any one of claims 5-7, wherein each of the N number of driving circuits comprises a driving transistor (TFT2),

wherein the organic light emitting diode (OLED) of each of the plurality of sub-pixels comprises a first electrode (6), a second electrode (8), and an organic light emitting layer (9) disposed between the first electrode (6) and the second electrode (8)" and wherein a driving transistor (TFT2) of the i^{th} driving circuit is connected to the first electrode (6) of the organic light emitting diode (OLED) of each respective sub-pixel of the plurality of sub-pixels of the i^{th} color.

9. The display panel according to claim 8, wherein the first electrodes (6) of adjacent same color sub-pixels are connected to each other.

10. The display panel according to any one of claims 5 to 9, wherein each driving circuit of the N number of driving circuits comprises:

a switching transistor (TFT1), wherein a first terminal of the switching transistor (TFT1) is connected to a scanning line (SL), and a second terminal of the switching transistor (TFT1) is connected to a data line (DL);

a driving transistor (TFT2), wherein a first terminal of the driving transistor (TFT2) is connected to a third terminal of the switching transistor (TFT1), a second terminal of the driving transistor (TFT2) is connected to a preset voltage, and a third terminal of the driving transistor (TFT2) is connected to the organic light emitting diode (OLED); and

a capacitor (C), wherein a first terminal of the capacitor (C) is connected to the first terminal of the driving transistor (TFT2), and a second terminal of the capacitor (C) is connected to the second terminal of the driving transistor (TFT2).

11. A display device, comprising:

a display panel that comprises:

a display structure (X) that comprises:

a plurality of pixels disposed in a first region (A) of the display structure (X), wherein each pixel of the plurality of pixels comprises a plurality of sub-pixels of N number of colors, and each sub-pixel of the plurality of sub-pixels comprises an organic light emitting diode (OLED); and

N number of driving circuits disposed in a second region (B) of the display structure (X), wherein an *i*th driving circuit of the N number of driving circuits is configured to drive each sub-pixel of an *i*th color of the plurality of sub-pixels, wherein $1 \leq i \leq N$, and N is an integer greater than 1, and

wherein the display panel further comprises an effective light emitting region (Y) comprising a plurality of effective light emitting pixels and a plurality of driving circuits disposed in the effective light emitting region (Y), wherein each of the plurality of effective light emitting pixels includes a plurality of sub-pixels, and each of the plurality of sub-

pixels in the effective light emitting region (Y) corresponds to a respective one of the plurality of driving circuits, and

wherein the display device further comprises an image acquisition device disposed in the first region (A) and located on a side of the display structure (X) away from a light exiting direction.

12. The display device according to claim 11, further comprising:

a sensor, disposed in the first region (A) and located on the side of the display structure (X) away from the light exiting direction.

13. The display device according to any one of claims 11-12, wherein each of the N number of driving circuits comprises a driving transistor (TFT2), wherein the organic light emitting diode (OLED) of each of the plurality of sub-pixels comprises a first electrode (6), a second electrode (8), and an organic light emitting layer (9) disposed between the first electrode (6) and the second electrode (8) and wherein a driving transistor (TFT2) of the *i*th driving circuit is connected to the first electrode (6) of the organic light emitting diode (OLED) of each respective sub-pixel of the plurality of sub-pixels of the *i*th color.

14. The display device according to claim 13, wherein the first electrodes (6) of adjacent same color sub-pixels are connected to each other.

15. The display device according to any one of claims 11 to 14, wherein each driving circuit of the N number of driving circuits comprises:

a switching transistor (TFT1), wherein a first terminal of the switching transistor (TFT1) is connected to a scanning line (SL), and a second terminal of the switching transistor (TFT1) is connected to a data line (DL);

a driving transistor (TFT2), wherein a first terminal of the driving transistor (TFT2) is connected to a third terminal of the switching transistor (TFT1), a second terminal of the driving transistor (TFT2) is connected to a preset voltage, and a third terminal of the driving transistor (TFT2) is connected to the organic light emitting diode (OLED); and

a capacitor (C), wherein a first terminal of the capacitor (C) is connected to the first terminal of the driving transistor (TFT2), and a second terminal of the capacitor (C) is connected to the second terminal of the driving transistor (TFT2).

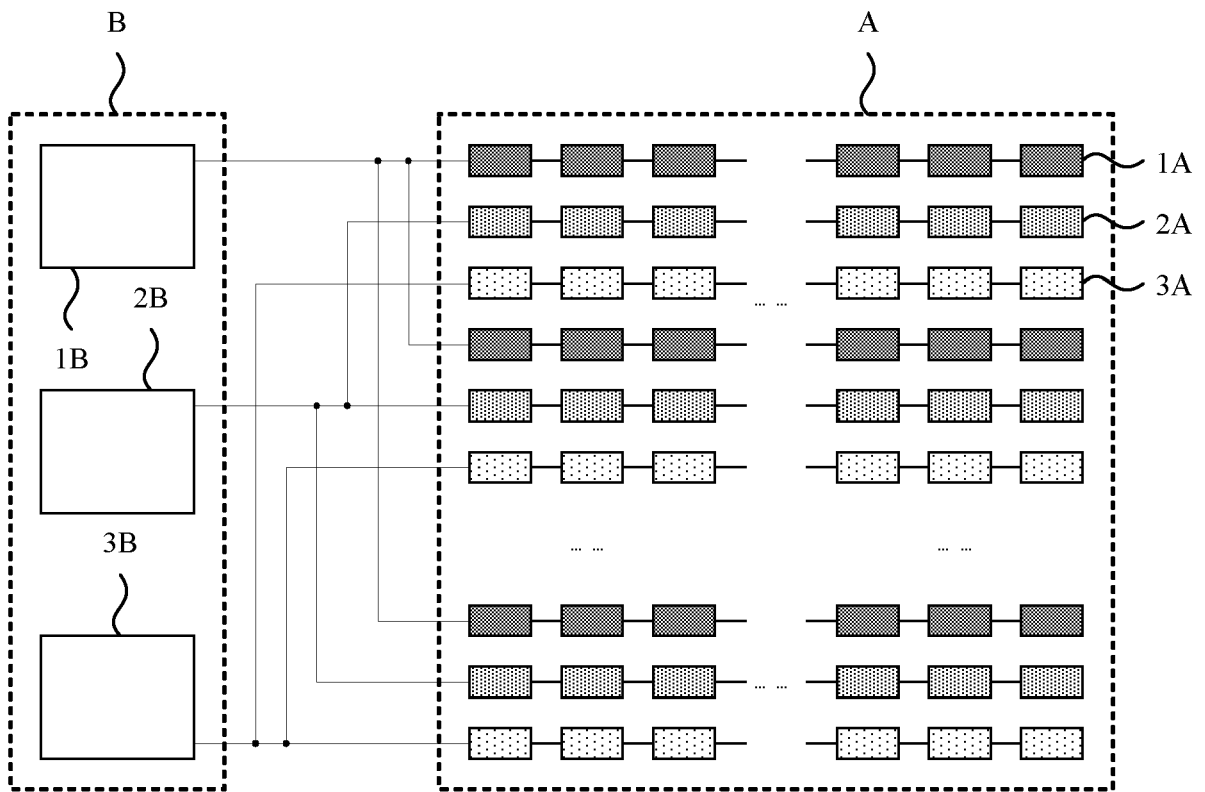


FIG. 1

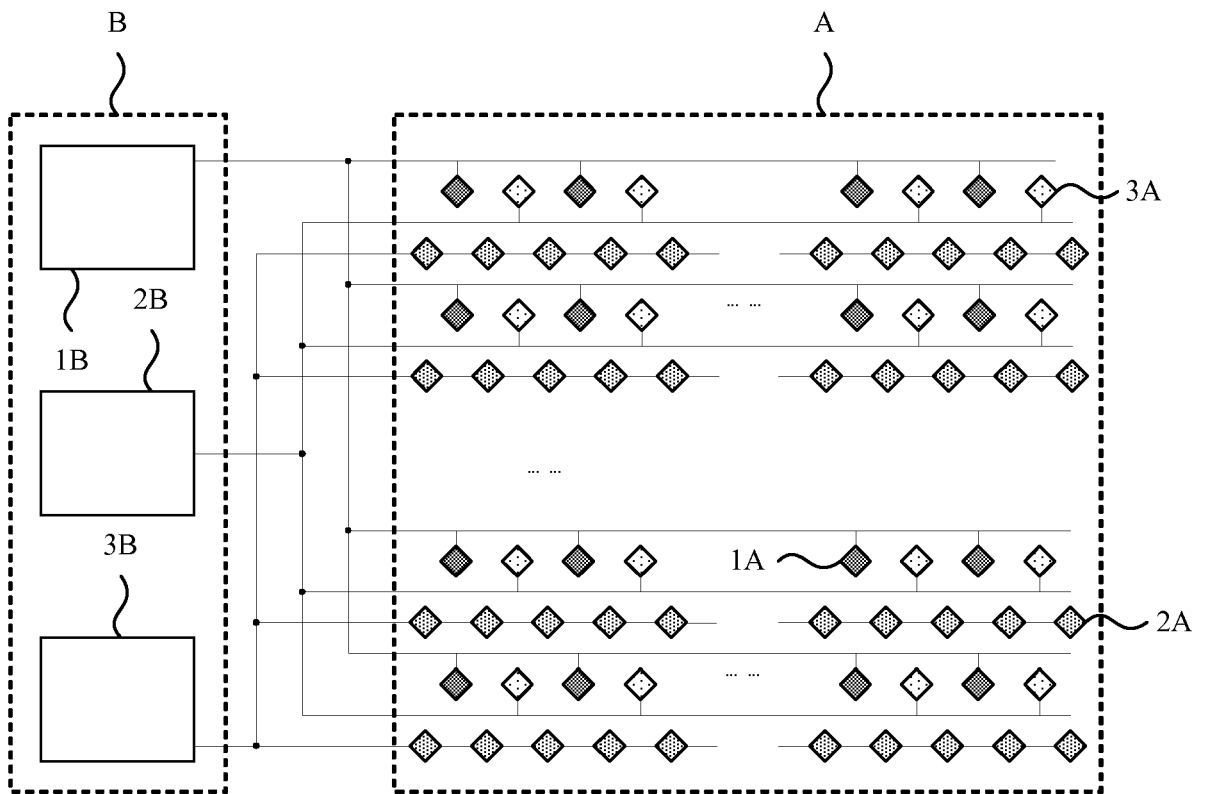


FIG. 2

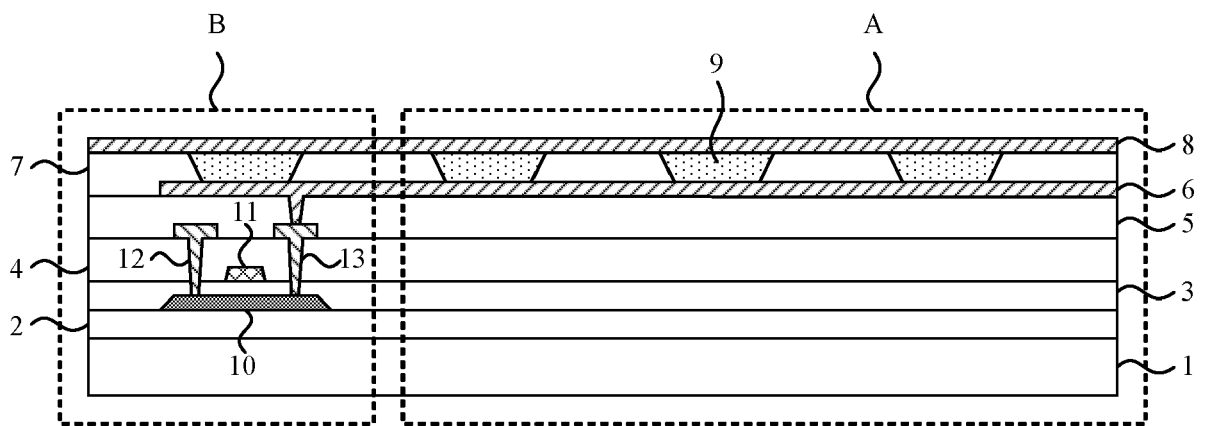


FIG. 3

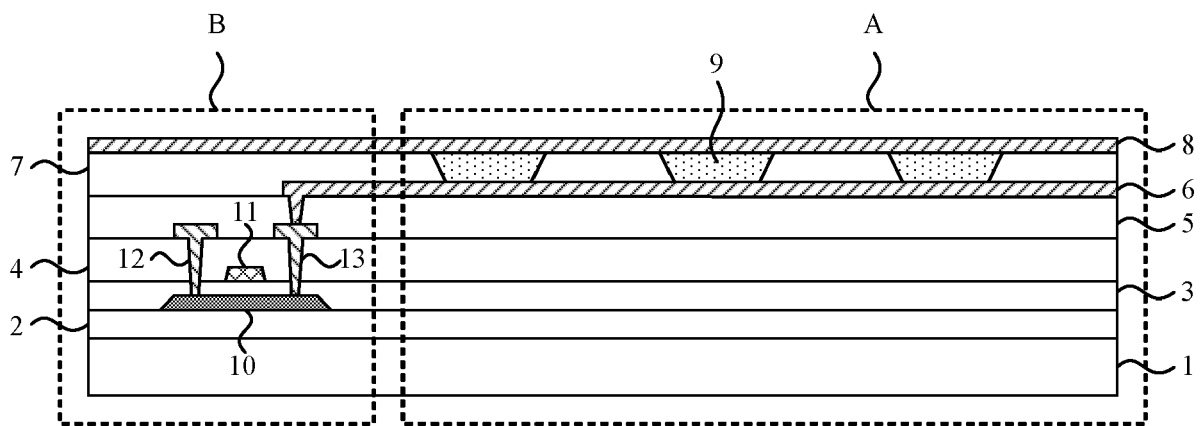


FIG. 4

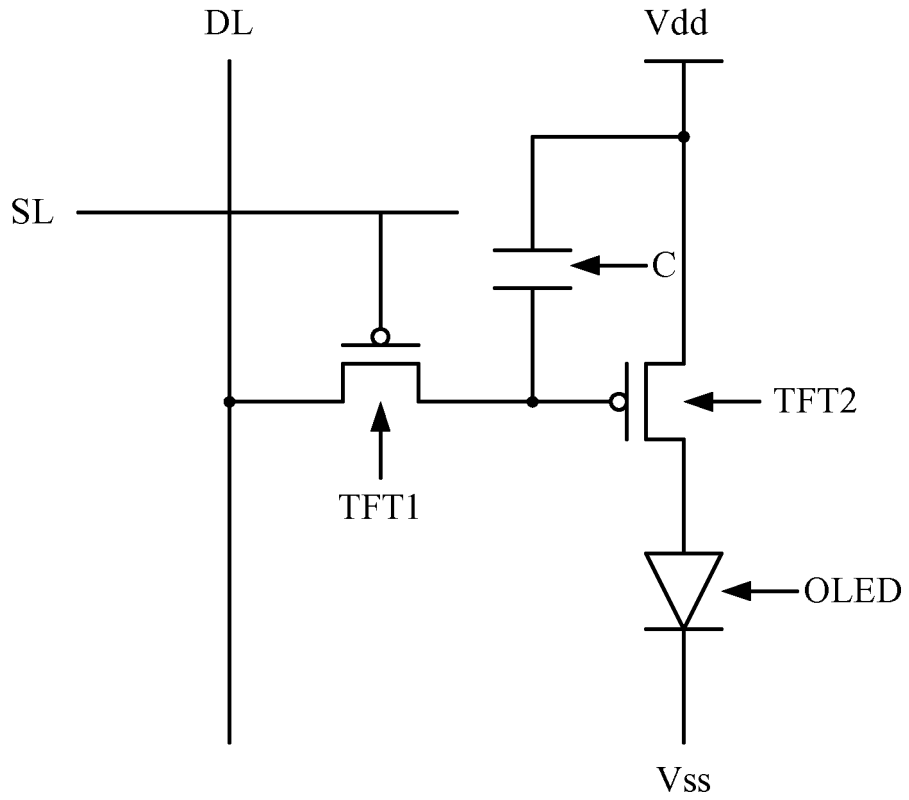


FIG. 5

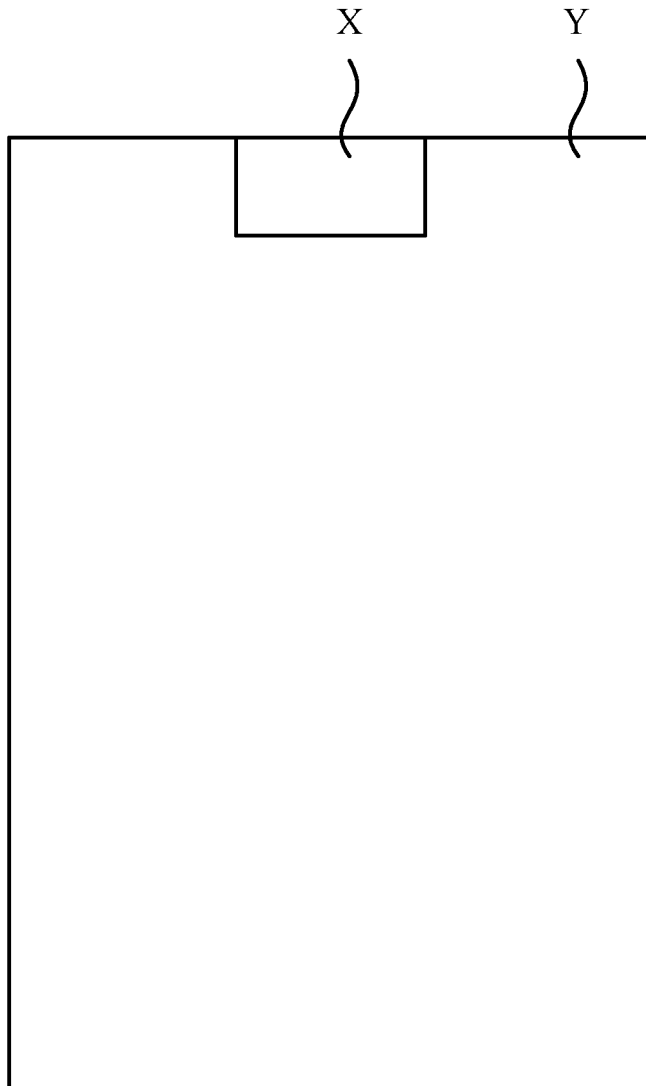


FIG. 6

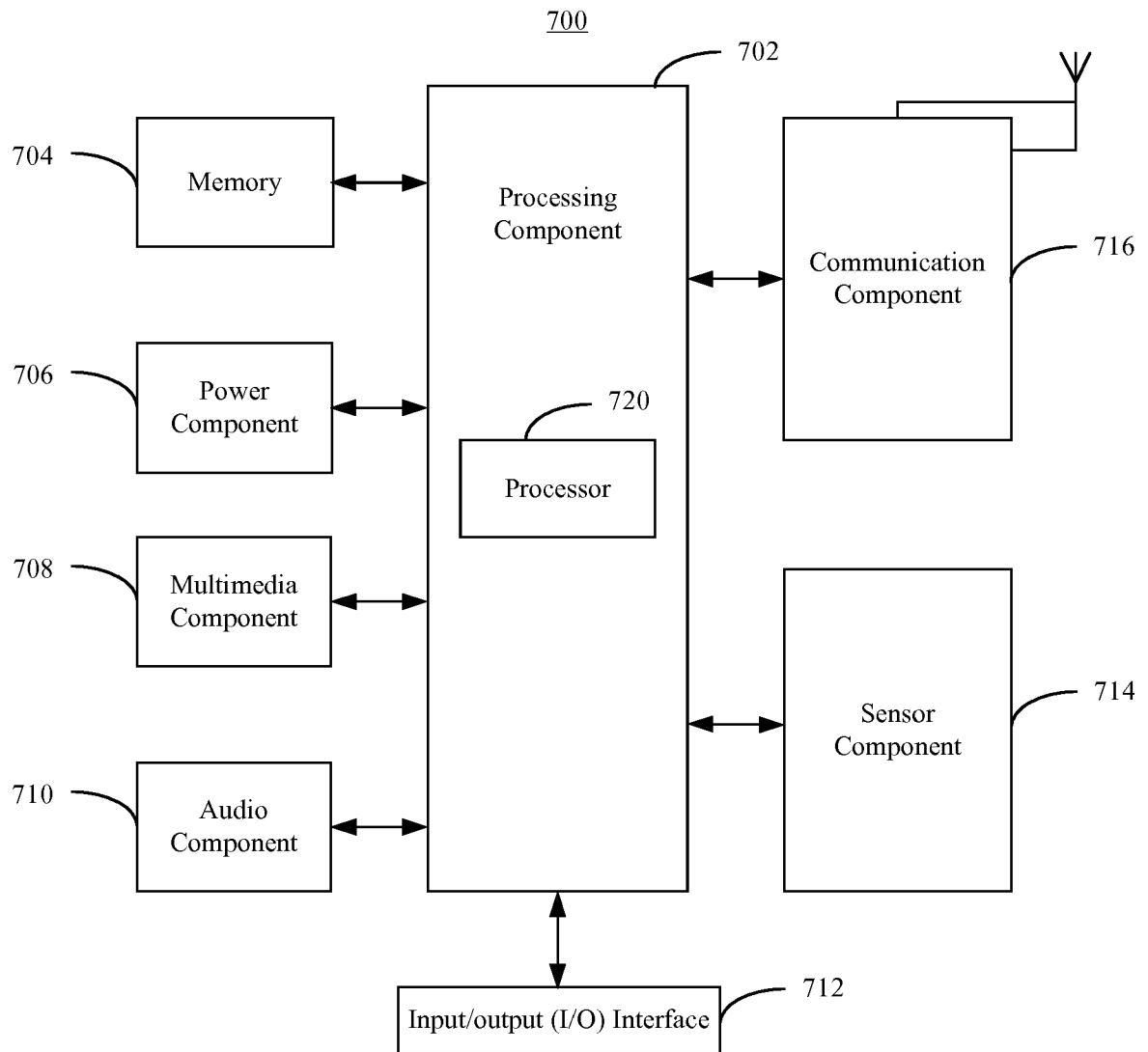


FIG. 7



EUROPEAN SEARCH REPORT

Application Number
EP 19 19 4871

5

10

15

20

25

30

35

40

45

50

55

| DOCUMENTS CONSIDERED TO BE RELEVANT | | | |
|--|---|--|--|
| Category | Citation of document with indication, where appropriate, of relevant passages | Relevant to claim | CLASSIFICATION OF THE APPLICATION (IPC) |
| X | US 2004/233140 A1 (JO HIROAKI [JP]) 25 November 2004 (2004-11-25) | 1-6,8-10 | INV. G09G3/3233 |
| Y | * paragraph [0049] - paragraph [0142]; figures 1-9 * | 11-15 | |
| X | EP 1 146 501 A1 (SEIKO EPSON CORP [JP]) 17 October 2001 (2001-10-17) | 1,5-7 | |
| Y | * paragraph [0078] - paragraph [0133]; figures 1-11 * | 11,12 | TECHNICAL FIELDS SEARCHED (IPC) G09G |
| Y | US 2008/165267 A1 (COK RONALD S [US]) 10 July 2008 (2008-07-10) | 11-15 | |
| | * paragraph [0051] - paragraph [0090]; figures 2A-18 * | | |
| The present search report has been drawn up for all claims | | | |
| Place of search Munich | | Date of completion of the search 29 January 2020 | Examiner Harke, Michael |
| CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document | | | |

EPO FORM 1503 03/02 (P04/C01)

ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

EP 19 19 4871

5 This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

29-01-2020

| Patent document cited in search report | Publication date | Patent family member(s) | Publication date |
|---|---------------------|----------------------------|---------------------|
| US 2004233140 A1 | 25-11-2004 | CN 1534555 A | 06-10-2004 |
| | | CN 101312010 A | 26-11-2008 |
| | | JP 4273809 B2 | 03-06-2009 |
| | | JP 2004302319 A | 28-10-2004 |
| | | KR 20040086173 A | 08-10-2004 |
| | | TW 1289922 B | 11-11-2007 |
| | | US 2004233140 A1 | 25-11-2004 |
| ----- | | | |
| EP 1146501 A1 | 17-10-2001 | CN 1340183 A | 13-03-2002 |
| | | EP 1146501 A1 | 17-10-2001 |
| | | JP 4061905 B2 | 19-03-2008 |
| | | KR 100433120 B1 | 27-05-2004 |
| | | TW 501080 B | 01-09-2002 |
| | | US 7180495 B1 | 20-02-2007 |
| | | WO 0129814 A1 | 26-04-2001 |
| ----- | | | |
| US 2008165267 A1 | 10-07-2008 | US 2008165267 A1 | 10-07-2008 |
| | | WO 2008085272 A1 | 17-07-2008 |
| ----- | | | |

| | | | |
|----------------|--|---------|------------|
| 专利名称(译) | 显示结构，具有其的显示板和具有其的显示装置 | | |
| 公开(公告)号 | EP3627486A1 | 公开(公告)日 | 2020-03-25 |
| 申请号 | EP2019194871 | 申请日 | 2019-09-02 |
| [标]申请(专利权)人(译) | 北京小米移动软件有限公司 | | |
| 申请(专利权)人(译) | 北京赛美移动软件有限公司 | | |
| 当前申请(专利权)人(译) | 北京赛美移动软件有限公司 | | |
| [标]发明人 | BIAN QINGFANG | | |
| 发明人 | BIAN, QINGFANG | | |
| IPC分类号 | G09G3/3233 | | |
| CPC分类号 | G09G3/3225 H01L27/3244 G09G3/3233 G09G2300/0443 G09G2300/0842 G09G3/2003 G09G3/3266 G09G2300/0408 G09G2300/0426 H01L27/124 H01L27/1255 H01L27/3218 H01L27/3234 H01L27/3248 H01L27/3262 H01L27/3265 H01L27/3276 | | |
| 审查员(译) | HARKE, 米迦勒 | | |
| 优先权 | 201811109264.5 2018-09-21 CN | | |
| 外部链接 | Espacenet | | |

摘要(译)

本公开涉及显示结构 (X)，包括该显示结构 (X) 的显示面板以及包括该显示面板和图像获取装置的显示装置。显示结构 (X) 包括布置在显示结构 (X) 的第一区域 (A) 中的多个像素，其中多个像素中的每个像素包括N个颜色的多个子像素，并且每个多个子像素中的一个子像素包括有机发光二极管 (OLED)。以及设置在显示结构 (X) 的第二区域 (B) 中的N个驱动电路，其中，N个驱动电路中的第i个驱动电路被配置为驱动多个像素中的第i个颜色的每个子像素子像素，其中 $1 \leq i \leq N$ ，N是大于1的整数。

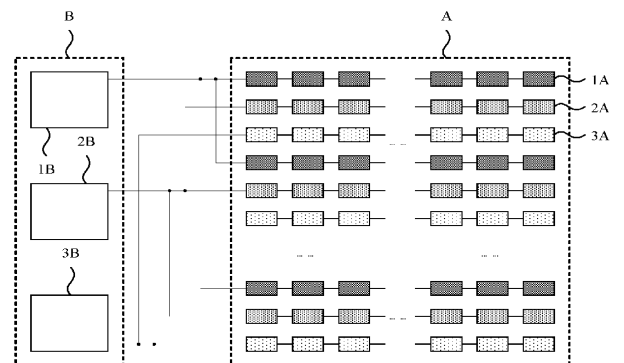


FIG. 1