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(54) **OLED DRIVE CIRCUIT AND MANUFACTURING METHOD THEREOF, AND DISPLAY DEVICE**

(57) There is provided an organic light-emitting diode drive circuit comprising a switch transistor (T_1), a drive transistor (T_2), a storage capacitor (C), and an organic light-emitting diode (OLED), wherein the switch transistor (T_1) uses an inorganic semiconductor transistor, and the

drive transistor (T_2) uses an organic semiconductor transistor. A display screen adopting the drive circuit as a unit pixel has the property of uniform brightness. In addition, there is provided a method of fabricating the drive circuit and a display device using the drive circuit.

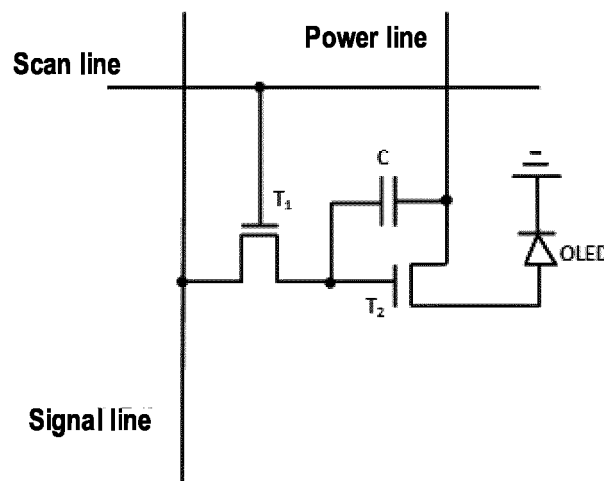


Fig.1

Description

TECHNICAL FIELD

[0001] The invention relates to an organic light-emitting diode (OLED) drive circuit, particularly an active-matrix driving organic light-emitting diode (AMOLED) drive circuit, a method of fabricating the same, and a display device using the same.

BACKGROUND OF THE INVENTION

[0002] Organic light-emitting diodes have advantages such as low driving voltage, high luminous brightness and luminous efficiency, fast response speed, wide work temperature range, simple forming process, adaptation to large-scale large-area processing, and good compatibility with flexible substrates, and thus have become core devices in new generation display devices. The driving modes of OLED can be categorized into passive-matrix driving and active-matrix driving. Among others, active-matrix driving OLEDs have advantages such as wide viewing angle, high brightness, and fast response speed. Active-matrix driving organic light-emitting diode-based display screens has been used for advanced mobile phones and televisions.

[0003] An AMOLED display screen is composed of several regions capable of independently emitting light, and each of the smallest regions capable of independently emitting light is referred to as a unit pixel. In the early stage of laboratory development, each unit pixel of an AMOLED includes at least one switch transistor T_1 , one drive transistor T_2 , one storage capacitor C , and one OLED, and the circuit structure formed by their interconnection is referred to as a 2T1C structure. 2T1C circuit has advantages such as simple structure, simple processing, and high yield, and thus is widely used in R&D stage.

[0004] However, OLEDs are current-type devices. To achieve the uniformity of display brightness of AMOLED display screens, it is necessary that the magnitude of the current flowing through each of pixels is as identical and stable as possible. 2T1C circuits are voltage-controlled circuits, so it is needed to convert input voltage signals to output current signals to drive and control OLEDs. In order to achieve the stability and uniformity of the output current signal, the conversion process presents more stringent requirements for the performance of transistor devices in AMOLED display screens.

[0005] Chinese patent CN1870113A and Chinese patent CN1411322A disclose 2T1C drive circuits for AMOLEDs, employing a silicon semiconductor transistor and an organic semiconductor transistor, respectively. However, the above patents do not solve the technical problem present in the current 2T1C circuits, i.e., the technical problem regarding the stability and uniformity of the conversion of voltage signals to current signals in each unit pixel in AMOLEDs.

[0006] Moreover, in the commercialization of AMOLEDs, the use of the current 2T1C circuit structures exhibits more apparant drawbacks, for example, particularly the failure of use in large-size display screens, such as computer monitors and televisions. Therefore, the use of 2T1C structures in the current AMOLED commercial products actually has been given up.

[0007] Mostly at the present, more complicated compensating circuits (such as multi-T1C circuits) are used to provide constant, uniform currents for OLEDs, in order to solve the technical problem present in the current 2T1C circuits. For example, Chinese patent CN1412854A provides a 3T1C drive circuit; Chinese patent CN103218970A even provides a more complicated 6T1C drive circuit, etc. However, these complicated circuits lead to complex production process, reduced yield, poor batch reliability, and high product costs.

[0008] Therefore, there is still a need to develop a new OLED drive circuit, which does not have the above-mentioned technical problem present in the current 2T1C circuits, and in particular, makes the display brightness of large size display screens uniform and is simple in production process.

SUMMARY OF THE INVENTION

[0009] In view of the technical problem that prior art AMOLED drive circuits are complicated (for example, multi-T1C) and 2T1C drive circuits cannot meet the need of AMOLED driving, the inventors have unexpectedly discovered that the new 2T1C drive circuit provided by the invention can be adopted to avoid many drawbacks such as complex production process caused by complicated drive circuits and overcome the technical problem present for prior art 2T1C drive circuits, and can ideally meet the need of AMOLED driving.

[0010] A first aspect of the invention provides an active-matrix driving organic light-emitting diode drive circuit comprising a switch transistor T_1 , a drive transistor T_2 , a storage capacitor C , and an organic light-emitting diode OLED, wherein the semiconductor layer of the the switch transistor T_1 is of an inorganic semiconductor material, and the semiconductor layer of the drive transistor T_2 is of an organic semiconductor material.

[0011] Preferably, the inorganic semiconductor material comprises, but is not limited to, any of polysilicon, microcrystalline silicon, amorphous silicon, or a compound semiconductor.

[0012] Preferably, the organic semiconductor material may be an organic small molecule semiconductor and a polymer semiconductor.

[0013] Preferably, the semiconductor layer of the drive transistor T_2 is fabricated by weak epitaxy growth.

[0014] Preferably, the switch transistor T_1 and the drive transistor T_2 transport different types of carriers (electrons or holes), respectively.

[0015] In the active-matrix driving organic light-emitting diode drive circuit, each of the switch transistor T_1

and the drive transistor T_2 have three electrodes, specifically including a gate, a first electrode, and a second electrode; the storage capacitor C has two electrodes, a first electrode and a second electrode; the OLED has two electrodes, a first electrode and a second electrode.

[0016] According to some embodiments, the active-matrix driving organic light-emitting diode drive circuit of the first aspect employs the first or the second connection mode as described below.

[0017] The first connection mode may be as follows: a gate of the switch transistor T_1 is connected with a scan line, a first electrode of the switch transistor T_1 is connected with a signal line, a second electrode of the switch transistor T_1 is connected with both a gate of the drive transistor T_2 and a first electrode of the storage capacitor C, a first electrode of the drive transistor T_2 is connected with both a second electrode of the storage capacitor C and a power line, and a second electrode of the drive transistor T_2 is connected with a first electrode of the OLED.

[0018] The second connection may be as follows: a gate of the switch transistor T_1 is connected with a scan line, a first electrode of the switch transistor T_1 is connected with a signal line, a second electrode of the switch transistor T_1 is connected with both a gate of the drive transistor T_2 and a first electrode of the storage capacitor C, a first electrode of the drive transistor T_2 is connected with a second electrode of the storage capacitor C, a second electrode of the drive transistor T_2 is connected with a first electrode of the OLED, and a second electrode of the OLED is connected with a power line.

[0019] A second aspect of the invention provides a method of fabricating drive circuits adopting the above connection modes.

[0020] The method for fabricating an organic light-emitting diode drive circuit adopting the first connection mode may comprise:

depositing one or more layers of a conductive metal or metal alloy on a glass substrate by magnetron sputtering; processing the conductive metal or metal alloy into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography; processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;

fabricating an inorganic semiconductor layer by methods such as magnetron sputtering or chemical vapor deposition or solution processing, and proceeding the inorganic semiconductor layer into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography;

depositing a layer of a conductive metal by magnetron sputtering, processing the conductive metal into a desired pattern as the signal line, the power line, the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C, and the gate of the drive transistor T_2

by mask lithography;

processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;

fabricating an organic semiconductor layer by weak epitaxy growth, processing the organic semiconductor layer into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography; fabricating a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and fabricating the conductive metal or metal alloy into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography;

fabricating a dielectric layer by methods such as magnetron sputtering or chemical vapor deposition or physical vapor deposition or solution processing, and processing the dielectric layer to form a via by mask photolithography, so as to connect the second electrode of the drive transistor T_2 with the first electrode of the OLED;

depositing a layer of a transparent conductive metal or semiconductor by techniques such as magnetron sputtering or solution processing, and processing the transparent conductive metal or semiconductor into a desired pattern as the first electrode of the OLED by mask lithography;

forming a semiconductor layer of the OLED by methods such as physical vapor deposition or solution processing or inkjet printing;

fabricating a layer of a conductive metal or metal alloy as the second electrode of the OLED by techniques such as physical vapor deposition or magnetron sputtering; and

integrally packaging the AMOLED display screen.

[0021] The method for fabricating an organic light-emitting diode drive circuit adopting the second connection mode may comprise:

depositing one or more layers of a conductive metal or metal alloy on a glass substrate by magnetron sputtering; processing the conductive metal or metal alloy into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography; processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;

fabricating an inorganic semiconductor layer by methods such as magnetron sputtering or chemical vapor deposition or solution processing, and proceeding the inorganic semiconductor layer into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography;

depositing a layer of a conductive metal by magnetron sputtering, processing the conductive metal into a desired pattern as the signal line, the power line,

the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C , and the gate of the drive transistor T_2 by mask lithography;

processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;

fabricating an organic semiconductor layer by weak epitaxy growth, processing the organic semiconductor layer into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography; fabricating a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and fabricating the conductive metal or metal alloy into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography; fabricating a dielectric layer by methods such as magnetron sputtering or chemical vapor deposition or physical vapor deposition or solution processing, and processing the dielectric layer to form a via by mask photolithography, so as to connect the second electrode of the drive transistor T_2 with the first electrode of the OLED;

depositing a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and processing the conductive metal or metal alloy into a desired pattern as the first electrode of the OLED by mask lithography;

forming a semiconductor layer of the OLED by methods such as physical vapor deposition or solution processing or inkjet printing;

depositing a layer of a transparent conductive metal or semiconductor as the second electrode of the OLED by techniques such as magnetron sputtering or physical vapor deposition; and integrally packaging the AMOLED display screen.

[0022] A third aspect of the invention further provides a display device comprising an AMOLED, wherein the above active-matrix driving organic light-emitting diode drive circuit is used as the unit pixel of the AMOLED.

[0023] The 2T1C circuit structure provided by the invention can effectively convert input voltage signals to stable and uniform current signals in a large size (for example, 370 mm × 470 mm) AMOLED display screen, such that the brightness of each of the unit pixels on the display screen is uniform. Furthermore, the use of the 2T1C structure provided by the invention can simplify production process, improve product yield and batch reliability, and reduce costs.

[0024] The above summary of the invention of the disclosure is not intended to describe each of the disclosed embodiments or every embodiment of the disclosure. More specific exemplary embodiments are described below. Therefore, it would be appreciated that the drawings

and the following description are for illustrative purpose only and do not improperly limit the scope of the present disclosure in a certain way.

5 BRIEF DESCRIPTION OF THE DRAWINGS

[0025]

Figure 1 is a schematic diagram illustrating the composition and connection of the first 2T1C drive circuit provided by the invention.

Figure 2 is a schematic diagram illustrating the composition and connection of the second 2T1C drive circuit provided by the invention.

DETAILED DESCRIPTION OF THE INVENTION

[0026] A first aspect of the invention provides an active-matrix driving organic light-emitting diode drive circuit comprising a switch transistor T_1 , a drive transistor T_2 , a storage capacitor C , and an organic light-emitting diode OLED, wherein a semiconductor layer of the the switch transistor T_1 is of an inorganic semiconductor material, and a semiconductor layer of the drive transistor T_2 is of an organic semiconductor material.

[0027] In the active-matrix driving organic light-emitting diode drive circuit, each of the switch transistor T_1 and the drive transistor T_2 has three electrodes, specifically including a gate, a first electrode, and a second electrode; the storage capacitor C has two electrodes, a first electrode and a second electrode; the OLED has two electrodes, a first electrode and a second electrode.

[0028] The inorganic semiconductor material constituting the switch transistor T_1 is not particularly limited in the invention. Preferably, examples of the inorganic semiconductor material useful for the semiconductor layer of the switch transistor T_1 of the invention comprises, but is not limited to, any of polysilicon, microcrystalline silicon, amorphous silicon, a compound semiconductor.

[0029] Unless otherwise indicated, the term "compound semiconductor" herein means that the semiconductor is an inorganic compound composed of two or more chemical elements, including, but not limited to, zinc oxide (ZnO), indium gallium zinc oxide (InGaZnO).

[0030] The organic semiconductor material constituting the drive transistor T_2 is not particularly limited in the invention. Preferably, the organic semiconductor material useful for the drive transistor T_2 of the invention may be an organic small molecule semiconductor and a polymer semiconductor. Preferably, examples of the organic semiconductor material useful for the drive transistor T_2 of the invention comprise, but are not limited to, rubrene, stannum dichlorine phthalocyanine, polythiophene (P3HT), etc.

[0031] Preferably, the semiconductor layer of the drive transistor T_2 of the invention is fabricated by weak epitaxy growth. While not being bound by any theory, it is believed that the organic semiconductor transistors fabri-

cated by weak epitaxy growth have good device uniformity and process reproducibility.

[0032] In the invention, "weak epitaxy growth" generally applies vacuum evaporation. An inducer layer is deposited on a substrate, and then an organic semiconductor material is deposited on the inducer layer, such that the π - π conjugation direction among organic semiconductor molecules is parallel to the surface of the substrate. In the meantime, a high-performance organic semiconductor layer is obtained by taking advantage of the epitaxial relationship or orientation relationship existing between the inducer layer lattices and the organic semiconductor lattices.

[0033] By applying the drive circuit of the first aspect of the present application, stable and uniform current signals are obtained. While not being bound by any theory, it is believed that this is because the two transistors, the switch transistor T_1 and the drive transistor T_2 , have different functions during the conversion of voltage signals to current signals by the 2T1C circuit, and thus the requirements on transistor performance are different. Transistors made of the same material cannot meet the different needs of both T_1 and T_2 . In the present application, two different materials are used for the switch transistor T_1 and the drive transistor T_2 , respectively. That is, the semiconductor layer of the switch transistor T_1 is of an inorganic semiconductor material, and the semiconductor layer of the drive transistor T_2 is of an organic semiconductor material. Therefore, the new 2T1C circuit of the present application can convert input voltage signals to stable and uniform current signals such that the brightness of each unit pixel on the display screen is uniform.

[0034] The new 2T1C circuit of the invention may allow the brightness of each cell pixel on a large size (e.g., 370mm $\times$ 470mm) AMOLED display screen to be uniform.

[0035] Preferably, the switch transistor T_1 and the drive transistor T_2 can transport different types of carriers (electrons or holes), respectively.

[0036] The invention further provides connection modes of the organic light-emitting diode drive circuit of the first aspect. See Figures 1 and 2 for details. In the drawings, T_1 denotes a switch transistor, T_2 denotes a drive transistor, C denotes a storage capacitor, and OLED denotes an organic light-emitting diode.

[0037] According to an embodiment, a first connection mode is shown in Figure 1: a gate of the switch transistor T_1 is connected with a scan line, a first electrode of the switch transistor T_1 is connected with a signal line, a second electrode of the switch transistor T_1 is connected with both a gate of the drive transistor T_2 and a first electrode of the storage capacitor C, a first electrode of the drive transistor T_2 is connected with both a second electrode of the storage capacitor C and a power line, and a second electrode of the drive transistor T_2 is connected with a first electrode of the OLED.

[0038] According to another embodiment, a second connection mode is shown in Figure 2: a gate of the switch

transistor T_1 is connected with a scan line, a first electrode of the switch transistor T_1 is connected with a signal line, a second electrode of the switch transistor T_1 is connected with both a gate of the drive transistor T_2 and a first electrode of the storage capacitor C, a first electrode of the drive transistor T_2 is connected with a second electrode of the storage capacitor C, a second electrode of the drive transistor T_2 is connected with a first electrode of the OLED, and a second electrode of the OLED is connected with a power line.

[0039] A second aspect of the invention further provides a method of fabricating the above organic light-emitting diode drive circuits.

[0040] For the first connection mode above, the following method can be used for fabrication:

- (1) depositing one or more layers of a conductive metal or metal alloy on a glass substrate by magnetron sputtering; processing the conductive metal or metal alloy into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography;
- (2) processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;
- (3) fabricating an inorganic semiconductor layer by methods such as magnetron sputtering or chemical vapor deposition or solution processing, and proceeding the inorganic semiconductor layer into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography;
- (4) depositing a layer of a conductive metal by magnetron sputtering, processing the conductive metal into a desired pattern as the signal line, the power line, the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C, and the gate of the drive transistor T_2 by mask lithography;
- (5) processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;
- (6) fabricating an organic semiconductor layer by weak epitaxy growth, processing the organic semiconductor layer into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography;
- (7) fabricating a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and fabricating the conductive metal or metal alloy into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography;
- (8) fabricating a dielectric layer by methods such as magnetron sputtering or chemical vapor deposition or physical vapor deposition or solution processing, and processing the dielectric layer to form a via by mask photolithography, so as to connect the second

electrode of the drive transistor T_2 with the first electrode of the OLED;

(9) depositing a layer of a transparent conductive metal or semiconductor by techniques such as magnetron sputtering or solution processing, and processing the transparent conductive metal or semiconductor into a desired pattern as the first electrode of the OLED by mask lithography;

(10) forming a semiconductor layer of the OLED by methods such as physical vapor deposition or solution processing or inkjet printing;

(11) fabricating a layer of a conductive metal or metal alloy as the second electrode of the OLED by techniques such as physical vapor deposition or magnetron sputtering; and

(12) integrally packaging the AMOLED display screen.

[0041] For the second connection mode above, the following method can be used for fabrication:

(1) depositing one or more layers of a conductive metal or metal alloy on a glass substrate by magnetron sputtering; processing the conductive metal or metal alloy into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography;

(2) processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;

(3) fabricating an inorganic semiconductor layer by methods such as magnetron sputtering or chemical vapor deposition or solution processing, and proceeding the inorganic semiconductor layer into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography;

(4) depositing a layer of a conductive metal by magnetron sputtering, processing the conductive metal into a desired pattern as the signal line, the power line, the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C, and the gate of the drive transistor T_2 by mask lithography;

(5) processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography;

(6) fabricating an organic semiconductor layer by weak epitaxy growth, processing the organic semiconductor layer into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography;

(7) fabricating a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and fabricating the conductive metal or metal alloy into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography;

(8) fabricating a dielectric layer by methods such as magnetron sputtering or chemical vapor deposition or physical vapor deposition or solution processing, and processing the dielectric layer to form a via by mask photolithography, so as to connect the second electrode of the drive transistor T_2 with the first electrode of the OLED;

(9) depositing a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and processing the conductive metal or metal alloy into a desired pattern as the first electrode of the OLED by mask lithography;

(10) forming a semiconductor layer of the OLED by methods such as physical vapor deposition or solution processing or inkjet printing;

(11) depositing a layer of a transparent conductive metal or semiconductor as the second electrode of the OLED by techniques such as magnetron sputtering or physical vapor deposition; and

(12) integrally packaging the AMOLED display screen.

[0042] The methods of fabricating the organic light-emitting diode drive circuit is not limited to the methods introduced above. Any method known in the art that can be used to fabricate an organic light-emitting diode drive circuit can be employed.

[0043] The invention further provides a display device comprising an AMOLED, wherein the above active-matrix driving organic light-emitting diode drive circuit is used as the unit pixel of the AMOLED. The term "unit pixel" herein means that an AMOLED display screen is composed of several regions capable of independently emitting light, and each of the smallest regions capable of independently emitting light is referred to as a unit pixel.

[0044] In certain embodiments, the display device provided by the invention is a large size display device. In the invention, the "large size" display device means a size of, for example, larger than 370 mm × 470 mm, including, for example, but not limited to, a computer monitor, a television set, etc. The "large size" herein means that the size of a substrate is not less than 370 mm × 470 mm. A substrate may be used as a display screen, e.g., a display screen for computers and televisions, or, as needed, split into several or even dozens of small display screens, e.g., display screens for mobile phones and tablet computers).

EXAMPLES

[0045] The invention will be further described in detail below in conjunction with the drawings and specific examples in order for those skilled in the art to better understand the invention. Unless otherwise indicated, all the contents, proportions, and parts in the description are expressed by weight, and all the units are International Standard Units.

[0046] The materials and test equipment used in the invention are as follows:

Materials

[0047] The metals and inorganic semiconductors used were provided by Truly Semiconductor;

[0048] The organic semiconductors used were purchased from Jilin OLED company.

Test Equipment

[0049] Two-dimensional color brightness meter: CA2000, Minolta.

Brightness test procedure

[0050] A sample was connected with a peripheral test circuit to make the sample display a single color. The sample was integrally sampled by using CA2000 brightness meter, and the collected data was analyzed by the software of the brightness meter to obtain the brightness at different positions of the sample.

Example 1:

[0051] For the 2T1C circuit shown in Figure 1, the specific fabrication process was as follows:

(1) Molybdenum-aluminum-molybdenum was sequentially deposited on a glass substrate by magnetron sputtering; the molybdenum-aluminum-molybdenum was processed into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography. The vacuum degree was 10^{-2} Pa, the flow rate of argon gas was 200 sccm, the sputtering rate was 300 nm/min, and the thicknesses of the metal layers were 50 nm, 150 nm, and 50 nm, respectively;

(2) A silicon nitride insulating layer was processed by chemical vapor deposition, wherein the vacuum degree was 266 Pa, the flow rate of silane was 250 sccm, the flow rate of ammonia gas was 1550 sccm, the flow rate of nitrogen gas was 1550 sccm, the flow rate of hydrogen gas was 250 sccm, the power was 1800 W, the film forming rate was 380 nm/min. the film thickness was 350 nm; a via was formed on the silicon nitride insulating layer by mask lithography;

(3) An amorphous silicon semiconductor layer was fabricated by plasma chemical vapor deposition, wherein the radio frequency was 13.56 MHz, the vacuum degree was 60-120 Pa, the substrate temperature was 250 °C, the flow rate of silane was 160 sccm, the film forming rate was 20 nm/min, and the film thickness was 200 nm; the amorphous silicon semiconductor layer was processed into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography, wherein the amor-

phous silicon can transport electrons;

(4) A layer of metal molybdenum was deposited by magnetron sputtering, wherein the vacuum degree was 10^{-2} Pa, the flow rate of argon gas was 200 sccm, the sputtering rate was 300 nm/min, and the film thickness was 250 nm; the metal molybdenum was processed into a desired pattern as the signal line, the power line, the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C, and the gate of the drive transistor T_2 by mask lithography;

(5) A silicon nitride insulating layer was processed by chemical vapor deposition, wherein the vacuum degree was 266 Pa, the flow rate of silane was 250 sccm, the flow rate of ammonia gas was 1550 sccm, the flow rate of nitrogen gas was 1550 sccm, the flow rate of hydrogen gas was 250 sccm, the power was 1800 W, the film forming rate was 380 nm/min, and the film thickness was 200 nm; a via was formed on the silicon nitride insulating layer by mask lithography;

(6) A rubrene organic semiconductor layer was fabricated by weak epitaxy growth using hexaphenyl as an inducer layer, wherein the vacuum degree was 8×10^{-4} Pa, the substrate temperature was 180 °C, the deposition rate was 1 nm/min, the thickness of hexaphenyl was 5 nm, and the thickness of rubrene was 20 nm; the rubrene organic semiconductor layer was processed into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography, wherein the rubrene can transport cavities;

(7) A layer of metal gold was fabricated by vacuum vapor deposition, wherein the vacuum degree was 8×10^{-4} Pa, the deposition rate was 50 nm/min, and the thickness was 100 nm; the metal gold was processed into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography;

(8) A silicon nitride dielectric layer was fabricated by chemical vapor deposition, wherein the vacuum degree was 266 Pa, the flow rate of silane was 250 seem, the flow rate of ammonia gas was 1550 sccm, the flow rate of nitrogen gas was 1550 sccm, the flow rate of hydrogen gas was 250 sccm, the power was 1800 W, the film forming rate was 380 nm/min, and the film thickness was 200 nm; the silicon nitride dielectric layer was processed to form a via by mask photolithography, so as to connect the second electrode of the drive transistor T_2 with the first electrode of the OLED;

(9) A layer of indium tin oxide (ITO) was deposited by magnetron sputtering, wherein the vacuum degree was 10^{-2} Pa, the flow rate of carrier gas was 200 sccm, the sputtering rate was 150 nm/min, and the thickness was 75 nm; the ITO was processed into a desired pattern as the first electrode of the

OLED by mask lithography;

(10) N,N'-diphenyl-N,N'-(1-naphthyl)-1,1'-biphenyl-4,4'-diamine (NPB) and 8-hydroxyquinoline aluminum (Alq3) were processed by physical vapor deposition to form the semiconductor layer of the OLED, wherein the vacuum degree was 8×10^{-4} Pa, the deposition rate was 10 nm/min, the thickness of NPB was 40 nm, and the thickness of Alq3 was 50 nm;

(11) A layer of metal aluminum was fabricated as the second electrode of the OLED by physical vapor deposition, wherein the vacuum degree was 8×10^{-4} Pa, the deposition rate was 50 nm/min, and the thickness was 100 nm;

(12) The AMOLED display screen was integrally packaged.

[0052] Among the 5 batches of products, 5 samples were randomly selected from each batch, and each sample was $370 \text{ mm} \times 470 \text{ mm}$ in size. The peak brightness was $400 \pm 23 \text{ cd/m}^2$, and the number of dead pixels per sample was not more than three. The samples were evenly divided into 9 regions by 3×3 . Each of the regions was tested for brightness. When the average brightness was 220 cd/m^2 , the error of the brightness of the regions was less than 7%. After 10,000 iterations of turning on and off, the brightness was not changed.

[0053] The test results showed that the batch stability, the process reliability, and the stability and uniformity of display brightness of the display screen products using the AMOLEDs based on the 2T1C circuits provided by the invention satisfied the requirements of current commercial products.

Example 2

[0054] For the 2T1C circuit shown in Figure 2, the specific fabrication process was as follows:

(1) A layer of metal molybdenum was deposited on a glass substrate by magnetron sputtering, wherein the vacuum degree was 10^{-2} Pa, the flow rate of argon gas was 200 sccm, the sputtering rate was 300 nm/min, and the thicknesses were 300 nm; the metal molybdenum was processed into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography;

(2) A silicon nitride insulating layer was processed by chemical vapor deposition, wherein the vacuum degree was 266 Pa, the flow rate of silane was 250 sccm, the flow rate of ammonia gas was 1550 sccm, the flow rate of nitrogen gas was 1550 sccm, the flow rate of hydrogen gas was 250 sccm, the power was 1800 W, the film forming rate was 380 nm/min. the film thickness was 350 nm; a via was formed on the silicon nitride insulating layer by mask lithography;

(3) An amorphous silicon semiconductor layer was fabricated by plasma chemical vapor deposition, wherein the radio frequency was 13.56 MHz, the vac-

uum degree was 60-120 Pa, the substrate temperature was 250°C , the flow rate of silane was 160 sccm, the film forming rate was 20 nm/min, and the film thickness was 200 nm; the amorphous silicon was converted to polycrystalline silicon by laser scanning annealing, wherein the substrate temperature was 435°C ; the amorphous silicon semiconductor layer was processed into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography, wherein the amorphous silicon can transport cavities;

(4) A layer of metal molybdenum was deposited by magnetron sputtering, wherein the vacuum degree was 10^{-2} Pa, the flow rate of argon gas was 200 sccm, the sputtering rate was 300 nm/min, and the thickness was 250 nm; the metal molybdenum was processed into a desired pattern as the signal line, the power line, the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C, and the gate of the drive transistor T_2 by mask lithography;

(5) A silicon nitride insulating layer was processed by chemical vapor deposition, wherein the vacuum degree was 266 Pa, the flow rate of silane was 250 sccm, the flow rate of ammonia gas was 1550 sccm, the flow rate of nitrogen gas was 1550 sccm, the flow rate of hydrogen gas was 250 sccm, the power was 1800 W, the film forming rate was 380 nm/min, and the film thickness was 200 nm; a via was formed on the silicon nitride insulating layer by mask lithography;

(6) A stannum dichlorine phthalocyanine organic semiconductor layer was fabricated by weak epitaxy growth using hexaphenyl as an inducer layer, wherein the vacuum degree was 8×10^{-4} Pa, the substrate temperature was 180°C , the deposition rate was 1 nm/min, the thickness of hexaphenyl was 5 nm, and the thickness of stannum dichlorine phthalocyanine was 30 nm; it was processed into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography, wherein the stannum dichlorine phthalocyanine can transport electrons;

(7) A layer of metal silver was fabricated by physical vapor deposition, wherein the vacuum degree was 8×10^{-4} Pa, the deposition rate was 50 nm/min, and the thickness was 100 nm; the metal silver was processed into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography;

(8) A silicon nitride dielectric layer was fabricated by chemical vapor deposition, wherein the vacuum degree was 266 Pa, the flow rate of silane was 250 seem, the flow rate of ammonia gas was 1550 sccm, the flow rate of nitrogen gas was 1550 sccm, the flow rate of hydrogen gas was 250 sccm, the power was 1800 W, the film forming rate was 380 nm/min, and the film thickness was 200 nm; the silicon nitride di-

electric layer was processed to form a via by mask photolithography, so as to connect the second electrode of the drive transistor T_2 with the first electrode of the OLED;

(9) A layer of metal aluminum was deposited by magnetron sputtering, wherein the vacuum degree was 10^{-2} Pa, the flow rate of argon gas was 200 sccm, the sputtering rate was 300 nm/min, and the thickness was 300 nm; the metal aluminum was processed into a desired pattern as the first electrode of the OLED by mask lithography;

(10) N,N'-diphenyl-N,N'-(1-naphthyl)1,1'-biphenyl-4,4'-diamine (NPB) and 8-hydroxyquinoline aluminum (Alq3) were processed by mask physical vapor deposition to form the semiconductor layer of the OLED, wherein the vacuum degree was 8×10^{-4} Pa, the deposition rate was 10 nm/min, the thickness of NPB was 40 nm, and the thickness of Alq3 was 50 nm;

(11) A layer of metal aluminum doped zinc oxide was fabricated as the second electrode of the OLED by magnetron sputtering, wherein the vacuum degree was 10^{-2} Pa, the flow rate of argon gas was 200 sccm, the sputtering rate was 150 nm/min, and the thickness was 80 nm;

(12) The AMOLED display screen was integrally packaged.

[0055] Among the 5 batches of products, 5 samples were randomly selected from each batch, and each sample was $370 \text{ mm} \times 470 \text{ mm}$ in size. The peak brightness was $400 \pm 18 \text{ cd/m}^2$, and the number of dead pixels per sample was not more than three. The samples were evenly divided into 9 regions by 3×3 . Each of the regions was tested for brightness. When the average brightness was 220 cd/m^2 , the error of the brightness of the regions was less than 6.7%. After 10,000 iterations of turning on and off, the brightness was not changed.

[0056] The test results showed that the batch stability, the process reliability, and the stability and uniformity of display brightness of the display screen products using the AMOLEDs based on the 2T1C circuits provided by the invention satisfied the requirements of current commercial products.

[0057] The foreseeable variations and modifications of the invention will be obvious to those skilled in the art without departing from the scope and spirit of the invention. For illustrative purposes, the invention should not be limited to the examples set forth in this patent application. The present specification shall prevail in the event of a conflict or inconsistency between the present specification and the disclosure of any document incorporated herein by reference.

Claims

1. An organic light-emitting diode drive circuit compris-

ing a switch transistor, a drive transistor, a storage capacitor, and an organic light-emitting diode, wherein a semiconductor layer of the the switch transistor uses an inorganic semiconductor material, and a semiconductor layer of the drive transistor uses an organic semiconductor material.

2. The organic light-emitting diode drive circuit according to claim 1, wherein the inorganic semiconductor material is selected from any of polysilicon, microcrystalline silicon, amorphous silicon, or a compound semiconductor.
3. The organic light-emitting diode drive circuit according to claim 2, wherein the compound semiconductor includes zinc oxide and indium gallium zinc oxide.
4. The organic light-emitting diode drive circuit according to claim 1, wherein the organic semiconductor material includes an organic small molecule semiconductor and a polymer semiconductor.
5. The organic light-emitting diode drive circuit according to claim 1, wherein the semiconductor layer of the drive transistor is fabricated by weak epitaxy growth.
6. The organic light-emitting diode drive circuit according to claim 1, wherein the switch transistor and the drive transistor can transport different types of carriers respectively.
7. The organic light-emitting diode drive circuit according to any of claims 1 to 6, adopting a connection mode as follows: a gate of the switch transistor is connected with a scan line, a first electrode of the switch transistor is connected with a signal line, a second electrode of the switch transistor is connected with both a gate of the drive transistor and a first electrode of the storage capacitor, a first electrode of the drive transistor is connected with both a second electrode of the storage capacitor and a power line, and a second electrode of the drive transistor is connected with a first electrode of the organic light-emitting diode.
8. The organic light-emitting diode drive circuit according to any of claims 1 to 6, adopting a connection mode as follows: a gate of the switch transistor is connected with a scan line, a first electrode of the switch transistor is connected with a signal line, a second electrode of the switch transistor is connected with both a gate of the drive transistor and a first electrode of the storage capacitor, a first electrode of the drive transistor is connected with a second electrode of the storage capacitor, a second electrode of the drive transistor is connected with a first electrode of the organic light-emitting diode, and a

second electrode of the organic light-emitting diode is connected with a power line.

9. A method for fabricating the organic light-emitting diode drive circuit according to claim 7, comprising:

depositing one or more layers of a conductive metal or metal alloy on a glass substrate by magnetron sputtering; processing the conductive metal or metal alloy into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography; processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography; fabricating an inorganic semiconductor layer by methods such as magnetron sputtering or chemical vapor deposition or solution processing, and proceeding the inorganic semiconductor layer into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography; depositing a layer of a conductive metal by magnetron sputtering, processing the conductive metal into a desired pattern as the signal line, the power line, the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C, and the gate of the drive transistor T_2 by mask lithography; processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography; fabricating an organic semiconductor layer by weak epitaxy growth, processing the organic semiconductor layer into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography; fabricating a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and fabricating the conductive metal or metal alloy into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography; fabricating a dielectric layer by methods such as magnetron sputtering or chemical vapor deposition or physical vapor deposition or solution processing, and processing the dielectric layer to form a via by mask photolithography, so as to connect the second electrode of the drive transistor T_2 with the first electrode of the OLED; depositing a layer of a transparent conductive metal or semiconductor by techniques such as magnetron sputtering or solution processing, and processing the transparent conductive metal or semiconductor into a desired pattern as the

first electrode of the OLED by mask lithography; forming a semiconductor layer of the OLED by methods such as physical vapor deposition or solution processing or inkjet printing; fabricating a layer of a conductive metal or metal alloy as the second electrode of the OLED by techniques such as physical vapor deposition or magnetron sputtering; and integrally packaging the AMOLED display screen.

10. A method for fabricating the organic light-emitting diode drive circuit according to claim 8, comprising:

depositing one or more layers of a conductive metal or metal alloy on a glass substrate by magnetron sputtering; processing the conductive metal or metal alloy into a desired pattern as the scan line and the gate of the switch transistor T_1 by mask lithography; processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography; fabricating an inorganic semiconductor layer by methods such as magnetron sputtering or chemical vapor deposition or solution processing, and proceeding the inorganic semiconductor layer into a desired pattern as the semiconductor layer of the switch transistor T_1 by mask lithography; depositing a layer of a conductive metal by magnetron sputtering, processing the conductive metal into a desired pattern as the signal line, the power line, the first electrode and the second electrode of the switch transistor T_1 , the first electrode of the storage capacitor C, and the gate of the drive transistor T_2 by mask lithography; processing an insulating layer by magnetron sputtering or chemical vapor deposition, and forming a via by mask lithography; fabricating an organic semiconductor layer by weak epitaxy growth, processing the organic semiconductor layer into a desired pattern as the semiconductor layer of the drive transistor T_2 by mask lithography; fabricating a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and fabricating the conductive metal or metal alloy into a desired pattern as the first electrode and the second electrode of the drive transistor T_2 and the second electrode of the storage capacitor C by mask lithography; fabricating a dielectric layer by methods such as magnetron sputtering or chemical vapor deposition or physical vapor deposition or solution processing, and processing the dielectric layer

to form a via by mask photolithography, so as
to connect the second electrode of the drive transistor T_2 with the first electrode of the OLED;
depositing a layer of a conductive metal or metal alloy by techniques such as physical vapor deposition or magnetron sputtering, and processing
the conductive metal or metal alloy into a desired pattern as the first electrode of the OLED by mask lithography;
forming a semiconductor layer of the OLED by methods such as physical vapor deposition or solution processing or inkjet printing;
depositing a layer of a transparent conductive metal or semiconductor as the second electrode of the OLED by techniques such as magnetron sputtering or physical vapor deposition; and
integrally packaging the AMOLED display screen.

11. A display device comprising the organic light-emitting diode drive circuit according to any of claims 1-8 as a unit pixel.

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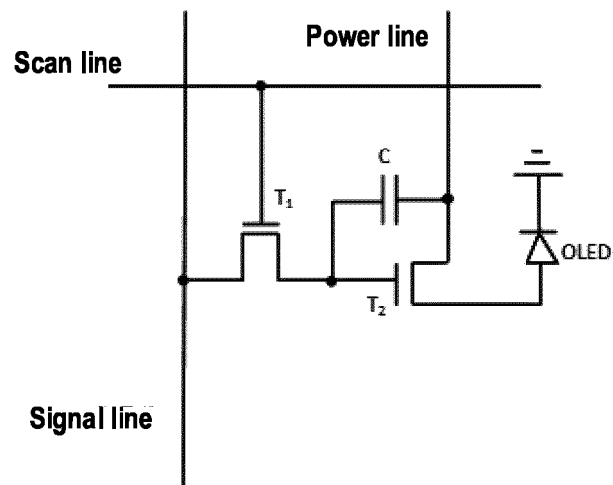


Fig.1

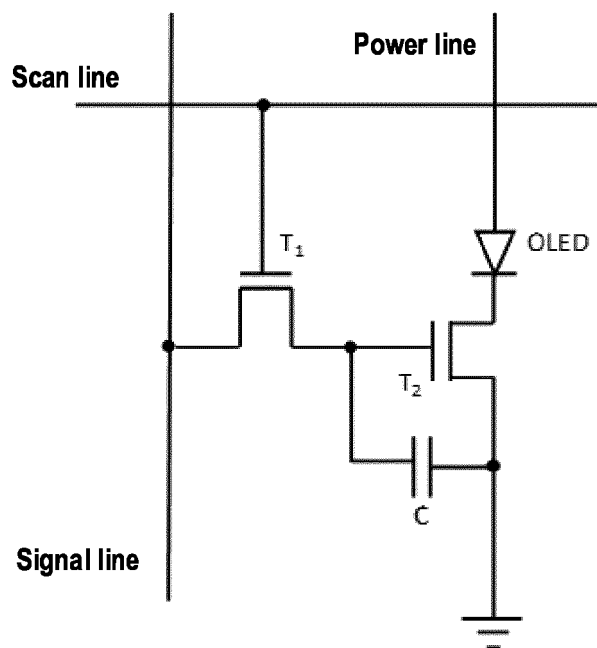


Fig.2

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2016/084505

A. CLASSIFICATION OF SUBJECT MATTER

G09G 3/32 (2016.01) i; H01L 29/786 (2006.01) i
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G09G, H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNPAT, EPODOC, WPI, CNKI, IEEE: organic light emitting diode, inorganic semiconductor, organic semiconductor, pixel, organic light emitting, OLED?, organic electroluminescen+, organic EL, switch+, inorganic, silicon, Si, driv+, organic, channel?, active

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2010001284 A1 (SAMSUNG ELECTRONICS CO., LTD.), 07 January 2010 (07.01.2010), description, paragraphs [0003]-[0006], [0024]-[0035], [0037] and [0045]-[0054], and figures 1-3	1-11
Y	CN 102017209 A (CAMBRIDGE DISPLAY TECHNOLOGY LTD. et al.), 13 April 2011 (13.04.2011), description, paragraphs [0090]-[0093]	1-11
Y	CN 104409047 A (HEFEI XINSHENG OPTOELECTRONICS TECHNOLOGY CO., LTD. et al.), 11 March 2015 (11.03.2015), description, paragraph [0003], and figure 1	8, 10
Y	CN 1411322 A (CHANGCHUN INSTITUTE OF APPLIED CHEMISTRY, CHINESE ACADEMY OF SCIENCES), 16 April 2003 (16.04.2003), description, page 2, paragraphs 4-5, and figure 2	1-11
A	CN 104332485 A (APPLE INC.), 04 February 2015 (04.02.2015), the whole document	1-11
A	CN 102867481 A (CPTF VISUAL DISPLAY (FUZHOU) LTD. et al.), 09 January 2013 (09.01.2013), the whole document	1-11

☒ Further documents are listed in the continuation of Box C. ☒ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	
"E" earlier application or patent but published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	"&" document member of the same patent family

Date of the actual completion of the international search 16 January 2017 (16.01.2017)	Date of mailing of the international search report 07 February 2017 (07.02.2017)
Name and mailing address of the ISA/CN: State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No.: (86-10) 62019451	Authorized officer YANG, Yan Telephone No.: (86-10) 62414051

Form PCT/ISA/210 (second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2016/084505

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2009267075 A1 (INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE), 29 October 2009 (29.10.2009), the whole document	1-11

Form PCT/ISA/210 (continuation of second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2016/084505

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US 2010001284 A1	07 January 2010	US 7846787 B2	07 December 2010
		KR 20100003612 A	11 January 2010
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		WO 2016048385 A1	31 March 2016
CN 102867481 A	09 January 2013	CN 102867481 B	04 May 2016
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Form PCT/ISA/210 (patent family annex) (July 2009)

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

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- CN 1412854 A [0007]
- CN 103218970 A [0007]

专利名称(译)	OLED驱动电路及其制造方法和显示装置		
公开(公告)号	EP3467812A1	公开(公告)日	2019-04-10
申请号	EP2016903526	申请日	2016-06-02
[标]申请(专利权)人(译)	长春富乐玻显示技术有限公司		
申请(专利权)人(译)	长春市柔性显示技术有限公司		
当前申请(专利权)人(译)	长春市柔性显示技术有限公司		
[标]发明人	GUO XIAODONG		
发明人	GUO, XIAODONG		
IPC分类号	G09G3/32 H01L29/786		
CPC分类号	G09G3/3225 G09G2300/0426 G09G3/32 H01L27/3274 H01L27/286 H01L29/786 G09G3/3233 G09G3/3266 G09G2300/0809 G09G2320/0233 H01L27/1225 H01L27/1288 H01L27/3276 H01L29/78663 H01L29/7869 H01L51/0512		
代理机构(译)	柜PLASSERAUD		
其他公开文献	EP3467812A4		
外部链接	Espacenet		

摘要(译)

提供一种有机发光二极管驱动电路，包括开关晶体管（T₁），驱动晶体管（T₂），存储电容器（C）和有机发光二极管（OLED），其中开关晶体管（T₁）使用无机半导体晶体管，驱动晶体管（T₂）使用有机半导体晶体管。采用驱动电路作为单位像素的显示屏具有均匀亮度的特性。另外，提供了一种制造驱动电路的方法和使用该驱动电路的显示装置。

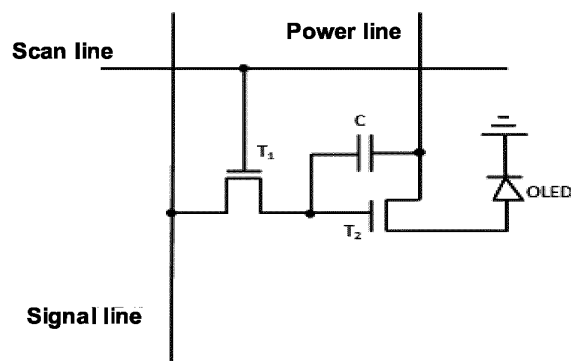


Fig.1