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(72) Inventors:
 • **Lee, Hyunjung Gyeonggi-do (KR)**
 • **Chung, Kyunghoon Gyeonggi-do (KR)**

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(74) Representative: **Hengelhaupt, Jürgen et al Gulde Hengelhaupt Ziebig & Schneider Patentanwälte - Rechtsanwälte Wallstrasse 58/59 10179 Berlin (DE)**

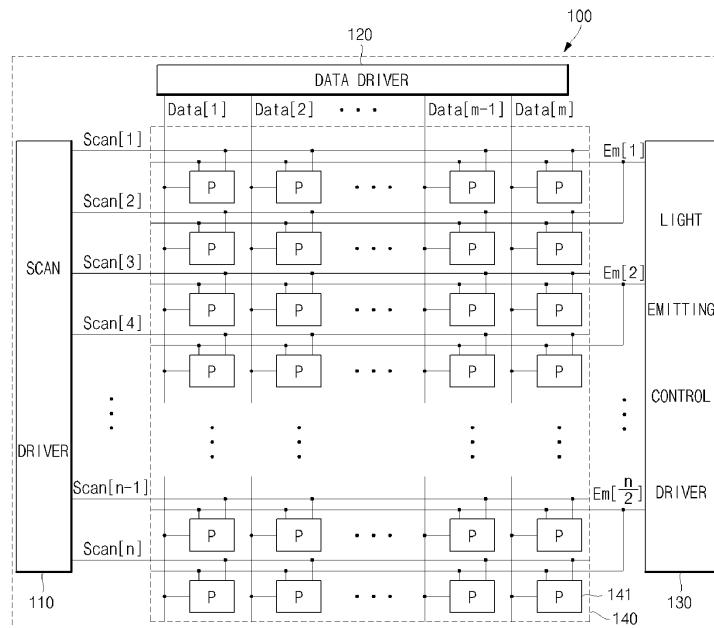
(71) Applicant: **Samsung SDI Co., Ltd. Suwon-si Gyeonggi-do (KR)**

(54) **Organic light emitting display and driving circuit thereof**

(57) A driving circuit including a plurality of light emitting control drivers includes an input terminal coupled to an initial driving line or a light emitting negative control line of a previous light emitting control driver, a first clock terminal and a second clock terminal that are electrically coupled to a first clock line and a first negative clock line

that are phase-inverted, or a second clock line and a second negative clock line, respectively, and an output terminal and a negative output terminal adapted to generate an output signal and a negative output signal when receiving an input signal, a clock signal and a negative clock signal via the input terminal, the first clock terminal and the second clock terminal, respectively.

Fig.1





EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	WO 2005/122178 A (SEMICONDUCTOR ENERGY LAB [JP]; OSAME MITSUAKI [JP]; ANZAI AYA [JP]) 22 December 2005 (2005-12-22) * abstract * * page 3, line 21 - page 16, line 17; figures 1,3 * * page 20 - page 21; figure 10 * -----	1-12	INV. G09G3/32
A	US 5 568 163 A (OKUMURA FUJIO [JP]) 22 October 1996 (1996-10-22) * abstract * * column 3, line 36 - column 4, line 44; claims 1,2,7,8; figures 4,6 * -----	1-12	
			TECHNICAL FIELDS SEARCHED (IPC)
			G09G G11C
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 8 September 2008	Examiner Wolff, Lilian
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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EPO FORM 1503 03.82 (P04C01)



Application Number

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CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing claims for which payment was due.

- Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s):
- No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet B

- All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.
- Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
- None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:
1-12
- The present supplementary European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims (Rule 164 (1) EPC).



**LACK OF UNITY OF INVENTION
SHEET B**

Application Number
EP 08 10 1553

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-12

First invention

2. claims: 13-32

Second invention

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 08 10 1553

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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08-09-2008

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 2005122178 A	22-12-2005	CN 1969341 A	23-05-2007
		EP 1756834 A1	28-02-2007
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		US 2008273004 A1	06-11-2008

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		JP 7072830 A	17-03-1995

EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	有机发光显示器及其驱动电路		
公开(公告)号	EP1965370A3	公开(公告)日	2009-01-28
申请号	EP2008101553	申请日	2008-02-13
[标]申请(专利权)人(译)	三星斯笛爱股份有限公司		
申请(专利权)人(译)	三星SDI CO., LTD.		
当前申请(专利权)人(译)	三星DISPLAY CO., LTD.		
[标]发明人	LEE HYUNJUNG CHUNG KYUNGHOON		
发明人	LEE, HYUNJUNG CHUNG, KYUNGHOON		
IPC分类号	G09G3/32		
CPC分类号	G09G3/3208 G09G3/3225 G09G3/3266 G09G2310/021 G09G2310/0286 G09G2330/021		
代理机构(译)	hengelhaupt, Jürgen		
优先权	1020070020737 2007-03-02 KR		
其他公开文献	EP1965370B1 EP1965370A2		
外部链接	Espacenet		

摘要(译)

包括多个发光控制驱动器的驱动电路，包括耦合到先前发光控制驱动器的初始驱动线或发光负控制线的输入端子，电耦合到第一时钟端子和第二时钟端子的第一时钟端子和第二时钟端子。分别反相的第一时钟线和第一负时钟线，或者第二时钟线和第二负时钟线，以及适于产生输出信号和负输出信号的输出端和负输出端当分别通过输入端，第一时钟端和第二时钟端接收输入信号，时钟信号和负时钟信号时。

