

**(S7) Abstract:** A light emitting display device using a drive circuit formed of only unipolar thin film transistors, which suppresses effects of characteristic shifts of transistors, and is applicable to large, high-resolution light emitting displays. The device includes a pixel having an organic EL device (LED) and a drive circuit thereof. In a current writing period, the drive circuit sets TFT3, TFT4 and TFT5 ON and sets a ground line and one end of LED to the same voltage through TFT3. A current from a data line is supplied to transistors L-TFT and D-TFT forming a current mirror circuit through TFT4 and TFT5, and a voltage between gate and source terminals of L-TFT and D-TFT is retained in a capacitor. During a LED driving period, TFT3, TFT4 and TFT5 are interrupted, and a current flowing between the source and drain of D-TFT is supplied to LED according to the retaining voltage.

## DESCRIPTION

## ORGANIC LIGHT EMITTING DISPLAY DEVICE

## 5 TECHNICAL FIELD

The present invention relates to a current load device which achieves a function thereof according to a current to be supplied, and more particularly, to a light emitting display device using a light emitting  
10 device as a current load. In particular, the present invention relates to a light emitting display device including a plurality of pixels formed in a matrix form, each of which is comprised of an organic electroluminescence (hereinafter, referred to as "EL") device  
15 serving as a light emitting device, and a drive circuit for supplying a current to the organic EL device.

## BACKGROUND ART

An organic EL device is a light emitting device  
20 which emits light when a current passes therethrough as in a light emitting diode (LED), and is also called an organic LED (OLED). For the light emitting display device including a plurality of pixels formed in a matrix form, each of which is comprised of the organic  
25 EL device and the drive circuit for driving the organic EL device, an active-matrix (hereinafter, referred to as "AM")-type organic EL display has been studied.

FIG. 6 illustrates a configuration example of pixels of the AM-type organic EL display. In FIG. 6, reference symbol LED denotes an organic EL device, reference numeral 101 denotes a drive circuit, reference symbol DL denotes a data line, and reference numeral SL denotes a scan line. FIG. 7 illustrates a configuration example of the AM-type organic EL display in which a plurality of pixels is arranged in a matrix form ( $n$  columns  $\times$   $m$  rows). In FIG. 7, reference symbols SL1 to SL $m$  each denote a scan line which is arranged for each row of the first to  $m$ -th rows, and reference symbols DL1 to DL $n$  each denote a data line which is arranged for each column of the first to  $n$ -th columns. An AM-type organic EL display 100 illustrated in FIG. 7 controls, for each pixel, voltages and currents to be supplied from drive circuits 101 to the organic EL device LEDs through the data lines DL for each column, times, and the like, in response to signals (H level or L level) of the scan lines SL for each row. Through this control, luminance of the organic EL device LED is adjusted, and gradation display thereof is performed.

In the AM-type organic EL display as described above, in a case where the voltage-luminance characteristic of the organic EL device changes over time, the display quality is affected. This also applies to a case where there is variation in

characteristics of a thin film transistor (hereinafter, referred to as "TFT") which is a component of a drive circuit, and a case where the characteristics of the TFT are changed due to an electrical stress to be applied. Accordingly, in order to achieve high-quality display without unevenness, it is necessary to develop a drive circuit and drive method, which is hardly affected by a temporal change of the characteristics of the organic EL device or by the variation and change in characteristics of the TFT.

(Prior Art 1)

FIG. 8 illustrates a simplest drive circuit as a first prior art. In FIG. 8, reference symbol LED denotes an organic EL device, reference numeral 101 denotes a drive circuit, reference symbol DL denotes a data line, reference symbol SL denotes a scan line, reference symbol VS denotes a power supply line, reference symbol GND denotes a ground line, reference symbol D-TFT denotes a driving p-type TFT, and reference symbol C denotes a capacitor. An on/off operation of a switch (switching element) SW1 is controlled in response to the signal of the scan line SL.

In this prior art, the switch SW1 is turned on in response to the signal of a scan line SL, and a voltage from the data line DL is applied to a gate terminal of the TFT (D-TFT), which is provided within the drive

circuit 101, through the switch SW1, thereby retaining a voltage between the gate terminal and a source terminal in the capacitor C. The TFT supplies a current to the organic EL device LED according to the voltage applied to the gate terminal. In this prior art, the change in the OLED luminescence is small since the time variation in the current-luminance characteristic of the OLED device is smaller than the voltage-luminance characteristics. On the other hand, when there is variation in characteristics of the TFT, the current supplied to the organic EL device LED varies, whereby display unevenness appears. In the prior art, some drive circuits have been proposed in order to solve the above-mentioned problem. In the following description, prior art examples of those drive circuits will be described.

(Prior Art 2)

FIG. 9 illustrates a drive circuit disclosed in U.S. Patent No. 6,373,454 as a second prior art. In FIG. 9, reference symbol LED denotes an organic EL device, reference numeral 101 denotes a drive circuit, reference symbol DL denotes a data line, reference symbols SLA and SLB each denote a scan line, reference symbol VS denotes a power supply line, reference symbol GND denotes a ground line, reference symbol D-TFT denotes a driving p-type TFT, and reference symbol C denotes a capacitor. An on/off operation of each of

switches (switching elements) SW1, SW2, and SW3 is controlled in response to the signal of the scan lines SL.

In this prior art, the switches SW1 and SW2 are turned on in response to the signal of the scan line SLA, and a current is supplied from the outside (data line DL) through the switch SW1 to the TFT (D-TFT) provided within the drive circuit 101, in which a short circuit between the gate terminal and the drain terminal is formed through the switch SW2. As a result, the voltage at the gate terminal of the TFT can be set as a voltage at which the current flows from the outside according to the threshold and the mobility of the TFT. Then, when the switch SW3 is turned on in response to the signal of the scan line SLB, the TFT serves as a current source and is capable of passing the current having the same intensity as that from the outside to the organic EL device LED through the switch SW3. Accordingly, if the current from the outside does not vary, according to this prior art, it is possible to cause a constant current to flow through the organic EL device and perform display without unevenness irrespective of the characteristic variation of the TFT. (Prior Art 3)

FIG. 10 illustrates a drive circuit disclosed in U.S. Patent No. 6,501,466 as a third prior art. In FIG. 10, reference symbol LED denotes an organic EL device,

reference numeral 101 denotes a drive circuit,  
reference symbol DL denotes a data line, reference  
symbol SL denotes a scan line, reference symbol VS  
denotes a power supply line, reference symbol GND  
5 denotes a ground line, reference symbols L-TFT and D-  
TFT denote a pair of p-type TFTs forming a current  
mirror circuit, and reference symbol C denotes a  
capacitor. An on/off operation of each of switches  
(switching elements) SW1 and SW2 is controlled in  
10 response to the signal of the scan line SL.

According to this prior art, the switches SW1 and  
SW2 are turned on in response to the signal of the scan  
line SL, the gate terminal and the drain terminal of  
one TFT (L-TFT) are short-circuited through the switch  
15 SW2, and a current is supplied from the outside (data  
line DL) through the switch SW1. As a result, the  
voltage at the gate terminal of the L-TFT can be set as  
a voltage with which the current flows from the outside.  
With this configuration, the other TFT (D-TFT) of the  
20 prior art TFTs supplies a current to the organic EL  
device LED according to the voltage. The two TFTs  
forming the current mirror circuit are positioned  
closer to each other and there is a small variation in  
characteristics therebetween, so the current supplied  
25 to the organic EL device LED is determined based on the  
current from the outside and the current capability  
ratio between the L-TFT and the D-TFT. Accordingly, if

the current from the outside does not vary, according to this prior art, it is possible to cause a constant current to flow through the organic EL device and perform display without unevenness irrespective of the variation in characteristics of the TFTs.

For the above-mentioned circuit, a TFT having a channel layer made of a polycrystal-Si (hereinafter, referred to as "p-Si"), amorphous silicon (hereinafter, referred to as "a-Si"), an organic semiconductor (hereinafter, referred to as "OS"), or the like has been studied. The p-Si TFT can be produced with a high mobility at low working voltage, but manufacturing costs therefor are high. On the other hand, the a-Si TFT or the OS TFT can be produced at low cost with a small number of manufacturing steps, but requires a high working voltage and large power consumption because the a-Si and OS the TFT have a lower mobility than the p-Si TFT. Further, a TFT using a metal oxide semiconductor such as ZnO as the channel layer has been developed in recent years, and it is reported that such a TFT has a higher mobility than the a-Si TFT and the OS TFT.

It is difficult for the TFT having the channel layer made of a-Si, an OS, or a metal oxide semiconductor to be of a complementary TFT in which an n-type TFT and a p-type TFT are formed on the same substrate. For example, a p-type semiconductor having



a high mobility has not been obtained with a-Si or a metal oxide, so it is difficult to form a p-type TFT.

In addition, with regard to the OS, the n-type semiconductor and the p-type semiconductor that have a high mobility are made of different materials, which requires twice as many processes and makes it difficult to manufacture the TFT at low costs. Accordingly, it is necessary that the drive circuit using those TFTs is formed of only the n-type TFT or the p-type TFT.

10 Further, it is known that the TFT having the channel layer made of a-Si, an OS, or a metal oxide has a current-voltage characteristic which can shift according to the voltage to be applied between the gate terminal and the source terminal.

15 In the above description, the a-Si TFT is used for a pixel of an AM-type liquid crystal display (hereinafter, referred to as "LCD") and a production technology therefor with a diagonal size of several ten inches is established. For this reason, the a-Si TFT is regarded as a promising TFT for a drive circuit of a large AM-type organic EL display having a diagonal size of 10 inches or larger, and technology development has been promoted (see fourth prior art as illustrated in FIG. 11 to be described later).

25 On the other hand, the organic EL device generally has a configuration in which at least a light emitting layer made of an organic material which is

sandwiched between an anode electrode and a cathode electrode. The organic material is affected by heat, electromagnetic wave, water, and the like, so characteristics thereof are liable to be changed. For this reason, for a light emitting display device using the organic EL device, it is desirable to use a manufacturing process in which the light emitting layer made of the organic material is formed after formation of the drive circuit and the anode electrode, and then the cathode electrode is formed by vacuum deposition or the like which causes less damage.

According to the above-mentioned process, it is considered a case where each pixel of the AM-type organic EL display includes a drive circuit formed of an n-type TFT, and an organic EL device having an anode electrode, an organic light emitting layer, and a cathode electrode that are formed in the stated order from the bottom. In this case, functions disclosed in U.S. Patent No. 6,373,454 and U.S. Patent No. 6,501,466 cannot be achieved only by replacing the p-type TFT with the n-type TFT. This is because, in U.S. Patent No. 6,373,454 and U.S. Patent No. 6,501,466, the source terminal voltage of the p-type TFT is fixed by a power supply, and the gate terminal voltage is determined based on the current from the outside. For this reason, at the time of driving the organic EL device, the voltage difference between the gate terminal and the

source terminal is fixed, which functions as a constant current source with respect to the organic EL device.

In this case, when the p-type TFT is replaced with the n-type TFT, the voltage between the gate terminal and  
5 the drain terminal is fixed, which does not function as a constant current source. Further, as described above, the characteristic shift due to the applied voltage is caused, so it is necessary to suppress the influence of the characteristic shift.

10 (Prior Art 4)

A fourth prior art is a prior art for solving the above-mentioned problem with the drive circuit using an a-Si TFT. FIG. 11 illustrates a drive circuit disclosed in A. Nathan et al. (SID 05 DIGEST, p. 26,  
15 Fig. 3) and A. Nathan et al. (SID 06 DIGEST, 46.1, Fig. 1). In FIG. 11, reference symbol LED denotes an organic EL device, reference numeral 101 denotes a drive circuit, reference symbol DL denotes a data line, reference symbol SL denotes a scan line, reference  
20 symbol VS denotes a power supply line, reference symbol GND denotes a ground line, reference symbols L-TFT and D-TFT denote a pair of n-type TFTs forming a current mirror circuit, and reference symbol C denotes a capacitor. An on/off operation of each of switches  
25 (switching elements) SW1 and SW 2 is controlled in response to the signal of the scan line SL.

In this prior art, the current mirror circuit

disclosed in U.S. Patent No. 6,501,466 is applied.

According to this prior art, the switches SW1 and SW2 are turned on in response to the signal of the scan line SL, the gate terminal and the drain terminal of the L-TFT are connected to each other through the switch SW2, and a current is supplied from the outside (data line DL) through the switch SW1. Then, the supplied current flows from the drain terminal of the L-TFT to the source terminal thereof and further to the organic EL device LED. Accordingly, the voltages at the gate terminal and the source terminal of the L-TFT become a voltage with which the current flows from the outside. In addition, the D-TFT has a common gate terminal and source terminal with the L-TFT, so the D-TFT supplies the current to the organic EL device LED according to the gate terminal voltage and the source terminal voltage of the L-TFT. By retaining the gate terminal voltage in the capacitor C, the D-TFT can supply a current which is the same as the current obtained during a period in which the current is supplied from the outside, even in a period in which the current from the outside is stopped.

In addition, during the operation, the gate terminals and the source terminals of the D-TFT and the L-TFT are supplied with the same voltage, and the characteristic shifts of the TFTs become the same. At this time, the current capability ratio between the D-

TFT and the L-TFT is retained. In this case, even when the characteristic shift is caused, the current flowing through those TFTs can be made comparable to the current obtained before the characteristic shift is  
5 caused.

Note that, in this prior art, it is necessary for the L-TFT to have sufficiently low capability for causing the current to flow, as compared with the D-TFT. This is because the organic EL device is supplied with  
10 a current from the L-TFT and the D-TFT during a period in which a current from the outside is supplied, while during a period in which the current from the outside is stopped, the organic EL device is supplied with a current only from the D-TFT. Accordingly, in both  
15 periods, the source voltages of the L-TFT and the D-TFT which are determined based on the current capability of the organic EL device do not match with each other when a current value of the L-TFT is larger than that of the D-TFT. In this case, the current set during the period  
20 in which the current from the outside is supplied cannot be caused to flow during the period in which the current from the outside is stopped. As a result, it is necessary that the current supplied to the L-TFT from the outside is made smaller than the current  
25 supplied by the D-TFT to the organic EL device.

On the other hand, in recent years, the current-luminance characteristic of the organic EL device has

been improved, and the current supplied to the organic EL device has been lowered. In addition, there is a demand for a larger and higher-definition organic EL display, and a line load tends to be increased.

5 Therefore, in the prior arts particularly in a case of supplying a low current corresponding to low gradation from the outside, a long time is necessary for charging the line load. In this case, it takes a long time to perform an operation for setting the voltage at the  
10 gate terminal of the TFT provided within the drive circuit to be equal to the voltage at which the current from the outside flows, according to the threshold and the mobility of the TFT, which makes it difficult to apply the organic EL device to a display device with  
15 high-resolution and a large screen. In order to overcome the difficulty, a unit for increasing the current from the outside may be employed, but the unit cannot be applied to the fourth prior art as described above.

20

#### DISCLOSURE OF THE INVENTION

It is an object of the present invention to provide a light emitting display device using a drive circuit comprised of only unipolar thin film  
25 transistors, which is capable of suppressing an effect of characteristic shifts of the thin film transistors due to an applied voltage, and is capable of being

applied to a large and high-resolution light emitting display device.

In order to attain the above-mentioned object, according to a first aspect of the present invention, there is provided a light emitting display device including a pixel having a light emitting device and a drive circuit for driving the light emitting device. The drive circuit includes a first thin film transistor, a second thin film transistor, a first switch, and a capacitor.

A gate terminal of the first thin film transistor is connected to a gate terminal of the second thin film transistor, and a source terminal of the first thin film transistor is connected to a source terminal of the second thin film transistor. The source terminals are connected to one end of the light emitting device, and the first thin film transistor and the second thin film transistor have the same polarity. In addition, the first switch has one end connected to the source terminals of the first thin film transistor and the second thin film transistor and to one end of the light emitting device and has the other end connected to a first line. Further, the capacitor has one end connected to the gate terminals of the first thin film transistor and the second thin film transistor and has the other end connected to the source terminals of the first thin film transistor and the second thin film

transistor. A second line supplies a drive signal to the light emitting device.

The drive circuit has at least a first period for writing the drive signal and a second period for  
5 driving the light emitting device after the first period.

The first period contains a period in which the first line and one end of the light emitting device are set to the same voltage through the first switch, and a  
10 second line is electrically connected to the drain terminal of the first thin film transistor and the gate terminals of the first thin film transistor and the second thin film transistor to thereby supply a current to the first thin film transistor from the second line.

15 The second period contains a period in which connection between the second line and the first thin film transistor, connection between the second line and the second thin film transistor, and the first switch are shut off.

20 Further, in order to attain the above-mentioned object, according to a second aspect of the present invention, there is provided a light emitting display device including a pixel having a light emitting device and a drive circuit for driving the light emitting  
25 device. The drive circuit includes a first thin film transistor, a second thin film transistor, a first switch, and a capacitor.



A gate terminal of the first thin film transistor is connected to a gate terminal of the second thin film transistor, and a source terminal of the first thin film transistor is connected to a source terminal of the second thin film transistor. The source terminals are connected to one end of the light emitting device, and the first thin film transistor and the second thin film transistor have the same polarity. In addition, the first switch has one end connected to the source terminals of the first thin film transistor and the second thin film transistor and to one end of the light emitting device and has the other end connected to a first line. Further, the capacitor has one end connected to the gate terminals of the first thin film transistor and the second thin film transistor and has the other end connected to the source terminals of the first thin film transistor and the second thin film transistor. A second line supplies a drive signal to the light emitting device.

The drive circuit has at least a first period for writing the drive signal and a second period for driving the light emitting device after the first period.

During the first period, the driving circuit sets the first line and one end of the light emitting device to the same voltage through the first switch and supplies a current from the second line to the drain

terminal of the first thin film transistor and the gate  
terminals of the first thin film transistor and the  
second thin film transistor to thereby retain in the  
capacitor a voltage between the gate terminal and the  
5 source terminal of the second thin film transistor  
which is determined based on a current flowing between  
the drain terminal and the source terminal of the first  
thin film transistor.

Further, during the second period, the drive  
10 circuit supplies to the light emitting device a current  
flowing between the source terminal and the drain  
terminal of the second thin film transistor according  
to the retaining voltage of the capacitor.

According to the present invention, each pixel  
15 has a drive circuit including a current mirror circuit  
formed of a pair of thin film transistors having the  
same polarity. The pair of thin film transistors have  
a common source terminal connected to one end of the  
light emitting device and connected to the first line  
20 through the first switch, and there is provided a  
capacitor between the gate terminal and the source  
terminal. With this configuration, it is possible to  
provide a light emitting display device using a drive  
circuit comprised of only unipolar thin film  
25 transistors, which is capable of suppressing the effect  
of the characteristic shifts of the thin film  
transistors due to the applied voltage, and is capable

of being applied to a large and high-resolution light emitting display device.

Further features of the present invention will become apparent from the following description of  
5 exemplary embodiments with reference to the attached drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a circuit diagram for illustrating a  
10 configuration of a pixel of a light emitting display device according to Example 1 of the present invention.

FIG. 2 is a timing chart illustrating an operation of the light emitting display device according to Example 1.

15 FIG. 3 is a timing chart for illustrating an operation of a light emitting display device according to Example 2 of the present invention.

FIG. 4 is a circuit diagram for illustrating a configuration of a pixel of a light emitting display  
20 device according to Example 3 of the present invention.

FIG. 5 is a timing chart for illustrating the operation of the light emitting display device according to Example 3.

FIG. 6 is a diagram for illustrating a  
25 configuration of a pixel.

FIG. 7 is a diagram for illustrating a configuration of an organic EL display device.

FIG. 8 is a circuit diagram for illustrating a configuration of a pixel according to a first prior art example.

FIG. 9 is a circuit diagram for illustrating a configuration of a pixel according to a second prior art example.

FIG. 10 is a circuit diagram for illustrating a configuration of a pixel according to a third prior art example.

FIG. 11 is a circuit diagram for illustrating a configuration of a pixel according to a fourth prior art example.

#### BEST MODE FOR CARRYING OUT THE INVENTION

Hereinafter, exemplary embodiments of the present invention will be described with reference to the drawings.

In an embodiment of the present invention, a light emitting display device using an organic EL device will be described, but the present invention can also be applied to a light emitting display device other than the organic EL device, which emits light with a supplied current, and to a current load device using a normal current load which indicates an arbitrary function for a supplied current. In addition, in the embodiment, an n-type TFT is described. Alternatively, as described later, it is possible to

employ a p-type TFT, instead of the n-type TFT, with an anode terminal of the organic EL device being replaced with a cathode terminal.

The light emitting device according to the  
5 embodiment has a pixel which includes at least an organic EL device whose luminance is determined based on a supplied current, and a drive circuit for supplying a constant current to the organic EL device.

The organic EL device is also called "OLED" as  
10 described above and can afford a planar self-emitting light capable of emitting light at high luminance. The organic EL device enables light emission at low voltage and with high efficiency by laminating organic layers serving as a light emitting layer between an anode  
15 electrode and a cathode electrode according to functions thereof and by increasing the number of functional laminated layers of the organic layers. A basic configuration of the organic EL device is such that the organic EL device includes an EL light  
20 emitting layer comprised of organic layers and a hole transport layer between an anode electrode and a cathode electrode to form an anode electrode/hole transport layer/EL light emitting layer/cathode electrode laminated structure. In the light emitting  
25 display device using the organic EL device as the light emitting device, light emitting luminance is controlled through injection of holes and electrons into the light

emitting layer. It should be noted that the organic EL device is a known matter, so a detailed description thereof will be omitted.

A light emitting display device according to the present invention will be described with reference to FIGS. 1 and 2.

The light emitting display device according to the present invention has a pixel including a light emitting device and a drive circuit 101 for driving the light emitting device. The drive circuit 101 includes a first thin film transistor L-TFT, a second thin film transistor D-TFT, a first switch TFT3, a capacitor C, and a first line GND.

The first thin film transistor and the second thin film transistor (L-TFT and D-TFT) have gate terminals connected to each other and source terminals connected to each other, and the source terminals are connected to one end (anode terminal) of the light emitting device. In this case, the first thin film transistor and the second thin film transistor (L-TFT and D-TFT) have the same polarity.

Further, the first switch TFT3 has one end connected to the source terminals of the first thin film transistor and the second thin film transistor (L-TFT and D-TFT) and one end (anode terminal) of the light emitting device and has the other end connected to the first line GND.

Further, the capacitor C has one end connected to the gate terminals of the first thin film transistor and the second thin film transistor (L-TFT and D-TFT) and has the other end connected to the source terminals of the first thin film transistor and the second thin film transistor. A second line DL supplies a drive signal of the light emitting device.

Then, the drive circuit according to the present invention has at least a first period (T1 of FIG. 2) for writing the drive signal and a second period (T2 of FIG. 2) for driving the light emitting device after the first period.

During the first period (T1), the drive circuit sets the first line and one end (anode terminal) of the light emitting device to the same voltage through the first switch TFT3. In addition, during the first period (T1), the drive circuit electrically connects the second line DL to the drain terminal of the first thin film transistor and the gate terminals of the first thin film transistor and the second thin film transistor to thereby supply a current from the second line to the first thin film transistor. The first period (T1) contains a period for performing the above-mentioned operations.

In this case, when connecting the second line to the drain terminal of the first thin film transistor and the gate terminals of the first thin film

transistor and the second thin film transistor, a second switch TFT4 and a third switch TFT5 may be used as illustrated in FIG. 1.

In other words, it is possible to use the second switch TFT4 having one end connected to the second line and the other end connected to the drain terminal of the L-TFT and the third switch TFT5 having one end connected to the drain terminal of the L-TFT and the other end connected to the gate terminal of the L-TFT.

In addition, in this case, the drain terminal of the third switch TFT5 may be directly connected to the second line DL.

The second period (T2) contains a period for shutting off the connection between the second line and the first thin film transistor, connection between the second line and the second thin film transistor, and the first switch.

Through the operations during the above-mentioned first period and second period, the drive circuit performs the following operations.

During the first period (T1), the drive circuit sets the first line and one end of the light emitting device to the same voltage through the first switch TFT3. Further, the drive circuit supplies a current from the second line DL to the drain terminal of the first thin film transistor and the gate terminals of the first thin film transistor and the second thin film



transistor. As a result, it is possible to retain, in the capacitor C, the voltage between the gate terminal and the source terminal of the second thin film transistor, which is determined based on the current  
5 flowing between the drain terminal and the source terminal of the first thin film transistor.

Further, during the second period (T2), the drive circuit supplies to the light emitting device a current flowing between the source terminal and the drain  
10 terminal of the second thin film transistor, according to a retaining voltage of the capacitor. In this case, the retaining voltage of the capacitor corresponds to a potential difference between  $V_a$  and  $V_b$ . In addition, the current supplied to the light emitting device is  
15 supplied from a power supply line  $V_S$ .

More desirably, in the light emitting display device according to the embodiment, the value ( $W/L$ ) obtained by dividing the channel width of the L-TFT by the channel length thereof is equal to the value  $W/L$  of  
20 the D-TFT, or the value  $W/L$  of the L-TFT is larger than the value  $W/L$  of the D-TFT. As a result, it is possible to define the size ratio between a pair of L-TFT and D-TFT that form a current mirror circuit.

More desirably, in the light emitting display  
25 device according to the embodiment, the capacitance value of the capacitor is three times or more as large as the total capacitance value obtained by adding the

channel capacitance and the gate-drain overlap capacitance of the L-TFT and the channel capacitance and the gate-drain overlap capacitance of the D-TFT. As a result, it is possible to define the size of the capacitance.

More desirably, in the light emitting display device according to the embodiment, the voltage of the first line is equal to or lower than a working voltage of the organic EL device. As a result, it is possible to interrupt the drive current flowing to the organic EL device at the time of current writing.

More desirably, the light emitting display device according to the embodiment includes a drive circuit which does not allow current to flow between the source and the drain of the D-TFT during the period in which at least first to third switches are made conductive (ON period; first period). As a result, it is possible to interrupt the drive current flowing through the organic EL device at the time of current writing.

More desirably, the light emitting display device according to the embodiment includes, as the drive circuit which does not allow current to flow between the source and the drain of the D-TFT, a circuit which sets the drain terminal voltage of the D-TFT to the voltage of the first line during the period in which the first to third switches are in the ON state. As a result, the drive current flowing through the organic

EL device is interrupted by the change in the power supply voltage that corresponds to the drain terminal voltage of the D-TFT. Alternatively, the light emitting display device according to the embodiment  
5 includes a fourth switch between the drain terminal of the D-TFT and the third line (or a power supply). The fourth switch includes at least a drive circuit (current breaker) which conducts interruption, or turning-off, during the period in which at least the  
10 first to third switches are in the ON state. Through the fourth switch, it is possible to interrupt the drive current flowing through the organic EL device.

More desirably, the light emitting display device according to the embodiment includes a drive circuit  
15 which provides a period in which a current is not caused to flow between the source and the drain of the D-TFT (third period) in at least a part of the period in which the first to third switches are shut off (OFF period; second period). The drive circuit is  
20 characterized by using the change in a power supply voltage or the fourth switch. As a result, it is possible to interrupt the drive current flowing to the organic EL device during the third period.

More desirably, in the light emitting display  
25 device according to the embodiment, the first to third switches are each formed of an n-type TFT (hereinafter, referred to as "third to fifth n-type TFTs") which have

the same configuration as the L-TFT and the D-TFT. In the third to fifth n-type TFTs, one of the source terminal and the drain terminal thereof functions as one end of each switch, and the other of the source  
5 terminal and the drain terminal functions as the other end of each switch. As a result, the first to third switches can be formed of the TFT having the same configuration as the L-TFT and the D-TFT.

More desirably, the light emitting display device  
10 according to the embodiment is characterized in that the gate terminals of the third to fifth n-type TFTs are connected to the fourth line. As a result, it is possible to perform common control of TFTs forming the switches.

15 More desirably, in the light emitting display device according to the embodiment, the fourth switch is formed of an n-type TFT (hereinafter, referred to as "sixth n-type TFT") which has the same configuration as the L-TFT, the D-TFT, and the third to fifth n-type  
20 TFTs. In the sixth n-type TFT, one of the source terminal and the drain terminal functions as one end of the switch, and the other of the source terminal and the drain terminal functions as the other end of the switch. As a result, the fourth switch is formed of  
25 the TFT having the same configuration as the L-TFT, the D-TFT, and the first to third switches.

More desirably, in the light emitting display

device according to the embodiment, the TFTs constituting the drive circuit each have an n-type semiconductor film made of an amorphous metal oxide having a carrier density of  $10^{18}$  [ $\text{cm}^{-3}$ ] or lower, as an n-type TFT channel film of the n-type TFT. The film has a mobility of 1 [ $\text{cm}^2/\text{Vs}$ ] or more, and an on/off ratio of  $10^6$  or more. As a result, as the TFT forming the drive circuit, the TFT using an oxide semiconductor as a channel film can be used.

More desirably, in the light emitting display device according to the embodiment, any one of the above-mentioned drive circuits is employed as the drive circuit, and plural drive circuits are formed on the substrate in a matrix form.

According to the drive circuit of the embodiment, during the period in which a current is supplied from the outside and a current which is caused to flow through the pair of n-type TFTs (L-TFT and D-TFT) which form the current mirror circuit is set, no current flows because the voltage between the cathode terminal and the anode terminal of the organic EL device becomes equal to or lower than the working voltage. In addition, the voltage between the gate terminal and the source terminal at which the current supplied from the outside flows is retained in the L-TFT and the D-TFT. Accordingly, the D-TFT functions as a constant current source as long as the D-TFT operates in a saturation

region. In addition, the capacitance is sufficiently larger than the parasitic capacitance such as the overlap capacitance, so it is possible to ignore the effect of the parasitic capacitance even when the voltage at the source terminal, the drain terminal, or the like is fluctuated.

Further, according to the embodiment, during the period for supplying current to the organic EL device, the voltages at the drain terminal and the source terminal of the L-TFT become equal to the voltage of the source terminal of the D-TFT, and the voltages at the gate terminal and the source terminal of each of the L-TFT and the D-TFT are equal to each other. Accordingly, it is possible to set a characteristic change due to an applied voltage to be equal between the L-TFT and the D-TFT.

Further, according to the embodiment, by setting the current capability of the L-TFT to be larger than that of the D-TFT, it is possible to make the current supplied from the outside to the L-TFT larger than the current supplied to the organic EL device by the D-TFT. Accordingly, the present invention can also be applied to a large and high-resolution display. In addition, according to the embodiment, as described above, during the period for setting the current, the current is not caused to flow through the organic EL device. Therefore, even when the current supplied from the

outside is large, no large current is caused to flow through the organic EL device. As a result, during the current setting period, degradation of the organic EL device due to the large current can be suppressed, and  
5 there is no need to set the voltages necessary at the time of current setting to be higher.

Further, according to the embodiment, during the period in which a current is supplied from the outside and a current caused to flow through the current mirror  
10 n-type TFTs (L-TFT and D-TFT) is set, the current flowing through the D-TFT can be stopped. In addition, if this function is used before and after the period in which the current is supplied to the organic EL device, that is, the organic EL device emits light, or only  
15 before the period, or only after the period, it is possible to provide a period for stopping light emission of the organic EL device without causing the current to flow through the D-TFT. When the period for stopping the light emission is provided in this manner,  
20 in order to achieve the same time-average luminance as that of a case where the period for stopping the light emission is not provided, the current supplied to the organic EL device is increased. This corresponds to increasing the current supplied from the outside, and  
25 thus, the present invention can also be applied to a large and high-resolution display. In addition, by providing the period for stopping the light emission, a

similar function to that of a cathode ray tube (CRT) is obtained, and a high-quality dynamic image display with less afterimages can be achieved.

Further, according to the embodiment, as the n-type TFT, an n-type TFT using a semiconductor layer made of an amorphous metal oxide having a carrier density of  $10^{18}$  [ $\text{cm}^{-3}$ ] or lower and a field effect mobility of 1 [ $\text{cm}^2/\text{Vs}$ ] or more as the channel layer is used. As a result, as compared with a case of constituting the light emitting display device using an a-Si TFT or an OS TFT, the light emitting display device using the TFT, capable of being formed at room temperature, with less power consumption can be produced. In addition, the light emitting device has a high mobility, so it is possible to achieve a high-resolution and large screen.

As described above, according to the embodiment, in the light emitting display device using the organic EL device, it is possible to provide a drive circuit for the organic EL device in which an anode electrode, an organic material light emitting layer, and a cathode electrode are laminated in the stated order from the bottom on the drive circuit. The drive circuit can be comprised of an n-type TFT using a-Si, OS, or a metal oxide semiconductor as the channel layer. Further, it is possible to provide a drive circuit capable of suppressing the effect of the characteristic shift of



the TFT caused by the applied voltage. In addition, it is possible to provide a drive circuit capable of being applied to a large and high-resolution light emitting display device.

5        Hereinafter, various examples of the light emitting display device using the organic EL device will be described. However, the present invention is not limited to the organic EL device, and can also be applied to other current loads. Further, in the  
10       following description, an n-type TFT using an amorphous metal oxide semiconductor as the channel layer is used, but the present invention can also be applied to an a-Si TFT and an OS TFT. In addition, the present invention can also be applied to a light emitting  
15       display device which is formed of only an n-type TFT having a channel layer made of another semiconductor material.

(Example 1)

20       First, Example 1 of the present invention will be described.

      A configuration of this example is illustrated in FIG. 1. The light emitting display device illustrated in FIG. 1 is an organic EL display device (AM-type organic EL display) including pixels each having an  
25       organic EL device LED having a cathode terminal connected (grounded) to a ground line GND, and a drive circuit 101 for driving the organic EL device LED.

The organic EL device LED has an anode electrode, an organic material light emitting layer and a cathode electrode, which are laminated in the stated order from the bottom.

5        The drive circuit 101 includes a first n-type TFT (hereinafter, referred to as "L-TFT"), a second n-type TFT (hereinafter, referred to as "D-TFT"), a third n-type TFT (hereinafter, referred to as "TFT3"), a fourth n-type TFT (hereinafter, referred to as "TFT4"), a  
10 fifth n-type TFT (hereinafter, referred to as "TFT5"), and a capacitor C. The L-TFT and the D-TFT are each formed of an n-type TFT forming a current mirror circuit (n-type current mirror TFT), and the TFT3, the TFT4, and the TFT5 are each formed of an n-type TFT  
15 forming a switch (switching element) (n-type switching TFT).

In the drive circuit 101, there are arranged lines such as a data line DL for supplying to the L-TFT a current which corresponds to display gradation of a  
20 pixel, a scan line SL connected to each gate terminal of the TFT3, the TFT4, and the TFT5, a power supply line VS, and a ground line GND. The ground line GND corresponds to the first line of the present invention, the data line DL corresponds to the second line of the  
25 present invention, the power supply line VS corresponds to the third line of the present invention, and the scan line SL corresponds to the fourth line of the

present invention, respectively.

The L-TFT has a source terminal connected to the anode terminal of the organic EL device LED, and a gate terminal connected to one end of the capacitor C. The L-TFT corresponds to the first thin film transistor forming the current mirror circuit of the present invention.

The D-TFT has a source terminal connected to the anode terminal of the organic EL device LED, and a gate terminal connected to one end of the capacitor C, and a drain terminal connected to the power supply line VS. The D-TFT corresponds to the second thin film transistor forming the current mirror circuit of the present invention.

The TFT3 has source and drain terminals, one of the source and drain terminals being connected to the anode terminal of the organic EL device LED, and the other of the source and drain terminals being connected (grounded) to the ground line GND. The TFT3 corresponds to the first switch of the present invention.

The TFT4 has source and drain terminals, one of the source and drain terminals being connected to the data line DL and the other of the source and drain terminals being connected to the drain terminal of the L-TFT. The TFT4 corresponds to the second switch of the present invention.

The TFT5 has source and drain terminals, one of the source and drain terminals being connected to the drain terminal of the L-TFT, and the other of the source and drain terminals being connected to the gate terminal of the L-TFT. The TFT5 corresponds to the third switch of the present invention.

The capacitor C has one end connected to the gate terminals of the L-TFT and the D-TFT, and the other end connected to the source terminals of the L-TFT and the D-TFT. In addition, the other end of the capacitor C is connected to the anode terminal of the organic EL device LED.

In this case, the voltage of the power supply line VS is set to a voltage VD at which the D-TFT operates in the saturation region even in a case where the current set during the current writing period to be described later is caused to flow through the D-TFT and the organic EL device LED.

Further, the current capability of the L-TFT is assumed to be 4 times as large as that of the D-TFT. This can be realized by setting the channel length of the L-TFT to be equal to that of the D-TFT and by setting the channel width of the L-TFT to be 4 times as long as that of the D-TFT.

In addition, the capacitance value of the capacitor C is set to be 3 times or more as large as the total sum of the parasitic capacitances such as the

overlap capacitance regarding the L-TFT and the D-TFT.

Next, referring to a timing chart illustrated in FIG. 2, operations according to this example will be described.

5 First, during the period T1 in which a signal of the scan line SL is set to an H level (current writing period: first period), the TFT3, the TFT4, and the TFT5 are turned on. During the period T1, when the TFT3 is turned on, the voltage at the source terminals of the  
10 L-TFT and the D-TFT and the voltage Vb at the anode terminal of the organic EL device LED are the same as the voltage of the ground line GND through the TFT3. On the other hand, when the TFT4 and the TFT5 are turned on, a current which is 4 times as much as the  
15 current to be supplied to the organic EL device LED is supplied from the data line DL to the drain terminal of the L-TFT through the TFT4. As a result, the voltage Va at the gate terminal is set as a voltage at which the current 4 times as the current to be supplied to  
20 the organic EL device LED flows between the drain terminal and the source terminal of the L-TFT. At the same time, between the drain terminal and the source terminal of the D-TFT, 1/4 of the current from the data line DL, that is, the current to be supplied to the  
25 organic EL device LED flows. On the other hand, the voltage Vb at the anode terminal of the organic EL device LED is at the same potential as that of the

voltage of the ground line GND. Accordingly, the current flowing through the D-TFT does not flow through the organic EL device LED, but entirely flows toward the ground line GND through the TFT3.

5           Next, during the period T2 in which the signal of the scan line SL is set to a L level (LED driving period which corresponds to light emitting period: second period), the TFT3, the TFT4, and the TFT5 are turned off. During the period T2, through the  
10   capacitor C, the voltage difference between the gate terminal and the source terminal of the D-TFT becomes a voltage difference set during the current writing period T1. In other words, the D-TFT becomes a current source for supplying the current set during the current  
15   writing period T1 from the D-TFT toward the organic EL device LED, as long as the D-TFT performs a saturation operation. Accordingly, the source terminal voltage of the D-TFT becomes such an anode terminal voltage that the current set during the current writing period T1  
20   flows through the organic EL device LED. Then, the gate terminal voltage of the D-TFT becomes a voltage obtained by adding the voltage difference between the gate terminal and the source terminal, which is set during the current writing period T1, to the anode  
25   terminal voltage of the organic EL device LED. As a result, the organic EL device LED emits light according to the supplied current.

On the other hand, the gate terminal of the L-TFT is at the same voltage as that of the gate terminal of the D-TFT, so the voltages of the source terminal and the drain terminal of the L-TFT become the same as that of the source terminal of the D-TFT.

Hereinafter, in the organic EL display, the above-mentioned operations are repeated for each line, thereby displaying images on the display.

Therefore, according to this example, during the current writing period for supplying a current from the data line to the L-TFT, the voltages at the cathode terminal and the anode terminal of the organic EL device LED become the same, so no current flows therethrough. In addition, the voltage between the gate terminal and the source terminal at which the current supplied from the data line flows is retained in the capacitor C for the L-TFT and the D-TFT. Also during the LED driving period, the D-TFT functions as the constant current source as long as the D-TFT operates in the saturation region. In addition, the capacitor C is sufficiently larger than the total sum of the parasitic capacitances such as the overlap capacitance regarding the L-TFT and the D-TFT. Accordingly, it is possible to ignore the effect of the parasitic capacitance even when the voltages of the source terminal, the drain terminal, and the like are fluctuated.

Further, according to this example, during the LED driving period, the voltage between the drain terminal and the source terminal of the L-TFT becomes equal to the voltage at the source terminal of the D-TFT, and voltages between the gate terminals and the source terminals of the L-TFT and the D-TFT can be set to be equal to each other. Accordingly, the same characteristic change caused by the applied voltage can be obtained in the L-TFT and the D-TFT. As a result, the change in current capability ratio between the L-TFT and the D-TFT does not appear, and the influence of the characteristic change of the L-TFT and the D-TFT can be suppressed as long as the current from the data line is written.

Further, according to this example, by setting the current capability of the L-TFT to be larger than that of the D-TFT, it is possible to make the current to be supplied from the data line to the L-TFT larger than the current supplied to the organic EL device LED by the D-TFT. As a result, the current writing period can be shortened, and application to the large and high-resolution display is possible.

Further, according to this example, during the current writing period, no current is caused to flow through the organic EL device. Accordingly, as described above, even when the current supplied from the outside is large, no large current flows through



the organic EL device. In this case, it is possible to suppress degradation of the organic EL device, and there is no need to increase the voltage of the data line in order to compensate the rise of the anode terminal voltage of the organic EL device.

In addition, according to this example, as the L-TFT and the D-TFT, an n-type TFT using a semiconductor layer made of an amorphous metal oxide having a carrier density of  $10^{18}$  [ $\text{cm}^{-3}$ ] or lower and a field effect mobility of 1 [ $\text{cm}^2/\text{Vs}$ ] or more as the channel layer is used. As a result, as compared with a case of constituting the light emitting device using an a-Si TFT or an OS TFT, the light emitting display device using the TFT capable of being formed at room temperature with less power consumption can be produced. In addition, the light emitting device has a high mobility, so it is possible to realize the high-resolution and large displays.

(Example 2)

Next, Example 2 of the present invention will be described. A configuration of a light emitting display device according to this example is identical with that of Example 1. Note that this example is characterized in that the voltage of the power supply line VS is varied.

Hereinafter, referring to a timing chart illustrated in FIG. 3, operations according to this

example will be described.

First, in a period T11 in which the signal of the scan line SL is set to the H level, and the voltage of the power supply line VS is set to be at the same potential as the voltage of the ground line GND (hereinafter, referred to as "GND") (current writing period), the TFT3, the TFT4, and the TFT5 are turned on. During the period T11, when the TFT3 is turned on, the voltage of the source terminals of the L-TFT and the D-TFT and the voltage Vb of the anode terminal of the organic EL device LED are at the same potential as that of the voltage of the ground line GND through the TFT3. On the other hand, when the TFT4 and the TFT5 are turned on, a current which is 16 times as much as the current to be supplied to the organic EL device LED is supplied from the data line DL to the drain terminal of the L-TFT. As a result, the voltage Va of the gate terminal is set to such a voltage that the current 16 times as much as the current to be supplied to the organic EL device LED flows between the drain terminal and the source terminal of the L-TFT. On the other hand, the voltage of the power supply line VS is equal to GND, so no current flows between the drain terminal and the source terminal of the D-TFT. In addition, the voltage Vb at the anode terminal of the organic EL device LED is at the same potential as the voltage of the ground line GND, so no current flows through the

organic EL device LED.

Then, there is provided a period T21 in which the signal of the scan line SL is set to a L level and the voltage of the power supply line VS is set to the voltage VD (LED driving period which corresponds to light emitting period). Note that the period T21 is set to 1/4 of the LED driving period T2 of Example 1. During the period T21, the TFT3, the TFT4, and the TFT5 are turned off. In addition, through the capacitor C, the voltage difference between the gate terminal and the source terminal of the D-TFT becomes a voltage difference set during the current writing period T11. Specifically, the D-TFT becomes a current source for supplying the current set during the current writing period T11 from the D-TFT toward the organic EL device LED, that is, a current 4 times as much as the current to be supplied to the organic EL device LED, as long as the D-TFT performs a saturation operation. Accordingly, the source terminal voltage of the D-TFT becomes such an anode terminal voltage that the current set during the current writing period T11 flows through the organic EL device LED. Then, the gate terminal voltage of the D-TFT becomes a voltage obtained by adding the voltage difference between the gate terminal and the source terminal, which is set during the current writing period T1, to the anode terminal voltage of the organic EL device LED. As a result, the organic EL

device LED emits light according to the supplied current.

In addition, there is provided a period T22 in which the signal of the scan line SL is set to a L level and the voltage of the power supply line VS is set to GND (dark displaying period). During the period T22, no current flows from the D-TFT, so the organic EL device LED does not emit light.

Hereinafter, in the organic EL display, the above-mentioned operations are repeated in each line, thereby displaying images on the display.

Therefore, according to this example, the same effects as those of Example 1 can be obtained. Further, in this example, a dark displaying period is provided, and the LED driving period is set to 1/4 of that of Example 1, and the current flowing through the organic EL device LED is set to 4 times as much as that of Example 1. As a result, the time average luminance can be set to be substantially equal to that of Example 1. On the other hand, the current supplied from the data line becomes 4 times as much as that of Example 1, so the current writing period can be shortened, and application to a larger and higher-definition display is possible as compared with Example 1.

(Example 3)

Next, Example 3 of the present invention will be described.

A configuration of this example is illustrated in FIG. 4. A light emitting display device illustrated in FIG. 4 is an organic EL display device (AM-type organic display) including pixels each having an organic EL device LED having a cathode terminal connected (grounded) to a ground line GND, and a drive circuit 101 for driving the organic EL device LED.

In the organic EL device LED, an anode electrode, an organic material light emitting layer, and a cathode electrode are laminated in the stated order from the bottom.

The drive circuit 101 includes a first n-type TFT (hereinafter, referred to as "L-TFT"), a second n-type TFT (hereinafter, referred to as "D-TFT"), a third n-type TFT (hereinafter, referred to as "TFT3"), a fourth n-type TFT (hereinafter, referred to as "TFT4"), and a fifth n-type TFT (hereinafter, referred to as "TFT5"). In addition, the drive circuit 101 includes a sixth n-type TFT (which corresponds to sixth thin film transistor; hereinafter, referred to as "TFT6"), and a capacitor C. The L-TFT and the D-TFT are each formed of an n-type TFT (n-type current mirror TFT) forming a current mirror, and the TFT3, the TFT4, the TFT5, and the TFT6 are each formed of an n-type TFT (n-type switching TFT) forming a switching element (switch).

In the drive circuit 101, there are arranged a data line DL for supplying a current, which corresponds

to display gradation of a pixel, to the L-TFT, and a first scan line SLA connected to each gate terminal of the TFT3, the TFT4, and the TFT5. In addition, in the drive circuit 101, there are provided lines such as a  
5 second scan line SLB connected to the gate terminal of the TFT6, a power supply line VS, and the ground line GND. The ground line GND corresponds to the first line of the present invention, the data line DL corresponds to the second line of the present invention, the power  
10 supply line VS corresponds to the third line of the present invention, and the first scan line SLA and the second scan line SLB correspond to the fourth and fifth line of the present invention, respectively.

The L-TFT has a source terminal connected to the  
15 anode terminal of the organic EL device LED and a gate terminal connected to one end of the capacitor C. The L-TFT corresponds to the first thin film transistor forming a current mirror circuit of the present invention.

20 The D-TFT has a source terminal connected to the anode terminal of the organic EL device LED and a gate terminal connected to one end of a capacitor C. The D-TFT corresponds to the second thin film transistor forming a current mirror circuit of the present  
25 invention.

The TFT3 has source and drain terminals, one of the source and drain terminals being connected to the

anode terminal of the organic EL device LED, and the other of the source and drain terminals being connected (grounded) to the ground line GND. The TFT3 corresponds to the first switch of the present invention.

The TFT4 has source and drain terminals, one of the source and drain terminals being connected to the data line DL, and the other of the source and drain terminals being connected to the drain terminal of the L-TFT. The TFT4 corresponds to the second switch of the present invention.

The TFT5 has source and drain terminals, one of the source and drain terminals being connected to the drain terminal of the L-TFT, and the other of the source and drain terminals being connected to the gate terminal of the L-TFT. The TFT5 corresponds to the third switch of the present invention.

The TFT6 has source and drain terminals, one of the source and drain terminals being connected to the drain terminal of the D-TFT, and the other of the source and drain terminals being connected to the power supply line VS. The TFT6 corresponds to the fourth switch of the present invention.

The capacitor C has one end connected to the gate terminals of the L-TFT and the D-TFT and the other end connected to the source terminals of the L-TFT and the D-TFT. In addition, the other end of the capacitor C

is connected to the anode terminal of the organic EL device LED.

In this case, the voltage of the power supply line VS is set to the voltage VD at which the D-TFT operates in the saturation region even in a case where  
5 the current written during the current writing period to be described later is caused to flow through the D-TFT and the organic EL device LED.

Further, the current capability of the L-TFT is  
10 assumed to be 4 times as large as that of the D-TFT, which can be realized by setting the channel length of the L-TFT to be equal to that of the D-TFT and by setting the channel width of the L-TFT to be 4 times as long as that of the D-TFT.

15 In addition, the capacitance value of the capacitor C is set to be 3 times or more as large as the total sum of the parasitic capacitances such as the overlap capacitance regarding the L-TFT and the D-TFT.

Next, referring to a timing chart illustrated in  
20 FIG. 5, operations according to this example will be described.

First, during a period T11 in which a signal of the scan line SLA is set to a H level, and a signal of the second scan line SLB is set to a L level (current  
25 writing period), the TFT3, the TFT4, and the TFT5 are turned on and the TFT6 is turned off. During the period T11, the TFT3 is turned on, so the voltage of



the source terminals of the L-TFT and the D-TFT and the voltage  $V_b$  of the anode terminal of the organic EL device LED are at the same potential as that of the voltage of the ground line GND. On the other hand, the

5 TFT4 and the TFT5 are turned on, so the current which is 16 times as much as the current to be supplied to the organic EL device LED is supplied from the data line DL to the drain terminal of the L-TFT. As a result, the voltage  $V_a$  of the gate terminal is set to

10 such a voltage that the current 4 times as much as the current to be supplied to the organic EL device LED flows between the drain terminal and the source terminal of the L-TFT. On the other hand, between the drain terminal and the source terminal of the D-TFT,

15 the TFT6 is turned off, so a current path between the power supply line VS is interrupted, and thus no current flows. Further, the voltage of the anode terminal of the organic EL device LED and the voltage of the ground line GND are at the same potential, so no

20 current flows through the organic EL device LED.

Next, there is provided a period T21 in which the signal of the first scan line SLA is set to a L level and the signal of the second scan line SLB is set to a H level (LED driving period which corresponds to light

25 emitting period). Note that the period T21 is set to 1/4 of the LED driving period T2 of Example 1. During the period T21, the TFT3, the TFT4, the TFT5 are turned

off, and the TFT6 is turned on. In addition, through the capacitor C, the voltage difference between the gate terminal and the source terminal of the D-TFT becomes a voltage difference set during the current writing period T11. In other words, as long as the D-TFT performs a saturation operation, the D-TFT becomes a current source for supplying the current set during the current writing period T11 from the D-TFT toward the organic EL device LED, that is, the current 4 times as much as the current to be supplied to the organic EL device LED. Accordingly, the source terminal voltage of the D-TFT becomes such an anode terminal voltage that the current set during the current writing period T11 flows through the organic EL device LED. Then, the gate terminal voltage of the D-TFT becomes a voltage obtained by adding the voltage difference between the gate terminal and the source terminal, which is set during the current writing period T1, to the anode terminal voltage of the organic EL device LED. As a result, the organic EL device LED emits light according to the supplied current.

In addition, there is provided a period T22 in which the signal of the first scan line SLA is set to a L level and the signal of the second scan line SLB is set to a L level (dark displaying period). During the period T22, the TFT6 is turned off, and the current path between the power supply line VS and the drain

terminal of the D-TFT is interrupted, so no current flows from the D-TFT, so the organic EL device LED does not emit light.

Hereinafter, in the organic EL display, the  
5 above-mentioned operations are repeated in each line, thereby displaying images on the display.

In this example, by adding the signal line SLB and the TFT6, it is possible to achieve the effects of Example 2 without varying the voltage supply VS.

10 It should be noted that in Examples 1 to 3, the current capability ratio between the L-TFT and the D-TFT is set to '4', but the current capability ratio between the L-TFT and the D-TFT can be set according to the current-luminance characteristics of the organic EL  
15 device LED and the load capacitance of the data line DL.

Further, in Examples 2 and 3, the LED driving period is set to 1/4 of the LED driving period of Example 1. Although the LED driving period of Example 1 is shortened, it is possible to obtain the same  
20 effects even though the degrees of the effects somewhat vary.

Further, in Examples 1 to 3, the organic EL device LED has the cathode terminal grounded, and all the TFTs are formed of the n-type TFT (n-type thin film transistor). In a case of constituting the organic EL  
25 device LED with only the p-type TFT (p-type thin film transistor), the following configuration may be adopted.

The anode terminal of the organic EL device LED is connected to the power supply line VS, and the source terminals of p-type current mirror TFTs (first and second p-type thin film transistors of L-TFT and D-TFT) are connected to the cathode terminal of the organic EL device LED. A p-type TFT (TFT3) is provided between the source terminals of the L-TFT and the D-TFT and the power supply line VS. A p-type TFT (TFT4) is provided between the drain terminal of the L-TFT and the line DL for supplying the current corresponding to the gradation. A p-type TFT (TFT5) is provided between the drain terminal and the gate terminal of the L-TFT. The drain terminal of the D-TFT is connected to the power supply line to which the voltage GND is applied. Alternatively, the drain terminal of the D-TFT is connected to the power supply to which the voltage GND can be applied during the LED driving period and the voltage VS can be applied during the other period. More alternatively, the drain terminal of the D-TFT is connected to the power supply line to which the voltage GND is applied through the p-type TFT (TFT6). Then, the signals of the scan lines SL, SLA, and SLB are inverted. Thus, the same configuration as in Examples 1 to 3 can be realized and the same effects can be obtained.

Further, by adding the scan line in Examples 1 to 3, the TFT5 is caused to operate from the ON state to

the OFF state earliest among the TFTs which fulfill a switching function at the end of the first period. As a result, noise caused in association with operations of the other TFTs which fulfill the switching operation  
5 can be suppressed, and drive can be performed with high accuracy.

The present invention can be applied not only to a light emitting display device using an organic EL device, but also to a light emitting display device  
10 using a light emitting device other than the organic EL device, which emits light with a supplied current, and to a typical current load device using a current load which represents an arbitrary function by a supplied current.

15 While the present invention has been described with reference to exemplary embodiments, it is to be understood that the invention is not limited to the disclosed exemplary embodiments. The scope of the following claims is to be accorded the broadest  
20 interpretation so as to encompass all such modifications and equivalent structures and functions.

This application claims the benefit of Japanese Patent Application No. 2006-240257, filed September 5, 2006, which is hereby incorporated by reference herein  
25 in its entirety.

## CLAIMS

1. A light emitting display device comprising a pixel having a light emitting device and a drive circuit for driving the light emitting device, the  
5 drive circuit comprising:

a first thin film transistor and a second thin film transistor having the same polarity, a gate terminal of the first thin film transistor being connected to a gate terminal of the second thin film  
10 transistor, a source terminal of the first thin film transistor being connected to a source terminal of the second thin film transistor, and the source terminals of the first thin film transistor and the second thin film transistor being connected to one end of the light  
15 emitting device;

a first switch having one end connected to the source terminals of the first thin film transistor and the second thin film transistor and to one end of the light emitting device and having the other end  
20 connected to a first line; and

a capacitor having one end connected to the gate terminals of the first thin film transistor and the second thin film transistor and having the other end connected to the source terminals of the first thin  
25 film transistor and the second thin film transistor,

wherein the drive circuit has at least a first period for writing a drive signal and a second period

for driving the light emitting device after the first period,

wherein the first period contains a period in which the first line and the one end of the light emitting device are set to the same voltage through the first switch, and a second line for supplying the drive signal of the light emitting device is electrically connected to a drain terminal of the first thin film transistor and the gate terminals of the first thin film transistor and the second thin film transistor, to supply a current to the first thin film transistor from the second line, and

wherein the second period contains a period in which connection between the second line and the first thin film transistor, connection between the second line and the second thin film transistor, and the first switch are shut off.

2. A light emitting display device comprising a pixel having a light emitting device and a drive circuit for driving the light emitting device, the drive circuit comprising:

a first thin film transistor and a second thin film transistor having the same polarity, a gate terminal of the first thin film transistor being connected to a gate terminal of the second thin film transistor, a source terminal of the first thin film transistor being connected to a source terminal of the

second thin film transistor, and the source terminals of the first thin film transistor and the second thin film transistor being connected to one end of the light emitting device;

5           a first switch having one end connected to the source terminals of the first thin film transistor and the second thin film transistor and to one end of the light emitting device and having the other end connected to a first line; and

10           a capacitor having one end connected to the gate terminals of the first thin film transistor and the second thin film transistor and having the other end connected to the source terminals of the first thin film transistor and the second thin film transistor,

15           the drive circuit has at least a first period for writing a drive signal and a second period for driving the light emitting device after the first period,

          wherein, during the first period, the first line and one end of the light emitting device are set to the  
20           same voltage through the first switch, and a current from a second line for supplying the drive signal of the light emitting device is supplied to a drain terminal of the first thin film transistor and the gate  
          terminals of the first thin film transistor and the  
25           second thin film transistor to retain in the capacitor a voltage between the gate terminal and the source terminal of the second thin film transistor which is



determined based on a current flowing between the drain terminal and the source terminal of the first thin film transistor, and

wherein, during the second period, a current  
5 flowing between the source terminal and the drain terminal of the second thin film transistor is supplied to the light emitting device according to a retaining voltage of the capacitor.

3. A light emitting display device according to  
10 claim 2, wherein the drive circuit further comprises:

a second switch having one end connected to the second line and having the other end connected to the drain terminal of the first thin film transistor; and

a third switch having one end connected to the  
15 drain terminal of the first thin film transistor and having the other end connected to the gate terminal of the first thin film transistor; and

wherein the current from the second line is supplied to the drain terminal of the first thin film  
20 transistor and to the gate terminals of the first thin film transistor and the second thin film transistor through the second and third switches.

4. A light emitting display device according to claim 1, wherein a value obtained by dividing a channel  
25 width of the first thin film transistor by a channel length thereof is equal to or larger than a value obtained by dividing a channel width of the second thin

film transistor by a channel length thereof.

5. A light emitting display device according to claim 1, wherein the drive circuit further comprises a fourth switch between the third line and the drain  
5 terminal of the second thin film transistor.

6. A light emitting display device according to claim 1, wherein the capacitor has a capacitance value which is three times or more as large as a total  
10 capacitance and a gate and drain overlap capacitance of the first thin film transistor and a channel capacitance and a gate and drain overlap capacitance of the second thin film transistor.

7. A light emitting display device according to claim 1, wherein the first thin film transistor and the  
15 second thin film transistor are each comprised of a p-type thin film transistor; and wherein a cathode terminal of the light emitting device is connected to the source terminals of the first p-type thin film  
20 transistor and the second p-type thin film transistor.

8. A light emitting display device according to claim 1, wherein the first thin film transistor and the second thin film transistors are each comprised of an  
n-type thin film transistor; and wherein an anode  
25 terminal of the light emitting device is connected to the source terminals of the first thin film transistor and the second thin film transistor.

9. A light emitting display device according to claim 8, wherein the first switch, the second switch, the third switch, and the fourth switch are respectively comprised of a third thin film transistor, a fourth thin film transistor, a fifth thin film transistor, and a sixth thin film transistor; and wherein the third thin film transistor, the fourth thin film transistor, the fifth thin film transistor, and the sixth thin film transistor have the same polarity as that of the first thin film transistor and the second thin film transistor.

10. A light emitting display device according to claim 9, wherein the third thin film transistor, the fourth thin film transistor, and the fifth thin film transistor, respectively corresponding to the first switch, the second switch, and the third switch each have a gate terminal connected to a fourth line.

11. A light emitting display device according to claim 1, wherein a voltage which is equal to or lower than a working voltage of the light emitting device is applied to the first line.

12. A light emitting display device according to claim 1, wherein the drive circuit further comprises a current breaker which does not allow a current to flow between the source terminal and the drain terminal of the second thin film transistor in a part of at least one of the first period and the second period.

13. A light emitting display device according to claim 12, wherein the current breaker sets a voltage at the drain terminal of the second thin film transistor to the same potential as that of a voltage of the first  
5 line.

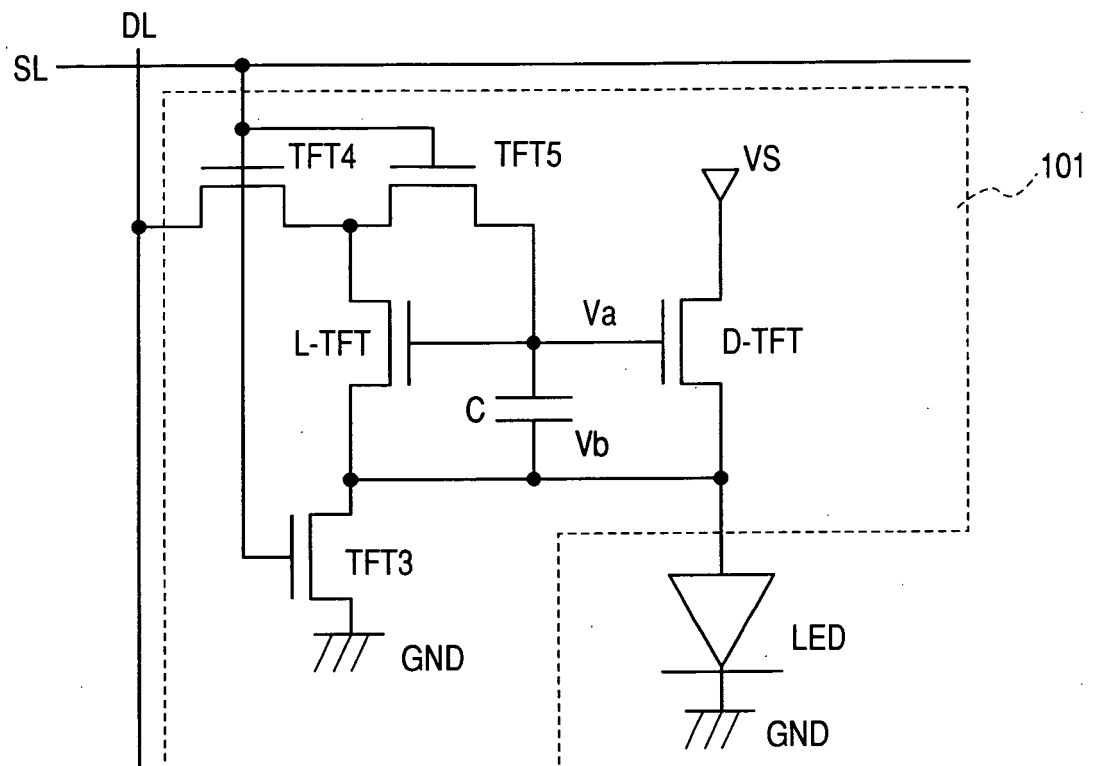
14. A light emitting display device according to claim 12, wherein the current breaker interrupts a current path of the second thin film transistor with the fourth switch.

10 15. A light emitting display device according to claim 1, wherein the light emitting device is an organic EL device.

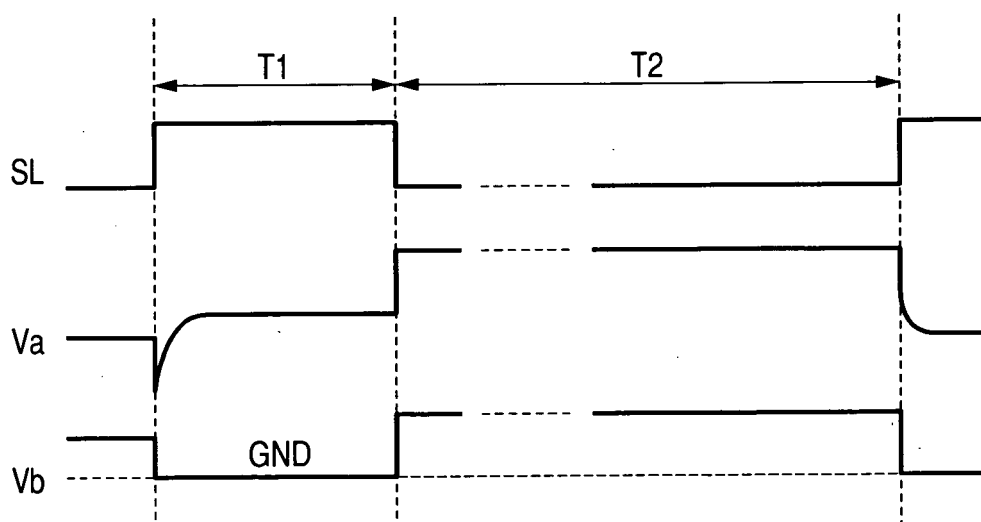
16. A light emitting display device according to claim 8, wherein the n-type thin film transistor of the  
15 drive circuit uses an n-type semiconductor film made of an amorphous metal oxide having a carrier density of  $10^{18}$  [cm<sup>-3</sup>] or less, as a channel film of the n-type thin film transistor, and has a mobility of 1 [cm<sup>2</sup>/Vs] or more and an on/off ratio of  $10^6$  or more.

20 17. A light emitting display device according to claim 1, wherein a plurality of the pixels are arranged on a substrate in a matrix form.

**FIG. 1**

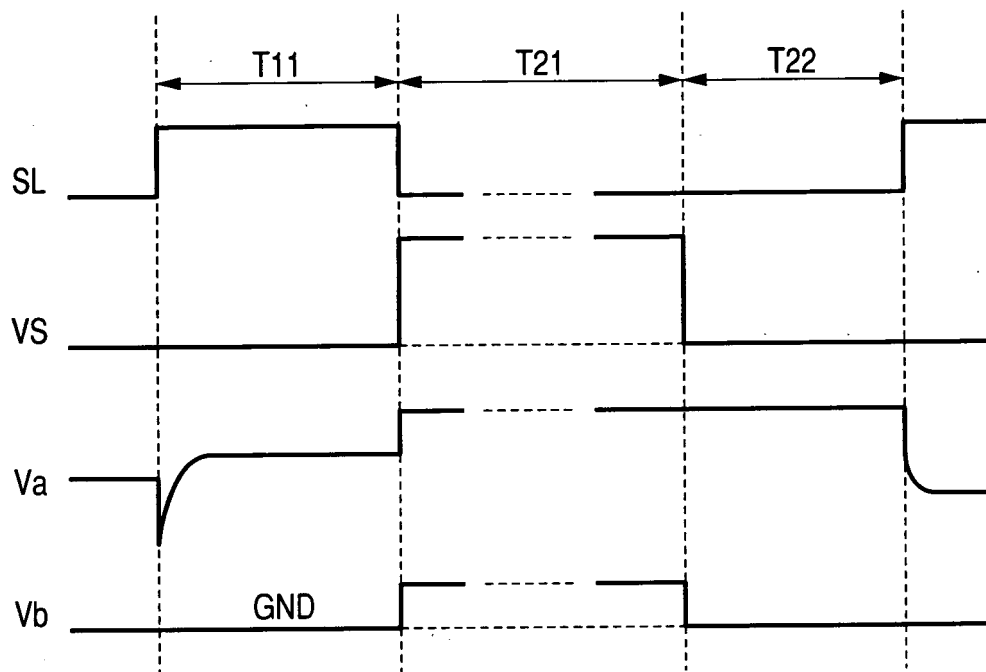


**FIG. 2**



T1; CURRENT WRITING PERIOD

**T2; LED DRIVING (LIGHT EMITTING) PERIOD**

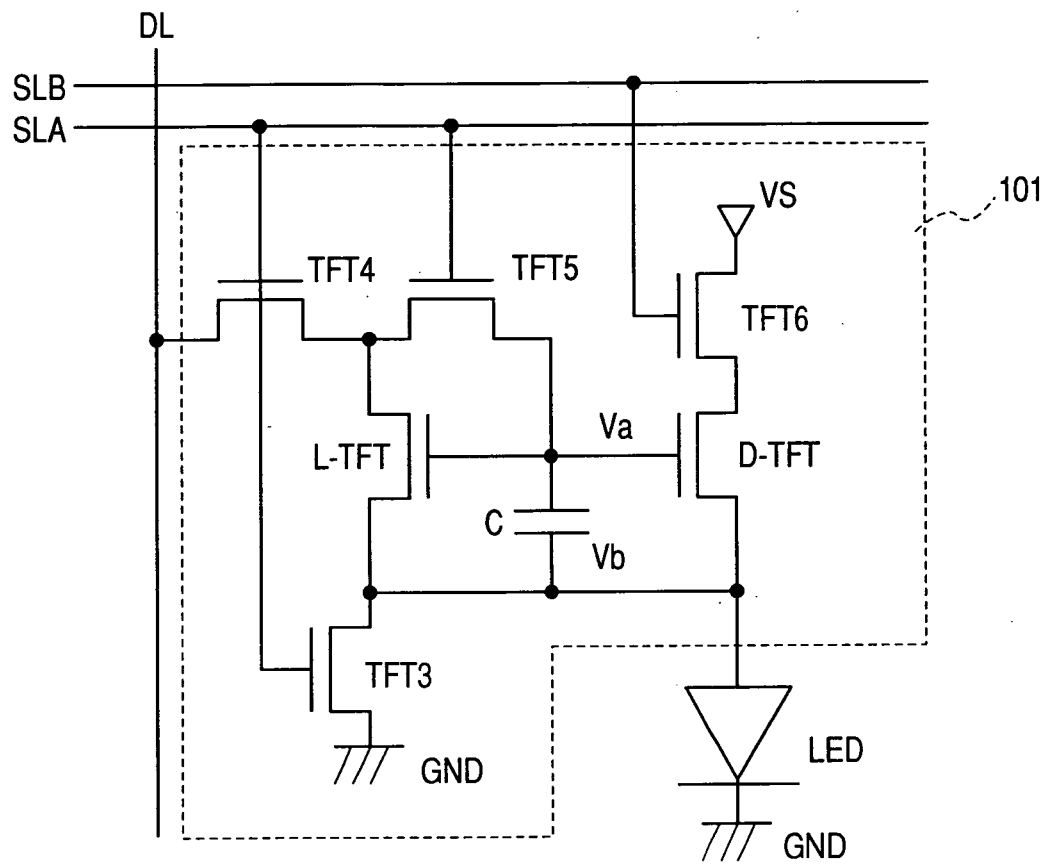
**FIG. 3**

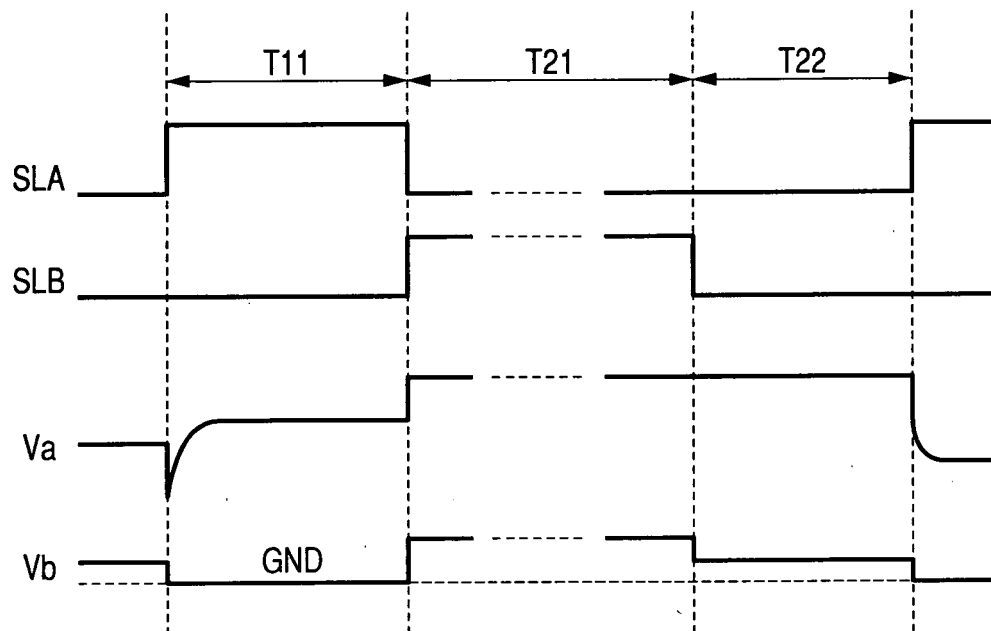
T11; CURRENT WRITING PERIOD

T21; LED DRIVING (LIGHT EMITTING) PERIOD

T22; DARK DISPLAYING PERIOD

FIG. 4



**FIG. 5**

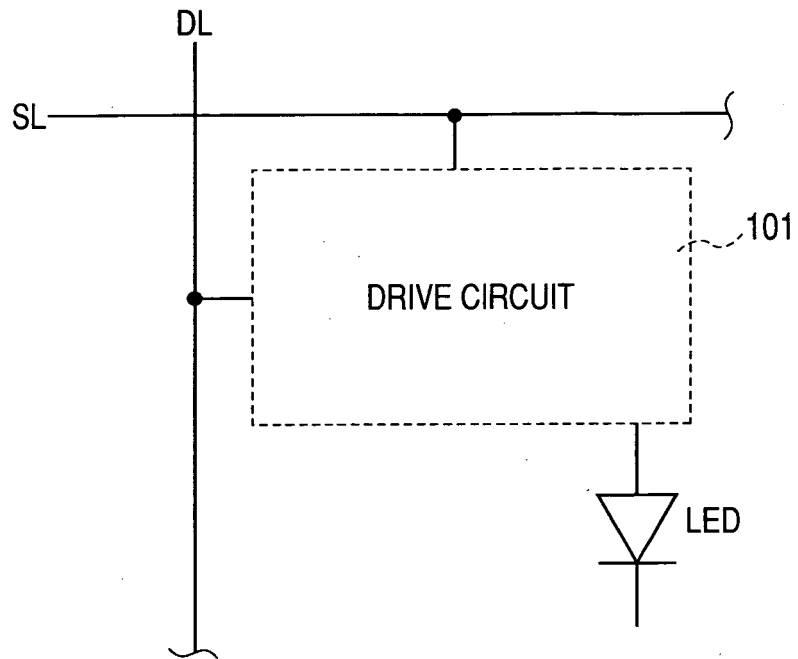
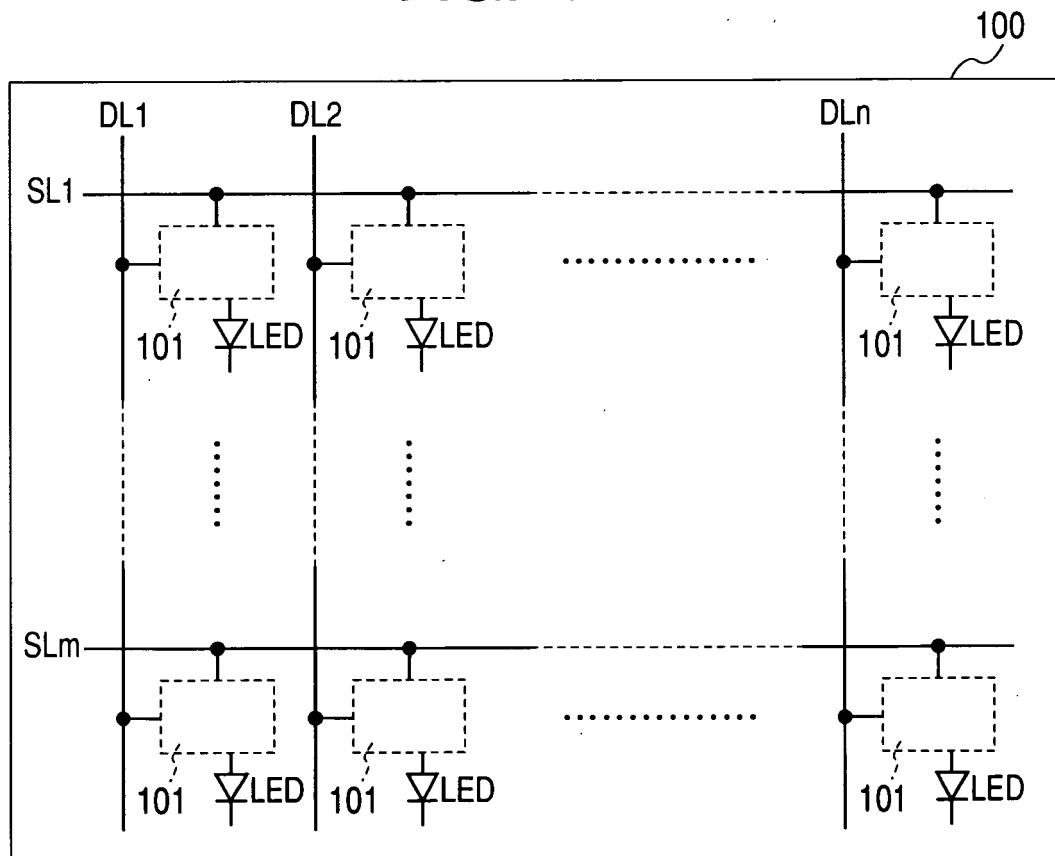
T11; CURRENT WRITING PERIOD

T21; LED DRIVING (LIGHT EMITTING) PERIOD

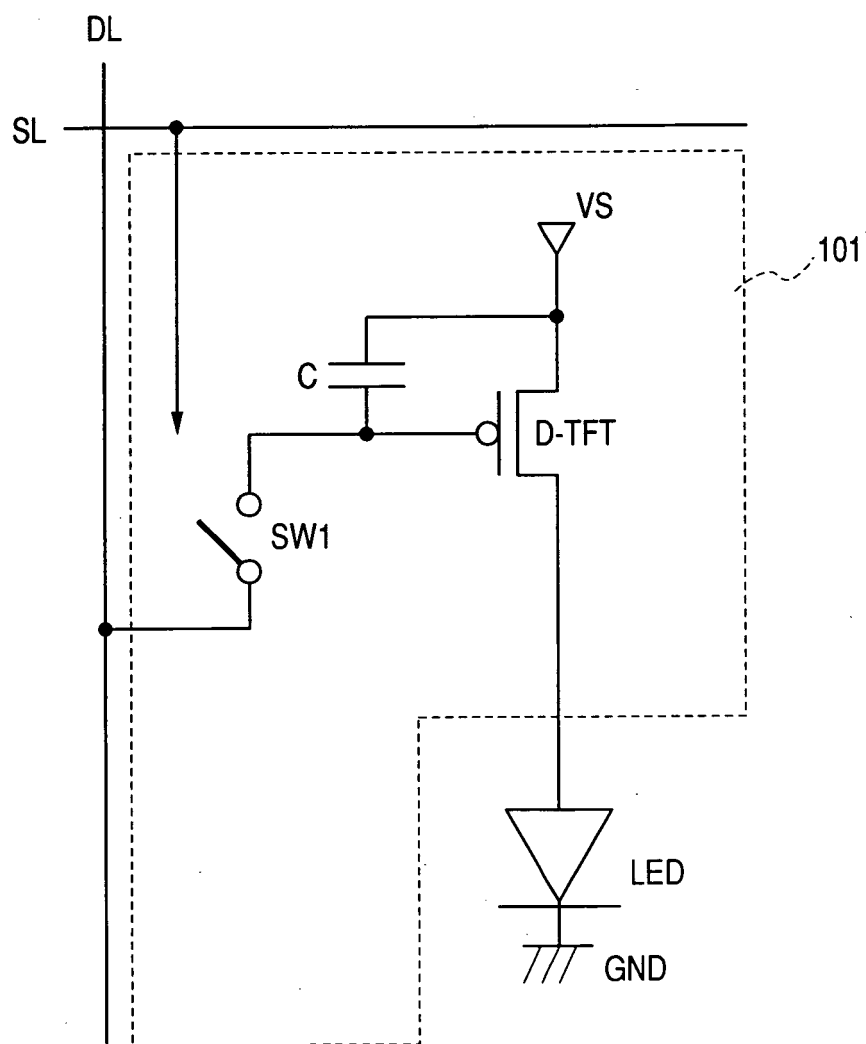
T22; DARK DISPLAYING PERIOD



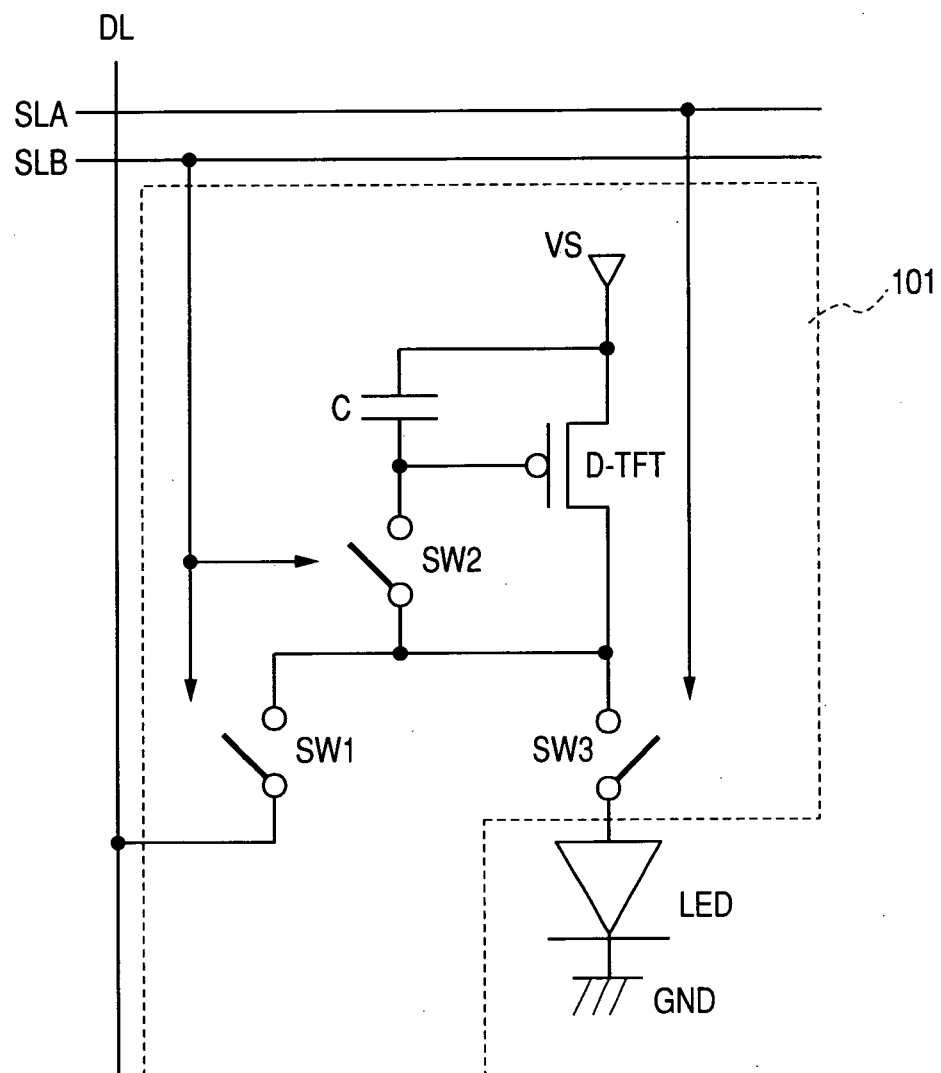
5/9

**FIG. 6****FIG. 7**

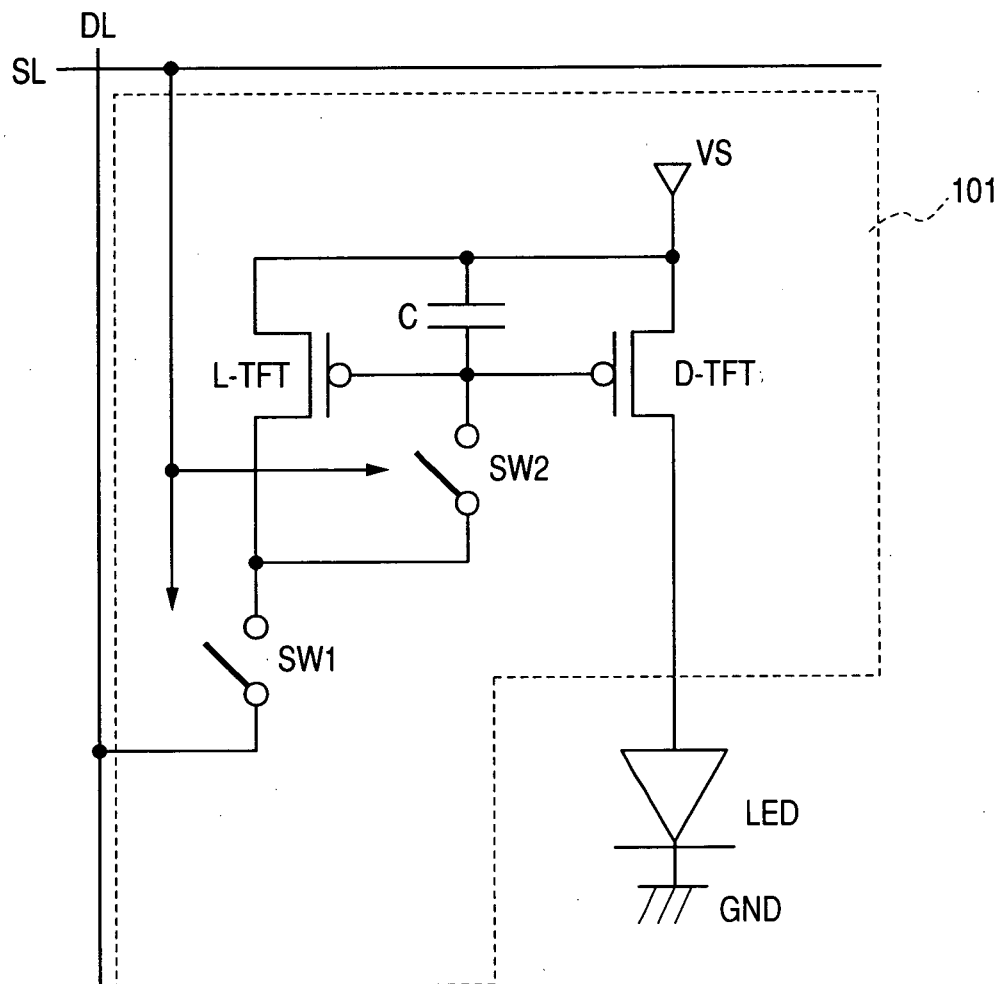
**FIG. 8**  
**PRIOR ART**



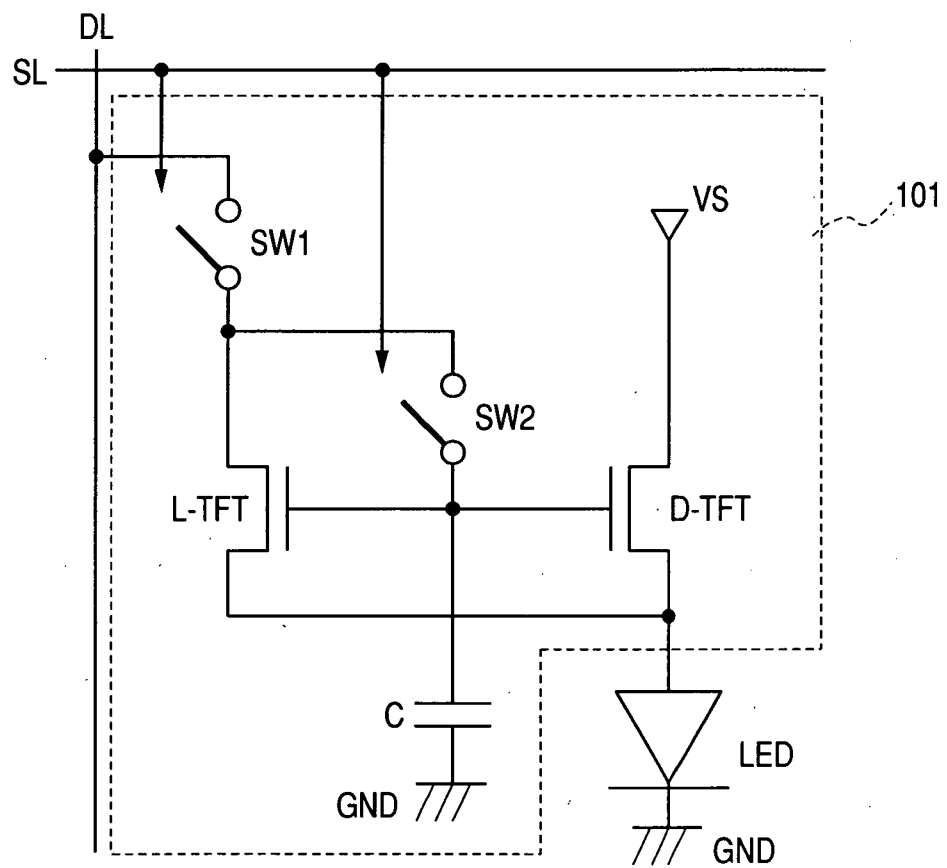
**FIG. 9**  
**PRIOR ART**



**FIG. 10**  
**PRIOR ART**



**FIG. 11**  
**PRIOR ART**



## INTERNATIONAL SEARCH REPORT

International application No

PCT/JP2007/066953

## A. CLASSIFICATION OF SUBJECT MATTER

INV. G09G3/32

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G09G

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2003/062524 A1 (KIMURA HAJIME [JP]) 3 April 2003 (2003-04-03) abstract paragraphs [0210] - [0261]; figures 37,38 -----	1-17
A	US 2005/093804 A1 (YAMAZAKI SHUNPEI [JP] ET AL) 5 May 2005 (2005-05-05) abstract paragraphs [0210] - [0288]; figures 18-20 -----	1-17
A	US 2005/180083 A1 (TAKAHARA HIROSHI [JP] ET AL) 18 August 2005 (2005-08-18) abstract paragraphs [0793] - [0805]; figures 38,39 -----	1-17

☐ Further documents are listed in the continuation of Box C.

See patent family annex.

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"&amp;" document member of the same patent family

Date of the actual completion of the international search

30 October 2007

Date of mailing of the international search report

12/11/2007

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Wolff, Lilian

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/JP2007/066953

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 2003062524	A1	03-04-2003	CN 1407629 A	02-04-2003
			KR 20030019194 A	06-03-2003
			TW 546600 B	11-08-2003
			US 2006256046 A1	16-11-2006
<hr/>				
US 2005093804	A1	05-05-2005	NONE	
<hr/>				
US 2005180083	A1	18-08-2005	CN 1662947 A	31-08-2005
			CN 1666242 A	07-09-2005
			WO 03091977 A1	06-11-2003
			WO 03091978 A1	06-11-2003
			KR 20070004109 A	05-01-2007
			KR 20070005007 A	09-01-2007
			KR 20070004133 A	05-01-2007
			KR 20070004136 A	05-01-2007
			KR 20070064438 A	20-06-2007
			TW 264691 B	21-10-2006
			US 2005168491 A1	04-08-2005
<hr/>				

专利名称(译)	有机发光显示装置		
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IPC分类号	G09G3/32		
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优先权	2006240257 2006-09-05 JP		
外部链接	<a href="#">Espacenet</a>		

#### 摘要(译)

一种发光显示装置，其使用仅由单极薄膜晶体管形成的驱动电路，其抑制晶体管的特性偏移的影响，并且适用于大型高分辨率发光显示器。该器件包括具有有机EL器件（LED）的像素及其驱动电路。在电流写入时段中，驱动电路将TFT3，TFT4和TFT5设置为ON，并通过TFT3将地线和LED的一端设置为相同的电压。来自数据线的电流被提供给晶体管L-TFT和D-TFT，形成通过TFT4和TFT5的电流镜电路，并且L-TFT和D-TFT的栅极和源极端子之间的电压保持在电容器中。在LED驱动期间，TFT3，TFT4和TFT5被中断，并且在D-TFT的源极和漏极之间流动的电流根据保持电压被提供给LED。