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### (54) **PIXEL UNIT CIRCUIT AND COMPENSATING METHOD THEREFOR, AND DISPLAY APPARATUS**

(57) A pixel unit circuit, a compensating method thereof and a display device. The pixel unit circuit comprises a driving transistor (T1), a first transistor (T2), a second transistor (T3), a third transistor (T4), a fourth transistor (T5), a storage capacitor ( $C_{ST}$ ) and a light-emitting device (OLED). The pixel unit circuit, the compensating method thereof and the display device may compensate the light emitting device by combining an internal compensation and an external compensation, and have advantages of both the internal compensation and the external compensation. The Mura phenomenon caused by non-uniformity in threshold voltages or drifts of threshold voltages in the N-type depletion or enhanced driving transistor TFT may be eliminated effectively by the internal compensation, which may enhance a display effect. Additionally, the pixel unit circuit, the compensating method thereof and the display device may have a function for extracting characteristics of the driving TFT and characteristics of the light emitting device, which may be applicable to the external compensation driving effectively.

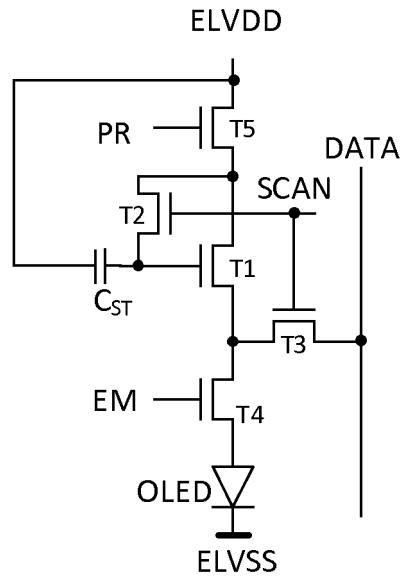


Fig.4

**Description****TECHNICAL FIELD**

5 [0001] The present disclosure relates to a field of display technique, and particularly, to a pixel unit circuit, a compensating method thereof and a display device.

**BACKGROUND**

10 [0002] As a current-type light-emitting device, Organic Light-Emitting Diodes (OLED) have been widely used in display devices with high performance. A traditional Passive Matrix OLED requires a shorter driving time for a single pixel as a display size increases, therefore a transient current should be increased and power consumption increases. Also, an application of a great current may lead to an over-large voltage drop on lines of nanometer Indium Tin Oxides (ITO), and cause an over-high operation voltage of the OLED, which may in turn decrease its efficiency. As compared, an  
15 Active Matrix OLED (AMOLED) may settle these problems perfectly by scanning input OLED currents progressively by means of switching transistors.

[0003] In a design for an array substrate of the AMOLED, a major problem needed to be settled is a non-uniformity in brightness among pixel unit circuits.

20 [0004] Firstly, the AMOLED constructs the pixel unit circuit with Thin-Film Transistors (TFTs) to provide a corresponding current to the OLED device. In the prior art, Low Temperature Poly-Silicon TFTs (LTPS TFTs) or Oxide TFTs are generally used. As compared with a general amorphous-Si TFT, the LTPS TFT and the Oxide TFT have a higher mobility and a better stability, and is more suitable to be applied to the AMOLED display. However, because of the limitation of the crystallization process, there is a disadvantage of non-uniformity in electric parameters such as threshold voltages, the mobility and the like while manufacturing LTPS TFTs on a glass substrate with a large area. Such non-uniformity may  
25 be transformed as a current difference and a brightness difference among the OLED display devices, and be perceived by viewer, which is called as a Mura phenomenon. The Oxide TFT has a good uniformity in the process, but similar to the a-Si TFT, the threshold voltage of the Oxide TFT would drift when a voltage is applied for a long time and under a high temperature. Amounts of the drift in the thresholds of the TFTs in respective parts on a panel would be different because displayed contents are different, which may lead to difference in the display brightness. Because such difference  
30 relates to an image displayed previously, it is generally shown as an image sticking phenomenon.

35 [0005] Secondly, in the display application with a large size, a power supply voltage at a region close to a supply position of an ARVDD power supply is higher as compared with that at a region far away from the power position in the array substrate, because power lines on the array substrate have certain resistances and the driving current for all pixels are provided by the power supply (ARVDD), and such phenomenon is called as power supply drop (IR Drop). The IR Drop may also lead to the current differences among the different regions and in turn generate the Mura phenomenon as display, since the voltage of the ARVDD power supply is associated with the current. The LTPS process constructing the pixel unit with P-Type TFTs is sensitive to this problem especially, because its storage capacitor is connected between the ARVDD and gates of the driving transistors TFTs, and a gate-source voltage Vgs of the driving transistor TFT would be affected directly when the voltage of the ARVDD changes.

40 [0006] Thirdly, the OLED device may also cause the non-uniformity in the electric performance because of a non-uniformity in thicknesses of a mask during an evaporation process. For the a-Si or Oxide TFT process constructing the pixel unit with N-Type TFTs, its storage capacitor is connected between a gate of a driving transistor TFT and an anode of the OLED, and the gate-source voltages Vgs applied to the driving transistors TFT would be different actually if the voltages at the anodes of the respective OLEDs are different when a data voltage is transferred to the gates of the  
45 respective driving transistors TFTs, such that the different driving currents may cause the difference in the display brightness.

50 [0007] The AMOLED may be divided into three categories based on the driving types: a digital type, a current type and a voltage type. The digital type driving method may implement gray scales by a manner of controlling driving timing with the TFTs as switches without compensating the non-uniformity, but its operation frequency would increase doubled and redoubled as the display size grows, which leads to a great power consumption, and reach a physical limitation of the design within a certain range, therefore it is not suitable for the display application with the large size. The current type driving method may implement the gray scales by a manner of providing the driving transistors TFTs with currents having different values directly, and may compensate the non-uniformity of the driving transistors TFTs and the IR drop better, but when a signal having a low gray scale is written, a over-long writing time may be raised because a small current charges a big parasitic capacitor on a data line. Such problem is especially serious and even can not be overcome in the display with the large size. The voltage type driving method is similar to a driving method for the traditional Active Matrix Liquid Crystal Display (AMLCD) and provides a voltage signal representing the gray scale by a driving IC, and the voltage signal may be transformed to a current signal for the driving transistors inside the pixel circuit so as to drive

the OLED to realize the luminance gray scales. Such method has advantages of a quick driving speed and simple implementation, which is suitable for driving the panel with the large size and widely used in industry, however it needs to design additional TFTs and capacitor devices to compensate the non-uniformity among the driving transistors TFTs, the IR Drop and the non-uniformity of OLEDs.

**[0008]** Fig.1 illustrates a pixel unit circuit in the prior art. As illustrated in Fig.1, the pixel unit circuit comprises two thin film transistors T2 and T1, and one capacitor C. The pixel unit circuit illustrated in Fig.1 is a typical structure for a pixel circuit of a voltage driving type (2T1C). Wherein the thin film transistor T2 operates as a switching transistor, transfers a voltage on a data line to a gate of the thin film transistor T1, which operates as a driving transistor, and the driving transistor transforms the data voltage to a corresponding current to be supplied to an OLED device. The driving transistor T1 should be in a saturation zone when it operates normally, and provide a constant current during a scanning period for one row. The current may be expressed as follows:

$$15 \quad I_{OLED} = \frac{1}{2} \mu_n \cdot C_{OX} \cdot \frac{W}{L} \cdot (V_{data} - V_{OLED} - V_{thn})^2.$$

**[0009]** Wherein  $\mu_n$  is a mobility of carriers,  $C_{OX}$  is a capacitance value of a capacitor in an oxide layer at the gate,  $\frac{W}{L}$  is a width-length ratio of the transistor,  $V_{data}$  is a signal voltage on the data line,  $V_{OLED}$  is an operation voltage of the OLED,  $V_{thn}$ , is a threshold voltage of the driving transistor TFT, which is a positive value for an enhanced TFT and is a negative value for a depletion TFT. It can be seen from the above equation that the currents would be different if the  $V_{thn}$  is different among the different pixel units. If the  $V_{thn}$  of the driving transistor TFT in a pixel unit drifts as time elapses, the currents before and after drifting would be different and the image sticking may occur. Also, the difference in the current may be also caused by difference in the operation voltages of the OLEDs because of a non-uniformity in the OLED devices.

**[0010]** There are many pixel structures for compensating the non-uniformity of the  $V_{thn}$ , the drift of the  $V_{thn}$  and the non-uniformity of the OLEDs, and they may be divided into two classes, an internal compensation and an external compensation, generally. The internal compensation is a compensation manner for, inside a pixel, storing information on the threshold voltage of the driving transistor TFT in the pixel with TFTs and a capacitor and feeding back the same to a bias voltage Vgs of the driving transistor TFT, and Fig.2a is a pixel unit circuit constituted by enhanced TFTs with the internal compensation manner in the prior art, while Fig.2b is a pixel unit circuit constituted by depletion TFTs with the internal compensation manner in the prior art. As illustrated in Figs.2a and 2b, the pixel unit circuit with the internal compensation manner in the prior art comprises a driving transistor, which is a thin film transistor, a gate and a source of the driving transistor are connected with each other, a drain of the driving transistor is connected with an anode of an OLED, and a cathode of the OLED is connected with a second power supply voltage ELVSS. Such structure is only applicable to the enhanced TFT, but for the depletion TFT, the TFT is still turned on when a voltage at the gate of the TFT is 0, therefore the voltage stored through the TFT would not include any information on the  $V_{thn}$  such that the non-uniformity in the  $V_{thn}$  can not be compensated.

**[0011]** Another compensation manner is the external compensation, that is, its compensation manner is as follows: I-V characteristics of the driving transistor and I-V characteristics of the light-emitting device are read to an external sensing circuit by TFTs inside the pixel, driving voltage value required to be compensated is calculated and fed back to a chip in a driving panel. Fig.3 is a pixel unit circuit with the external compensation manner in the prior art. As illustrated in Fig.3, the pixel unit circuit with the external compensation manner in the prior art comprises: an Active Matrix Organic Light-Emitting Diode (AMOLED), a display row selector, a sensor row selector, a column readout, an image processing LSI, an Analog-Digital Convertor (ADC) and an ASIC Processor (AP). Wherein the ASIC Processor (AP) provides display data to the image processing LSI, the AMOLED comprises an array of pixel unit circuits and reads out the currents or voltages of the respective pixel unit circuits by the column readout. As illustrated in Fig.3, a triangle frame between the column readout and the ADC represents an amplifying and compensating circuit. Given a data voltage as a reference voltage, when the voltage flowing out from the column readout is smaller than the reference voltage, it indicates that the voltage of the pixel unit circuit at this position is needed to be compensated, and the voltage from the column readout is compensated by the amplifying and compensating circuit, so that the voltage or current of the driving transistor and/or the OLED device in the corresponding pixel unit circuit may be compensated.

**[0012]** The internal compensation and the external compensation have their own advantages and disadvantages. Generally, the internal compensation may only compensate the non-uniformity and the drifts of the threshold voltages of the driving transistor TFTs under limitations of a limited space and a circuit structure, while the external compensation may compensate the non-uniformity in the threshold voltages and the non-uniformity in the mobility of the driving transistor

TFTs, and may also compensate some nonideal factors such as an ageing of the OLED, by implementing complex algorithm by means of the external integrated circuit chip(s). However, a compensation range of the external compensation is limited, its compensating voltage can not exceed a maximum range for voltage on the data line (DATA), while an internal driving voltage obtained by the internal compensation circuit may exceed the maximum range for the voltage on the data line. If the internal compensation and the external compensation may be combined with each other, their advantages may be acquired together.

## SUMMARY

[0013] The present disclosure provides a pixel unit circuit, a compensating method thereof and a display device, which may settle a problem in the pixel unit circuit of the prior art that an internal compensation and an external compensation can not to be combined, may settle a problem of non-uniformity in threshold voltages of driving transistors of light-emitting devices and the corresponding pixel unit circuits occurred when a compensation is performed, and may have an extraction function for circuit characteristics of the driving transistors and the light-emitting devices so as to help implementation of the external compensation and realize an object for eliminating the Mura phenomenon in the display device finally.

[0014] In embodiments of the present disclosure, there is provided a pixel unit circuit comprising a driving transistor, a first transistor, a second transistor, a third transistor, a fourth transistor, a storage capacitor and a light-emitting device, wherein,

a drain of the driving transistor is connected with a source of the fourth transistor, a source thereof is connected with a drain of the third transistor, and a gate thereof is connected with a first terminal of the storage capacitor and a source of the first transistor;

a drain of the first transistor is connected with the source of the fourth transistor, the source thereof is connected with the gate of the driving transistor, and a gate thereof is connected with a scan control signal line;

a drain of the second transistor is connected with a data line, a source thereof is connected with the source of the driving transistor and the drain of the third transistor, and a gate thereof is connected with the scan control signal line;

the drain of the third transistor is connected with the source of the driving transistor, a source thereof is connected with an anode of the light-emitting device, and a gate thereof is connected with a light-emitting control signal line;

a drain of the fourth transistor is connected with a first power supply voltage, the source thereof is connected with the drain of the driving transistor and the drain of the first transistor, and a gate thereof is connected with a pre-charging control signal line;

the first terminal of the storage capacitor is connected with the gate of the driving transistor, and a second terminal thereof is connected with the first power supply voltage;

a cathode of the light-emitting device is connected with a second power supply voltage.

[0015] Further, in the pixel unit circuit according to the embodiments of the present disclosure, the light-emitting device is an Organic Light-Emitting Diode device.

[0016] Further, in the embodiments of the present disclosure, there is further provided a compensating method for the pixel unit circuit, comprising:

selecting a compensation manner according to an operation stage of a light-emitting device, wherein the compensation manner comprises an internal compensation manner and an external compensation manner;

compensating the light-emitting device with the internal compensation manner, if the light-emitting device is in an operation stage for light-emitting normally; and

compensating the light-emitting device with the external compensation manner, if the light-emitting device is in an operation stage of a panel reset or an operation stage of an idle display between frames or rows.

[0017] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, said compensating the light-emitting device with the internal compensation manner further comprises:

pre-charging the driving transistor;

performing a voltage compensation or a current compensation on the driving transistor; and

performing a voltage compensation or a current compensation on the light-emitting device, in order to remain the light-emitting device to emit light.

[0018] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, said pre-charging the driving transistor further comprises:

setting a light-emitting control signal as a low level to turn off the third transistor; setting a pre-charging control signal as a high level to turn on the fourth transistor; setting a scan control signal as a high level to turn on the first transistor

and the second transistor; and making a voltage at the source of the driving transistor be a voltage  $V_{DATA}$  on the data line.

5 [0019] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, said performing a voltage compensation or a current compensation on the driving transistor further comprises:

10 setting a light-emitting control signal as a low level to turn off the third transistor; setting a pre-charging control signal as a low level to turn off the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and making a voltage at the gate of the driving transistor be  $V_{DATA} + V_{thn}$ , wherein  $V_{DATA}$  is the voltage on the data line, and  $V_{thn}$  is a threshold voltage of the driving transistor.

15 [0020] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, said performing a voltage compensation or a current compensation on the light-emitting device in order to remain the light-emitting device to emit light further comprises:

20 setting a light-emitting control signal as a high level to turn on the third transistor; setting a pre-charging control signal as a high level to turn on the fourth transistor; setting a scan control signal as a low level to turn off the first transistor and the second transistor; and making a current  $I_{OLED}$  input to the light-emitting device through the driving transistor be:

25 wherein  $\mu_n$  is a mobility of carriers,  $C_{OX}$  is a capacitance value of the storage capacitor in an oxide layer at the gate,  $\frac{W}{L}$  is a width-length ratio of the driving transistor,  $V_{DATA}$  is a voltage on the data line,  $V_{OLED}$  is an anode voltage of the light-emitting device.

30 [0021] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, said compensating the light-emitting device with the external compensation manner further comprises:

35 extracting a current from the driving transistor;  
extracting a current from the light-emitting device; and  
detecting the current extracted from the driving transistor or the light-emitting device, and performing a voltage compensation or a current compensation on the light-emitting device according to a value of the detected current.

40 [0022] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, said extracting a current from the driving transistor further comprises:

45 setting a light-emitting control signal as a low level to turn off the third transistor; setting a pre-charging control signal as a high level to turn on the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and enabling a current of the driving transistor to be input to the data line while shielding a current of the light-emitting device from being input to the data line.

55 [0023] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, said extracting a current from the light-emitting device further comprises:

50 setting a light-emitting control signal as a high level to turn on the third transistor; setting a pre-charging control signal as a low level to turn off the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and enabling a current of the light-emitting device to be input to the data line while shielding a current of the driving transistor from being input to the data line.

55 [0024] Further, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, the light-emitting device is an Organic Light-Emitting Diode device.

[0025] In the embodiments of the present disclosure, there is further provided a display device comprising the pixel unit circuit according to the embodiments of the present disclosure.

[0026] With the pixel unit circuit, the compensating method thereof and the display device according to the embodiments of the present disclosure, following benefit effects may be acquired.

[0027] First, the pixel unit circuit and the compensating method thereof according to the embodiments of the present disclosure may compensate the OLED device by combining the internal compensation and the external compensation, and have advantages of both the internal compensation and the external compensation. The Mura phenomenon caused by the non-uniformity in the threshold voltages or their drifts in the N-type depletion or enhanced driving transistor TFT may be eliminated effectively by the internal compensation, which may enhance a display effect. Additionally, the pixel unit circuit and the compensating method thereof according to the embodiments of the present disclosure may have a function for extracting characteristics of the driving TFT and characteristics of the OLED, which may be applicable to the external compensation driving effectively.

[0028] Second, the pixel unit circuit and the compensating method thereof according to the embodiments of the present disclosure may compensate a current difference among different regions caused by the IR drop and enhance the display effect.

[0029] Third, the display device according to the embodiments of the present disclosure may further eliminate the Mura phenomenon and enhance the display effect on the display device by utilizing the pixel unit circuit according to the embodiments of the present disclosure.

#### BRIEF DESCRIPTION OF THE DRAWINGS

##### [0030]

Fig.1 is a pixel unit circuit in the prior art;

Fig.2 is a pixel unit circuit with an internal compensation manner in the prior art;

Fig.3 is a pixel unit circuit with an external compensation manner in the prior art;

Fig.4 is a circuit diagram illustrating a pixel unit circuit according to embodiments of the present disclosure;

Fig.5 is a flowchart illustrating a compensating method for the pixel unit circuit according to the embodiments of the present disclosure;

Fig.6 is a flowchart illustrating the compensating method for the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure;

Fig.7 is an equivalent circuit diagram illustrating the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure;

Fig.8 is a control signal timing diagram of the compensating method for the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure;

Fig.9 is a flowchart illustrating the compensating method for the pixel unit circuit under the external compensation manner according to the embodiments of the present disclosure;

Fig.10 is an equivalent circuit diagram illustrating the pixel unit circuit under the external compensation manner according to the embodiments of the present disclosure; and

Fig.11 is a control signal timing diagram of the compensating method for the pixel unit circuit under the external compensation manner according to the embodiments of the present disclosure.

#### DETAILED DESCRIPTION

[0031] In order to understand the present disclosure better, the present disclosure would be described below in connection with accompanying drawings and embodiments of the present disclosure.

[0032] A pixel unit circuit according to the embodiments of the present disclosure is mostly used for a driving compensation of a light-emitting device OLED, driving for each of the light-emitting devices is compensated by one pixel unit circuit, and each of pixel unit circuits is structured by connecting 5 thin film transistors and 1 transistor to the light-emitting device. This structure may be used for both of an internal compensation and an external compensation. A display process for the internal compensation is divided into 3 sub-processes, and they are a precharging sub-process, a compensating sub-process and a displaying sub-process, respectively. The external compensation is divided into 2 sub-processes, and they are a current-extraction sub-process of a driving transistor TFT and a current-extraction sub-process of the light-emitting device, respectively. As compared the traditional pixel structure, the pixel unit circuit according to the embodiments of the present disclosure may compensate the drifts and non-uniformity in the threshold voltages of the enhanced-type or depletion-type driving transistor TFT, and the non-uniformity in the voltages and an ageing of the light-emitting device.

[0033] In the pixel unit circuit according to the embodiments of the present disclosure, the light-emitting device at its output terminal may be an AMOLED. The pixel unit circuit may eliminate the non-uniformity in the threshold voltages in the N-type depletion or enhanced driving transistor TFT effectively by the internal compensation, which may enhance

a display effect. Additionally, the pixel unit circuit according to the embodiments of the present disclosure may have a function for extracting characteristics of the driving transistor TFT and characteristics of the light-emitting device, which may be applicable to the external compensation driving effectively. The light-emitting device herein refers to an OLED device, and the characteristics of the light-emitting device refer to voltage-current characteristics of the OLED device.

5 [0034] Fig.4 is a circuit diagram illustrating the pixel unit circuit according to embodiments of the present disclosure. As illustrated in Fig.4, the pixel unit circuit according to the embodiments of the present disclosure comprises a driving transistor T1, a first transistor T2, a second transistor T3, a third transistor T4, a fourth transistor T5, a storage capacitor  $C_{ST}$  and a light-emitting device, and the light-emitting device is an Organic Light-Emitting Diode OLED device.

10 [0035] The driving transistor T1 is used for driving the light-emitting device. In an example, a drain of the driving transistor T1 is connected with a source of the fourth transistor T5, a source thereof is connected with a drain of the third transistor T4, and a gate thereof is connected with a first terminal of the storage capacitor  $C_{ST}$  and a source of the first transistor T2.

15 [0036] The first transistor T2 is a control switch for a scan control signal. In an example, a drain of the first transistor T2 is connected with the source of the fourth transistor T5, the source thereof is connected with the gate of the driving transistor T1, and a gate thereof is connected with a scan control signal line SCAN.

[0037] The second transistor T3 is another control switch for the scan control signal. In an example, a drain of the second transistor T3 is connected with a data line DATA, a source thereof is connected with the source of the driving transistor T1 and the drain of the third transistor T4, and a gate thereof is connected with the scan control signal line SCAN.

20 [0038] The third transistor T4 is a control switch for a light-emitting control signal. In an example, the drain of the third transistor T4 is connected with the source of the driving transistor T1, a source thereof is connected with an anode of the light-emitting device OLED, and a gate thereof is connected with a light-emitting control signal line EM.

25 [0039] The fourth transistor T5 is a control switch for a pre-charging control signal. In an example, a drain of the fourth transistor T5 is connected with a first power supply voltage ELVDD, the source thereof is connected with the drain of the driving transistor T1 and the drain of the first transistor T2, and a gate thereof is connected with a pre-charging control signal line PR.

[0040] The first terminal of the storage capacitor  $C_{ST}$  is connected with the gate of the driving transistor T1, and a second terminal thereof is connected with the first power supply voltage ELVDD.

[0041] A cathode of the light-emitting device OLED is connected with a second power supply voltage ELVSS.

30 [0042] The second power supply voltage ELVSS is a voltage supplied to the cathode of the light-emitting device, and is within a range between -5V to 0V generally and may be acquired by an actual test.

[0043] Further, Fig.5 is a flowchart illustrating a compensating method for the pixel unit circuit according to the embodiments of the present disclosure, as illustrated in Fig.5, the method comprises:

35 at a step S100, selecting a compensation manner according to an operation stage of a light-emitting device, wherein the compensation manner comprises an internal compensation manner and an external compensation manner;  
 at a step S200, compensating the light-emitting device with the internal compensation manner, if the light-emitting device is in an operation stage for light-emitting normally; and  
 40 at a step S300, compensating the light-emitting device with the external compensation manner, if the light-emitting device is in an operation stage of a PANEL RESET or an operation stage of an idle display between frames or rows, wherein the light-emitting device is an Organic Light-Emitting Diode device OLED.

[0044] Furthermore, Fig.6 is a flowchart illustrating the compensating method for the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure. As illustrated in Fig.6, in the step S200, the step of compensating the light-emitting device with the internal compensation manner further comprises:

45 at a step S210, pre-charging the drain of the driving transistor;  
 at a step S220, performing a voltage compensation or a current compensation on the gate of the driving transistor; and  
 at a step S230, performing a voltage compensation or a current compensation on the light-emitting device, in order to remain the light-emitting device to emit light.

50 [0045] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, the step of pre-charging the drain of the driving transistor in the step S210 further comprises:

55 setting a light-emitting control signal EM as a low level to turn off the third transistor; setting a pre-charging control signal PR as a high level to turn on the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and making a voltage at the source of the driving transistor be a voltage  $V_{DATA}$  on the data line.

[0046] Fig.7 is an equivalent circuit diagram illustrating the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure. As illustrated in (a) of Fig.7, in a precharging stage: the driving transistor T1, the first transistor T2, the second transistor T3 and the fourth transistor T5 are turned on, and the third transistor T4 is turned off; the voltage on the data line is the signal voltage  $V_{DATA}$  of a current frame on the data line, electronic charges stored in the capacitor  $C_{ST}$  are released, so that the source of the driving transistor T1 is precharged to a high level, that is, the voltage  $V_{DATA}$  on the data line.

[0047] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, the step of performing a voltage compensation or a current compensation on the gate of the driving transistor in the step S220 further comprises:

10 setting the light-emitting control signal as a low level to turn off the third transistor; setting the pre-charging control signal as a low level to turn off the fourth transistor; setting the scan control signal as a high level to turn on the first transistor and the second transistor; and making a voltage at the gate of the driving transistor be  $V_{DATA} + V_{thn}$ , wherein  $V_{DATA}$  is the voltage on the data line, and  $V_{thn}$  is a threshold voltage of the driving transistor.

15 [0048] Fig.7 is the equivalent circuit diagram illustrating the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure. As illustrated in (b) of Fig.7, in a compensating stage: the driving transistor T1, the first transistor T2 and the second transistor T3 are turned on, and the third transistor T4 and the fourth transistor T5 are turned off, the gate of the driving transistor T1 is discharged until the voltage at the gate of the driving transistor T1 is equal to  $V_{DATA} + V_{thn}$ , and at this time the transistor precharged is compensated, the electronic charges stored across the two terminals of the storage capacitor  $C_{ST}$  are equal to  $(V_{ELVDD} - V_{thn} - V_{DATA}) * C_{ST}$ , wherein  $V_{ELVDD}$  is a voltage of the first power supply voltage ELVDD,  $C_{ST}$  is a capacitance value of the storage capacitor  $C_{ST}$  in the oxide layer at the gate,  $V_{thn}$  is the threshold voltage of the driving transistor T1, and  $V_{DATA}$  is the signal voltage on the data line.

20 [0049] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, the step of performing a voltage compensation or a current compensation on the light-emitting device in order to remain the light-emitting device to emit light in the step S230 further comprises:

25 setting the light-emitting control signal as a high level to turn on the third transistor; setting the pre-charging control signal as a high level to turn on the fourth transistor; setting the scan control signal as a low level to turn off the first transistor and the second transistor; and making a current  $I_{OLED}$  input to the light-emitting device through the driving transistor be:

$$30 I_{OLED} = \frac{1}{2} \cdot \mu_n C_{OX} \cdot \frac{W}{L} \cdot [V_{DATA} - V_{OLED}]^2,$$

35 wherein  $\mu_n$  is a mobility of carriers,  $C_{OX}$  is a capacitance value of the storage capacitor in an oxide layer at the gate,  $\frac{W}{L}$  is a width-length ratio of the driving transistor,  $V_{DATA}$  is a voltage on the data line,  $V_{OLED}$  is a voltage at an anode of the light-emitting device.

40 [0050] Fig.7 is the equivalent circuit diagram illustrating the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure. As illustrated in (c) of Fig.7, in a light-emitting stage: the driving transistor T1, the third transistor T4 and the fourth transistor T5 are turned on, and the first transistor T2 and the second transistor T3 are turned off, the storage capacitor  $C_{ST}$  is connected between the gate of the driving transistor T1 and the first power supply voltage ELVDD and remains the voltage at the gate of the driving transistor T1 to be  $V_{DATA} + V_{thn}$ , wherein  $V_{thn}$  is the threshold voltage of the driving transistor T1, and  $V_{DATA}$  is the signal voltage at the data line; at this time, the data line is disconnected with the pixel unit circuit, the voltage at the source of the driving transistor T1 changes to  $V_{OLED}$  as the current of the OLED device begins to be stable, and the voltage at the gate of the driving transistor T1 is remained as  $V_{DATA} + V_{thn}$ , so the current  $I_{OLED}$  flowing through the thin film transistor T1 is as follows:

$$\begin{aligned}
 I_{OLED} &= \frac{1}{2} \cdot \mu_n C_{ox} \cdot \frac{W}{L} \cdot [V_{DATA} + V_{thn} - V_{thn} - V_{OLED}]^2 \\
 &= \frac{1}{2} \cdot \mu_n C_{ox} \cdot \frac{W}{L} \cdot [V_{DATA} - V_{OLED}]^2 \\
 &,
 \end{aligned}$$

5 wherein  $\mu_n$  is the mobility of carriers,  $C_{ox}$  is the capacitance value of the storage capacitor  $C_{ST}$  in the oxide layer at the  
 10 gate,  $\frac{W}{L}$  is the width-length ratio of the driving transistor T1,  $V_{DATA}$  is the signal voltage on the data line,  $V_{OLED}$  is the  
 15 voltage at the anode of the OLED device, that is, the operation voltage of the OLED device,  $V_{thn}$  is the threshold voltage  
 20 of the driving transistor T1, which is the positive value for an enhanced TFT transistor and is a negative value for a  
 25 depletion TFT transistor.

**[0051]** It can be seen from the above equation that the current flowing through the driving transistor is independent of its threshold voltage  $V_{thn}$ , and is also independent of the voltage across the light-emitting device, thus the effect caused by the non-uniformity in the threshold voltages and their drifts of the driving transistors is eliminated mainly. The pixel unit circuit according to the embodiments of the present disclosure may compensate the effect caused by the non-uniformity in the threshold voltages of the driving transistors both for the enhanced thin film transistor and for the depletion thin film transistor, therefore its applicability is wider.

**[0052]** Fig.8 is a control signal timing diagram of the compensating method for the pixel unit circuit under the internal compensation manner according to the embodiments of the present disclosure. As illustrated in Fig.8, in the internal compensation, the control timings for the light-emitting control signal EM, the pre-charging control signal PR and the scan control signal SCAN are:

30 during the precharging stage, which corresponds to the step S210, the light-emitting control signal EM is in a low level, the pre-charging control signal PR and the scan control signal SCAN are in a high level;  
 35 during the compensating stage, which corresponds to the step S220, the light-emitting control signal EM and the pre-charging control signal PR are in a low level, and the scan control signal SCAN is in a high level; and  
 40 during the light-emitting stage, which corresponds to the step S230, the light-emitting control signal EM and the pre-charging control signal PR are in a high level, and the scan control signal SCAN are in a low level.

**[0053]** Further, the compensating method for the pixel unit circuit according to the embodiments of the present disclosure further comprises compensating the light-emitting device under the external compensation manner. The external compensation occurs mainly during an operation stage of a PANEL RESET or during an operation stage of an idle display between frames or rows. For example, the PANEL RESET may occur at a moment of power on. The process of the external compensation is divided into two stages: the current extraction of the driving transistor and the current extraction of the light-emitting device.

**[0054]** Further, Fig.9 is a flowchart illustrating the compensating method for the pixel unit circuit under the external compensation manner according to the embodiments of the present disclosure. As illustrated in Fig.9, in the step S300, the step of compensating the light-emitting device with the external compensation manner further comprises:

45 at a step S310, extracting a current from the driving transistor;  
 at a step of S320, extracting a current from the light-emitting device; and  
 at a step S330, detecting the current extracted from the driving transistor or the light-emitting device, and performing a voltage compensation or a current compensation on the light-emitting device according to a value of the detected current.

50 **[0055]** Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, the step of extracting a current from the driving transistor in the step S310 further comprises:

55 setting a light-emitting control signal as a low level to turn off the third transistor; setting a pre-charging control signal as a high level to turn on the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and enabling a current of the driving transistor to be input to the data line while shielding a current of the light-emitting device from being input to the data line. A value of the current flowing through the driving transistor is detected by a sensing chip connected to the data line.

[0056] The voltage of the signal on the data line is denoted as a reference voltage  $V_{REF}$ , and  $V_{REF} < V_{ELVDD}$ , wherein  $V_{ELVDD}$  is the voltage of the power supply  $ELVDD$ . Fig.10 is an equivalent circuit diagram illustrating the pixel unit circuit under the external compensation manner according to the embodiments of the present disclosure. As illustrated in (a) of Fig.10, the light-emitting device is the OLED device, the driving transistor T1, the first transistor T2, the second transistor T3 and the fourth transistor T5 are turned on, the third transistor T4 is turned off; at this time, the OLED device is disconnected with the driving transistor T1, the voltage between the gate and the source of the driving transistor T1 is biased as  $V_{ELVDD} - V_{REF}$ , and the driving current of the driving transistor T1 flows into the data line through the second transistor T3, so that the external sensing chip connected to the data line may sense this current value and perform further processing.

[0057] Furthermore, in the compensating method for the pixel unit circuit according to the embodiments of the present disclosure, the step of extracting a current from the light-emitting device in the step S320 further comprises:

setting the light-emitting control signal as a high level to turn on the third transistor; setting the pre-charging control signal as a low level to turn off the fourth transistor; setting the scan control signal as a high level to turn on the second transistor; and enabling a current of the light-emitting device to be input to the data line while shielding a current of the driving transistor from being input to the data line. The value of a current flowing through the light-emitting device is detected by the sensing chip connected to the data line.

[0058] The voltage of the signal on the data line is denoted as the reference voltage  $V_{REF}$ , and  $V_{REF} > V_{thn}$ , wherein  $V_{thn}$  is the threshold voltage of the driving transistor T1. Fig.10 is the equivalent circuit diagram illustrating the pixel unit circuit under the external compensation manner according to the embodiments of the present disclosure. As illustrated in (b) of Fig.10, the light-emitting device is the OLED device, the first transistor T2, the second transistor T3 and the third transistor T4 are turned on at this time, the driving transistor T1 and the fourth transistor T5 are turned off; a voltage difference between the anode and the cathode of the OLED device is biased as  $V_{REF} - V_{ELVSS}$ , and the current flowing through the OLED device is input into the data line through the second transistor T3, so that the external sensing chip connected to the data line may sense this current value and perform further processing.

[0059] Fig.11 is a control signal timing diagram of the compensating method for the pixel unit circuit under the external compensation manner according to the embodiments of the present disclosure. As illustrated in Fig.11, the control timings for the light-emitting control signal EM, the pre-charging control signal PR and the scan control signal SCAN are:

30 during a first stage, which corresponds to the step S310 for extracting a current from the driving transistor T1, the light-emitting control signal EM is in a low level, the pre-charging control signal PR and the scan control signal SCAN are in a high level; and

35 during a second stage, which corresponds to the step S320 for extracting a current from the OLED device, the light-emitting control signal EM and the scan control signal SCAN are in a high level, and the pre-charging control signal PR is in a low level.

[0060] It can be seen from above, such pixel unit circuit may be operated with the two operation modes of the internal compensation and the external compensation, therefore its compensation effect may have the advantages of both of them.

[0061] In the embodiments of the present disclosure, there is further provided a display device comprising the pixel unit circuit according to the embodiments of the present disclosure and compensating the pixel unit circuit by the compensating method according to the embodiments of the present disclosure.

[0062] The embodiment of the invention being thus described, it will be obvious that the same may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to those skilled in the art are intended to be included within the scope of the following claims.

## Claims

1. A pixel unit circuit comprising a driving transistor, a first transistor, a second transistor, a third transistor, a fourth transistor, a storage capacitor and a light-emitting device, wherein, a drain of the driving transistor is connected with a source of the fourth transistor, a source thereof is connected with a drain of the third transistor, and a gate thereof is connected with a first terminal of the storage capacitor and a source of the first transistor; a drain of the first transistor is connected with the source of the fourth transistor, the source thereof is connected with the gate of the driving transistor, and a gate thereof is connected with a scan control signal line; a drain of the second transistor is connected with a data line, a source thereof is connected with the source of the

driving transistor and the drain of the third transistor, and a gate thereof is connected with the scan control signal line; the drain of the third transistor is connected with the source of the driving transistor, a source thereof is connected with an anode of the light-emitting device, and a gate thereof is connected with a light-emitting control signal line; a drain of the fourth transistor is connected with a first power supply voltage, the source thereof is connected with the drain of the driving transistor and the drain of the first transistor, and a gate thereof is connected with a pre-charging control signal line; the first terminal of the storage capacitor is connected with the gate of the driving transistor, and a second terminal thereof is connected with the first power supply voltage; a cathode of the light-emitting device is connected with a second power supply voltage.

10 2. The pixel unit circuit of claim 1, wherein the light-emitting device is an Organic Light-Emitting Diode device.

15 3. A compensating method for the pixel unit circuit of claim 1, comprising:

17 selecting a compensation manner according to an operation stage of a light-emitting device, wherein the compensation manner comprises an internal compensation manner and an external compensation manner; compensating the light-emitting device with the internal compensation manner, if the light-emitting device is in an operation stage for light-emitting normally; and compensating the light-emitting device with the external compensation manner, if the light-emitting device is in an operation stage of a panel reset or an operation stage of an idle display between frames or rows.

20 4. The compensating method for the pixel unit circuit of claim 3, wherein said compensating the light-emitting device with the internal compensation manner further comprises:

25 pre-charging the driving transistor; performing a voltage compensation or a current compensation on the driving transistor; and performing a voltage compensation or a current compensation on the light-emitting device, in order to remain the light-emitting device to emit light.

30 5. The compensating method for the pixel unit circuit of claim 4, wherein said pre-charging the driving transistor further comprises:

35 setting a light-emitting control signal as a low level to turn off the third transistor; setting a pre-charging control signal as a high level to turn on the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and making a voltage at the source of the driving transistor be a voltage  $V_{DATA}$  on the data line.

40 6. The compensating method for the pixel unit circuit of claim 4, wherein said performing a voltage compensation or a current compensation on the driving transistor further comprises:

45 setting a light-emitting control signal as a low level to turn off the third transistor; setting a pre-charging control signal as a low level to turn off the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and making a voltage at the gate of the driving transistor be  $V_{DATA} + V_{thn}$ , wherein  $V_{DATA}$  is the voltage on the data line, and  $V_{thn}$  is a threshold voltage of the driving transistor.

50 7. The compensating method for the pixel unit circuit of claim 4, wherein said performing a voltage compensation or a current compensation on the light-emitting device in order to remain the light-emitting device to emit light further comprises:

55 setting a light-emitting control signal as a high level to turn on the third transistor; setting a pre-charging control signal as a high level to turn on the fourth transistor; setting a scan control signal as a low level to turn off the first transistor and the second transistor; and making a current  $I_{OLED}$  input to the light-emitting device through the driving transistor be:

$$I_{OLED} = \frac{1}{2} \cdot \mu_n C_{ox} \cdot \frac{W}{L} \cdot [V_{DATA} - V_{OLED}]^2,$$

wherein  $\mu_n$  is a mobility of carriers,  $C_{OX}$  is a capacitance value of the storage capacitor in an oxide layer at the gate,  $\frac{W}{L}$  is a width-length ratio of the driving transistor,  $V_{DATA}$  is a voltage on the data line,  $V_{OLED}$  is a voltage at the anode of the light-emitting device.

5        8. The compensating method for the pixel unit circuit of claim 3, wherein said compensating the light-emitting device with the external compensation manner further comprises:

10        extracting a current from the driving transistor;  
               extracting a current from the light-emitting device; and  
               detecting the current extracted from the driving transistor or the light-emitting device, and performing a voltage compensation or a current compensation on the light-emitting device according to a value of the detected current.

15        9. The compensating method for the pixel unit circuit of claim 8, wherein said extracting a current from the driving transistor further comprises:

20        setting a light-emitting control signal as a low level to turn off the third transistor; setting a pre-charging control signal as a high level to turn on the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and enabling a current of the driving transistor to be input to the data line while shielding a current of the light-emitting device from being input to the data line.

25        10. The compensating method for the pixel unit circuit of claim 9, wherein said extracting a current from the light-emitting device further comprises:

30        setting a light-emitting control signal as a high level to turn on the third transistor; setting a pre-charging control signal as a low level to turn off the fourth transistor; setting a scan control signal as a high level to turn on the first transistor and the second transistor; and enabling a current of the light-emitting device to be input to the data line while shielding a current of the driving transistor from being input to the data line.

35        11. The compensating method for the pixel unit circuit of any one of claims 3-10, wherein the light-emitting device is an Organic Light-Emitting Diode device.

40        12. A display device comprising the pixel unit circuit of any one of claims 1 or 2.

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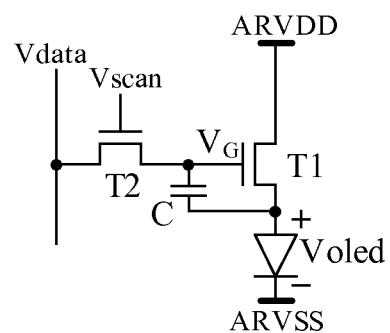


Fig.1

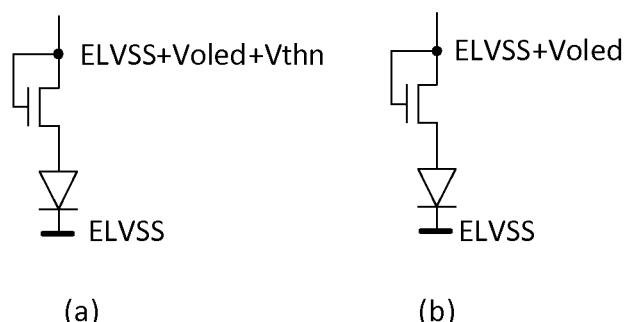


Fig.2

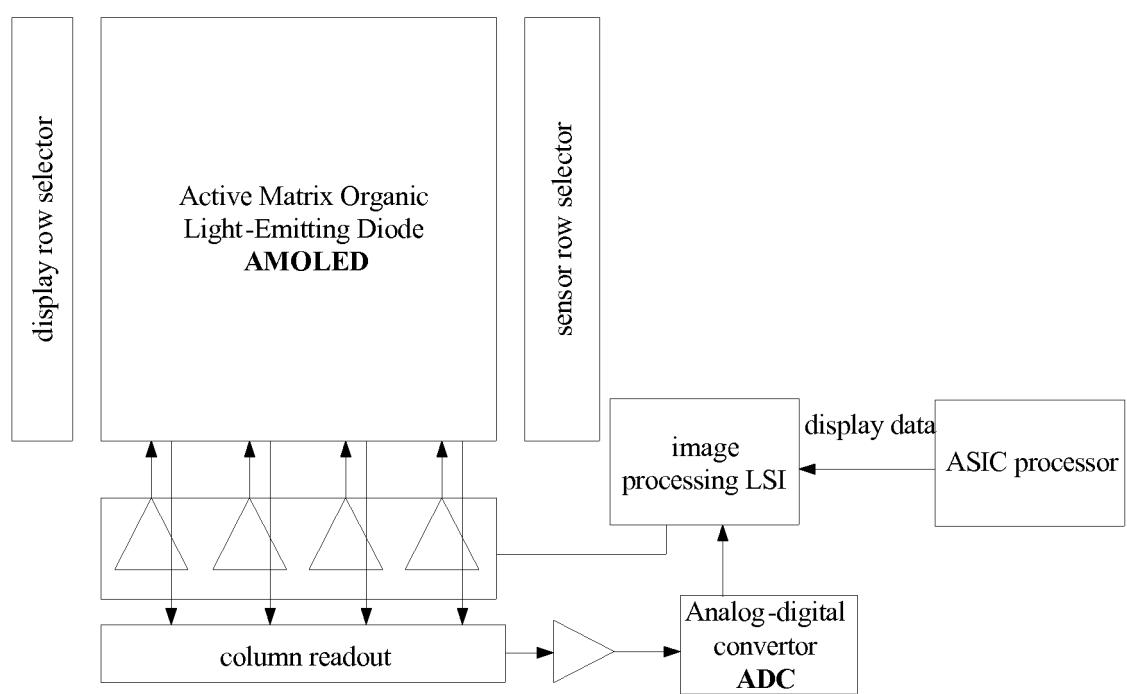


Fig.3

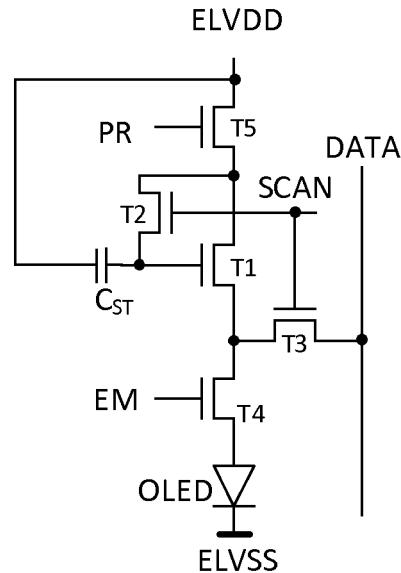


Fig.4

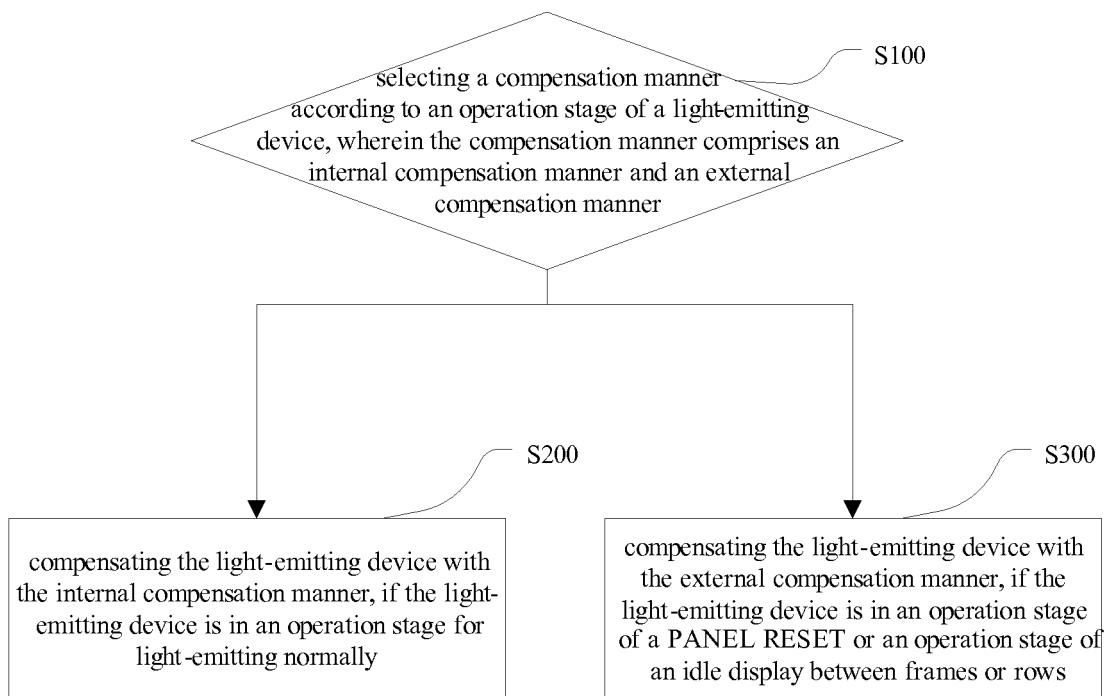


Fig.5

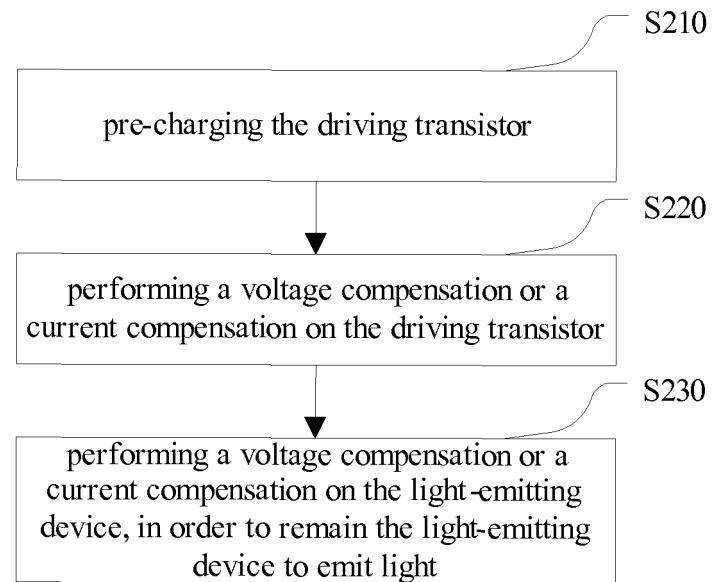


Fig.6

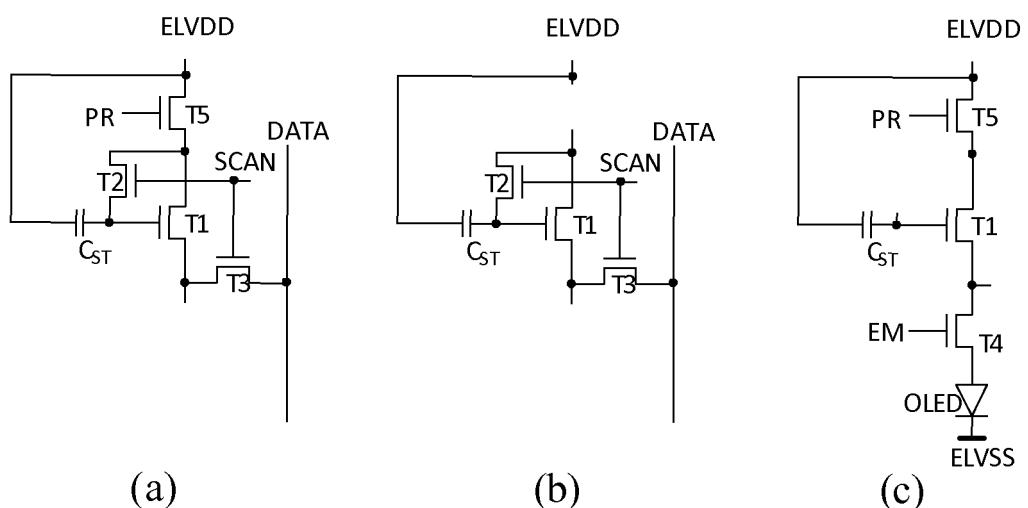


Fig.7

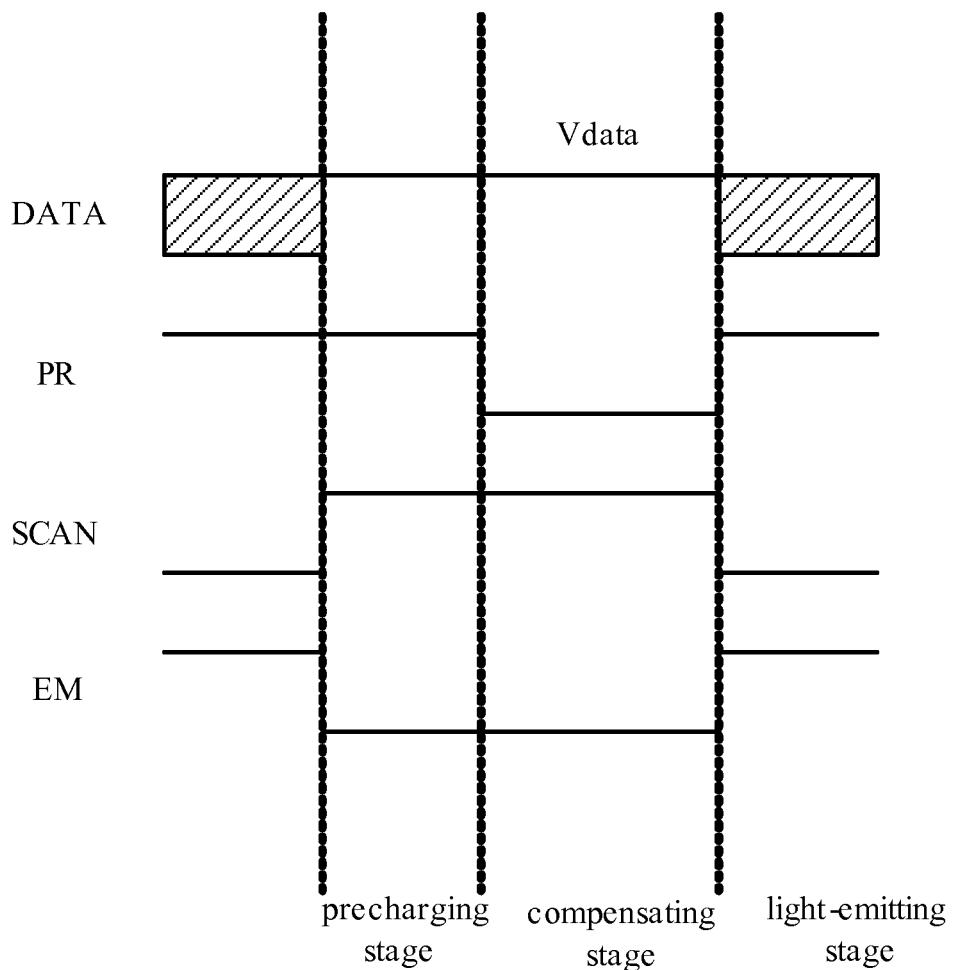


Fig.8

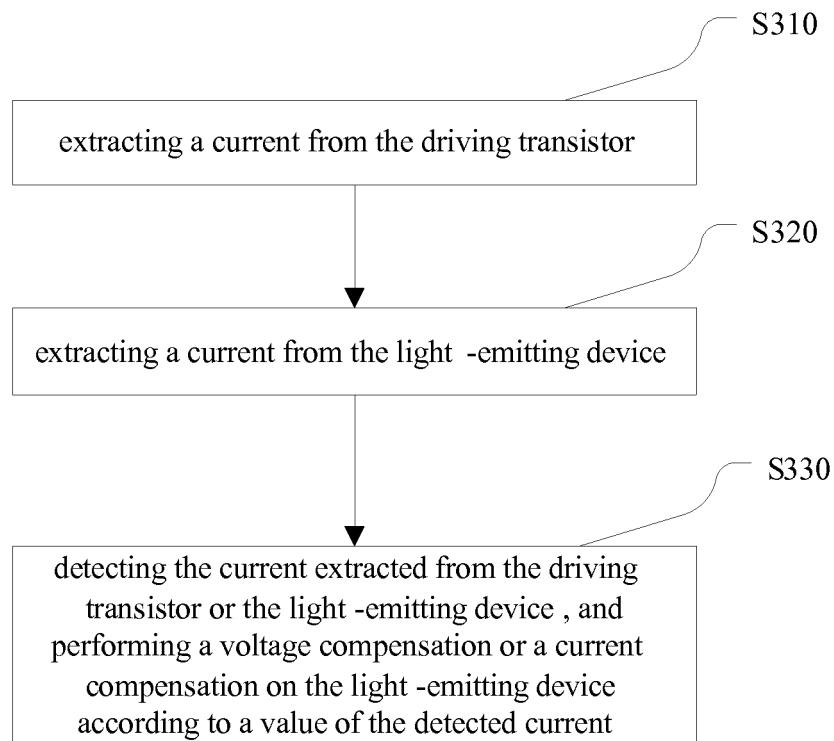


Fig.9

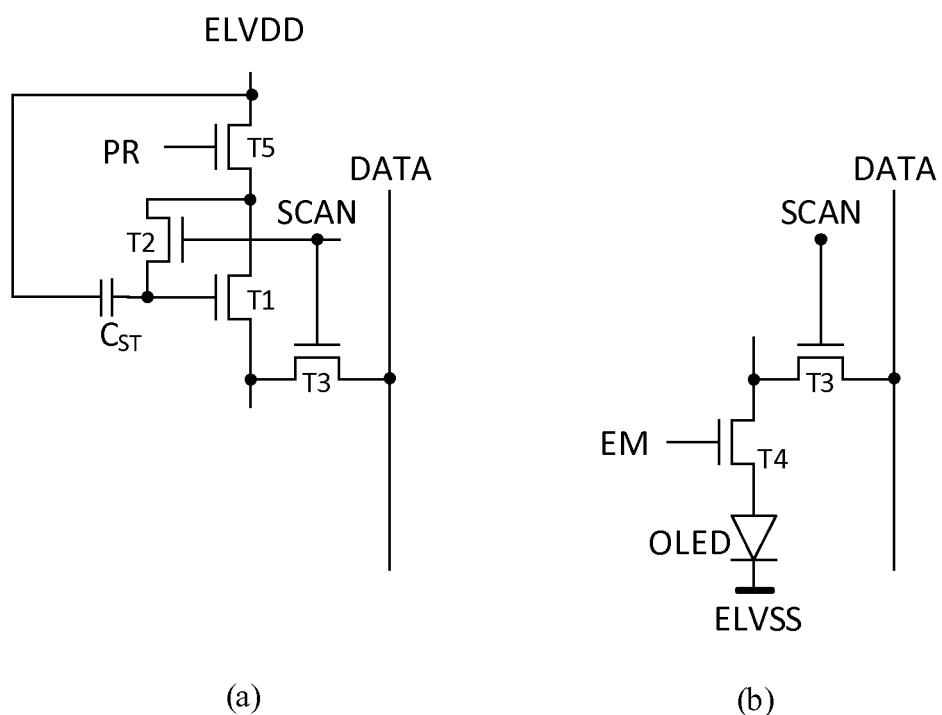


Fig.10

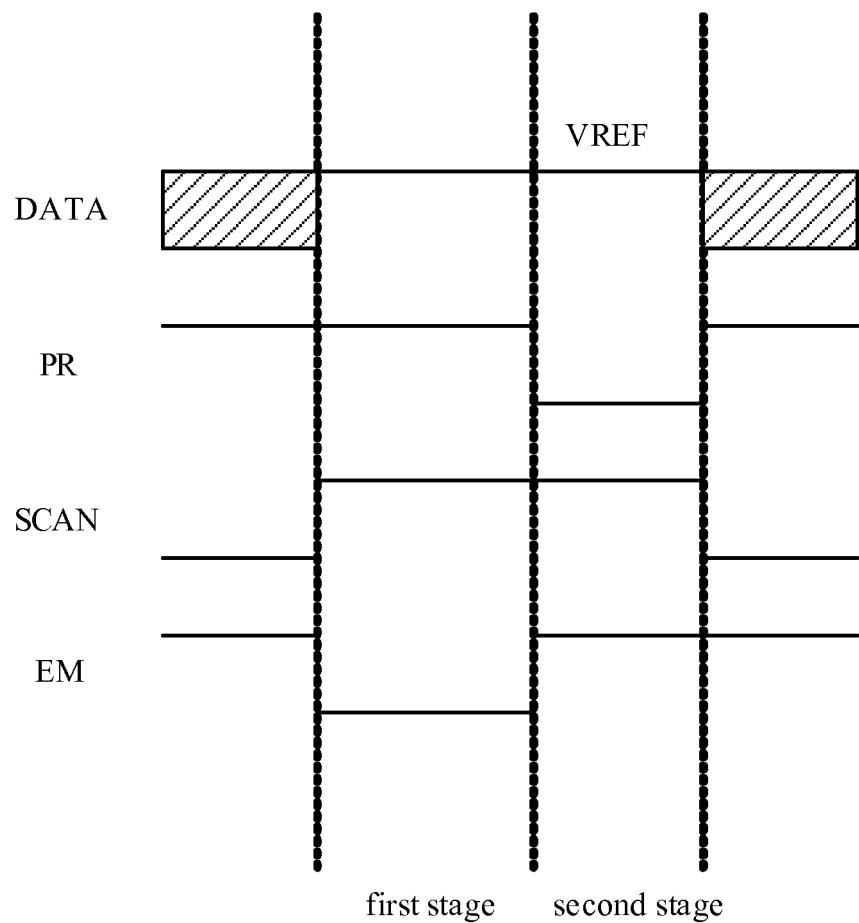


Fig.11

## INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2013/077965

5	<b>A. CLASSIFICATION OF SUBJECT MATTER</b>		
	G09G 3/32 (2006.01) i According to International Patent Classification (IPC) or to both national classification and IPC		
10	<b>B. FIELDS SEARCHED</b>		
	Minimum documentation searched (classification system followed by classification symbols) IPC: G09G 3, H05B 33		
15	Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
	Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) CNPAT, CNABS, CNTXT: fifth, five, transistor, switch, tft, fourth, four, internal, external, compensate, feedback SIPOABS, DWPI, USTXT, WOTXT, EPTXT: fifth, 5th, fourth, 4th, switch???, tft, transistor?, internal, external, compensat+, feedback		
20	<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
	Category*	Citation of document, with indication, where appropriate, of the relevant passages	
25	Y	CN 102651196 A (BOE TECHNOLOGY GROUP CO., LTD. et al.), 29 August 2012 (29.08.2012), description, paragraphs 0027-0076, and figures 2-6	1-2, 12
	Y	CN 102956201 A (BOE TECHNOLOGY GROUP CO., LTD.), 06 March 2013 (06.03.2013), description, paragraphs 0029-0060, and figures 1-5	1-2, 12
30	A	CN 103035202 A (AU OPTRONICS CORP.), 10 April 2013 (10.04.2013), the whole document	1-12
	A	US 2010220086 A1 (CHUNG, B.Y. et al.), 02 September 2010 (02.09.2010), the whole document	1-12
	A	CN 102074189 A (LG DISPLAY CO., LTD.), 25 May 2011 (25.05.2011), the whole document	3-11
	E	CN 203179479 U (BOE TECHNOLOGY GROUP CO., LTD.), 04 September 2013 (04.09.2013), description, paragraphs 0041-0094, and figures 1-11	1-12
35	<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.		
	* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed		
40	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
45	Date of the actual completion of the international search 23 January 2014 (23.01.2014)		Date of mailing of the international search report <b>20 February 2014 (20.02.2014)</b>
50	Name and mailing address of the ISA/CN: State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No.: (86-10) 62019451		Authorized officer <b>WANG, Chao</b> Telephone No.: (86-10) <b>62085834</b>
55			

**INTERNATIONAL SEARCH REPORT**  
Information on patent family members

International application No.

PCT/CN2013/077965

5	Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
10	CN 102651196 A	29.08.2012	None	
	CN 102956201 A	06.03.2013	None	
15	CN 103035202 A	10.04.2013	None	
	US 2010220086 A1	02.09.2010	KR 20100097873 A	06.09.2010
20			KR 1040816 B1	13.06.2011
			US 7936322 B2	03.05.2011
25	CN 102074189 A	25.05.2011	TW 201137828 A	01.11.2011
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40			US 8558825 B2	15.10.2013
45	CN 203179479 U	04.09.2013	None	
50				
55				

Form PCT/ISA/210 (patent family annex) (July 2009)

专利名称(译)	像素单元电路及其补偿方法和显示装置		
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IPC分类号	G09G3/32		
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优先权	201310150519.3 2013-04-26 CN		
其他公开文献	<a href="#">EP2991065A1</a>		
外部链接	<a href="#">Espacenet</a>		

#### 摘要(译)

一种像素单元电路及其补偿方法及显示装置。像素单元电路包括驱动晶体管 ( T1 ) , 第一晶体管 ( T2 ) , 第二晶体管 ( T3 ) , 第三晶体管 ( T4 ) , 第四晶体管 ( T5 ) , 存储电容器 ( CST ) 发光器件 ( OLED )。像素单元电路 , 其补偿方法和显示装置可以通过组合内部补偿和外部补偿来补偿发光器件 , 并且具有内部补偿和外部补偿的优点。由N型耗尽或增强的驱动晶体管TFT中的阈值电压的不均匀性或阈值电压的漂移引起的Mura现象可以通过内部补偿有效地消除 , 这可以增强显示效果。此外 , 像素单元电路 , 其补偿方法和显示装置可以具有用于提取驱动TFT的特性和发光器件的特性的功能 , 这可以有效地应用于外部补偿驱动。