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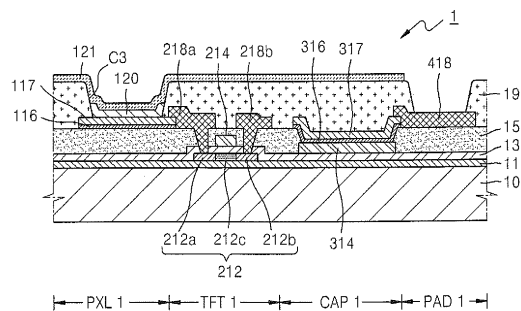
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(54) **Thin film transistor array substrate, organic light-emitting display device including the same, and method of manufacturing the organic light-emitting display device**

(57) A thin film transistor (TFT) array substrate includes a TFT on a substrate, the TFT including an active layer (212), gate electrode (214), source electrode (218a), drain electrode (218b), first insulating layer (13) between the active layer and the gate electrode, and second insulating layer (15) between the gate electrode and the source and drain electrodes; a pixel electrode (117) on the first insulating layer and the second insulating layer, the pixel electrode being connected to one of the source electrode and drain electrode; a capacitor including a lower electrode (314) on a same layer as the gate electrode and an upper electrode (317) including the same material as the pixel electrode; a third insulating layer (116, 316) directly between the second insulating layer and the pixel electrode and between the lower electrode and the upper electrode; and a fourth insulating layer (19) covering the source electrode, the drain electrode, and the upper electrode, and exposing the pixel electrode.

FIG. 1



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EUROPEAN SEARCH REPORT

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Y	* figure 2 *	8,9,14, 15	
A	* paragraphs [0056], [0057], [0061], [0062], [0063], [0064], [0127] * -----	18-23	
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	* figures 1,3 * -----		
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 27 May 2013	Examiner De Laere, Ann
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
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27-05-2013

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专利名称(译)	薄膜晶体管阵列基板，包括该薄膜晶体管阵列基板的有机发光显示装置，以及制造该有机发光显示装置的方法		
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优先权	1020110070027 2011-07-14 KR		
其他公开文献	EP2546903A2 EP2546903B1		
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摘要(译)

薄膜晶体管 (TFT) 阵列基板包括基板上的TFT，TFT包括有源层 (212)，栅电极 (214)，源电极 (218a)，漏电极 (218b)，第一绝缘层 (13) 在有源层和栅电极之间，以及在栅电极和源电极和漏电极之间的第二绝缘层 (15)；第一绝缘层和第二绝缘层上的像素电极 (117)，像素电极连接到源电极和漏电极中的一个；电容器包括位于与栅电极相同的层上的下电极 (314) 和包括与像素电极相同的材料的上电极 (317)；第三绝缘层 (116,316) 直接位于第二绝缘层和像素电极之间以及下电极和上电极之间；第四绝缘层 (19) 覆盖源电极，漏电极和上电极，并暴露像素电极。

FIG. 1

