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(43)10-2004-0061201
2004 07 07(21) 10-2002-0087004
(22) 2002 12 30(71) .
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(72) 5 54-35

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(74)

(54)

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3

4 1

5 4

6 4 5 ' - "

7 2

8 7

9 7 8 ' - ' , ' - '

10 1 2

< _____ >

$$2,14,24,44,54 : \quad 6,16,26,36,46,56,66 :$$

8,18,28,38,48,58,68 : 10,20,30,40,50,60 :

22,32,52,62 : 72 :

74 : 76 :

78 :

가

가

1

1 , (DL) (1) (Thin Film Transistor ; 'TFT') (T) , TF (GL)
) T(T) (2) .
 (2) TFT(T) (8,10) (DL1, DL2, ...) (2) 가
 . TFT(T) (6) . TFT(T) (2) 가
 (GL1, GL2, ...) (DL1, DL2,...) (GL1, GL2, ...) (2) () 가
 Vgh (2) (2) .

m (DL1 DLm),
 (DL1 DLm)
 (DL1 DLm)
 , m
 . , m
 (Integrated Circuit : 'IC')가

$$, \quad 2 \quad \quad 3 \quad \quad m(m) \quad \quad) \quad \quad m/2$$

1 TFT(T1)	k	(GLk)	(46)	i	(DLi)
(48)	, 2 TFT(T2)	(58)	(50)	.	.
2 TFT(T2)	k+1	(GLk+1)	(56)	, 1 TFT(T1)	(50)
(58)	, 1	(52)	1	(44)	.
3 TFT(T3)	k	(GLk)	(66)	, i	(DLi)
(68)	, 2	(62)	2	(54)	(70)
1 3 TFT(T1)	T3	6	(46,56,66)	.	.
(48,58,68)	.	(50,60,70)	.	.	.
1 3 TFT(T1)	T3)	k	k+1	(GLk,GLk+1)	(74,76)
i (DLi)	.	.	.	(44,54)	.
(44,54)	i	(DLi)	k	k+1	(GLk,GLk+1)
(78)	,	(78)	1	2	(44,54)
1 , 2 (51)	(44,54)	1 (44,54)	2 (52,62)	.	(51)
(51)	.	.	(44,54)	.	(60,70)
.
(SC)	k	k+1	(GLk,GLk+1)	.	.
.	.	.	(SC) 1 2	.	.
(44,54)	.	.	(44,54)	.	.
.
(SL)
.
90) 1 (90)	4 (90)	(DLi)	(DLi)	5 (90)가	(90)가
, 2 (54)	.	1 (DLi)	1 (44)	1 (DLi) 2 (94)	U (44,54)
.	.	1 (54)	(90)	.	(90)
(90)	.	2 (DLi)	.	1 2 (44,54)	(90)
.
1 SP2)가 k (SP1) 가 , 1 (TA)	10 (GLk)	2 (SP2)	k+1 (TA1)	1 (SP2) 2 (SP1) 가	(SP1) (SP2)
.	.	.	2 (TB)	2 (SP2) 1 (SP1) 가	.
k+1 (SP2)가 가 1 (GLk+1) 1 (TA1)	1 1 (SP1)가	2 TFT(T1,T2)가	1 k 1 (GLk)	1 2 (44)	.
.
1 1 (DA) (SP1) 2 TFT(T2)	, k 2 TFT(T2)	(GLk)	2 (SP2)	1 TFT(T1) (GLk+1)	.
(GLk)	2 (DA)	(58)	2 (SP2)	(58)	.
1 (DA)	.	2 TFT(T2)가 (60)	1 k+1 (44)	1 (GLk+1) 1 (44) k (SP1)가	.
.
.
, k 3 TFT(T3)가 (54)	2 (GLk)	2 (TA2)	(SP2) (DLi)	2 (TA2) 2 (DB)	3 TFT(T3)가 2 (DB)
.

7 Lk,GLk+1), i , 2 (DLi) 1 k(k) k+1 (G
 T(T1 T3) 2 (SC) 1 3 TFT(T1) 2 (44,54) , 1 2 (T3) , 1 (44,54) 3 TF
 1 TFT(T1) k (GLk) (50) , 2 TFT(T2) (46) , k+1 (56) 1 (GLk+1) 4
 (52d) (48) .
 2 TFT(T2) 1 TFT(T1) (52b) (48) 1 (44) (46) , i (60) (DLi)
 3 TFT(T3) k (52c) (GLk) 2 (54) (66) , i (70) (DLi)
 (68) , 3 (48,58,68) 3 TFT(T1) T3) 9 (50,60,70) (46,56,66)
 1 3 TFT(T1) T3) k k+1 (GLk,GLk+1) (74,76)
 i (DLi) (44,54) .
 (44,54) i (DLi) k k+1 (44,54) (GLk,GLk+1) (51)
 (78) , 1 2 (78) (44,54) 2 1 3 3 (52b,52c) (44,54) (60,70)
 (51) () () () (44,54) .
 (SC) k k+1 (GLk,GLk+1) (SC) 1 2 (44,54)
 (44,54) .
 90) 7 2 (DLi) 8 (DLi) 1 2 U (90)가 (90)
 (90) , 2 (54) (DLi) 1 (44) (90) (90) (92) (90)
 (90) 2 (54) (DLi) 1 2 (44,54) .
 2 10
 2 (SP1) 7 (GLk) 2 k+1 (GLk+1) 1 2 (S
 P1) (SP2) 1 (SP1) 가 , 1 (TA) 2 1 (SP2)
 2 (SP2) 가 1 (TA1) 1 2 TFT(TFT1,TFT2) 가 1 (GLk) 2 1 (4)
 4) .
 Lk) 1 TFT(T1) k+1 (50) (GLk+1) 1 (SP1) k (GLk)
 1 TFT(T1) 가 1 TFT(T1) 2 TFT(T2) 1 (50) 2 TFT(T2) 가 1 (SP1) 2 TFT(T2)
 (56) . (DLi)

1 (SP1) 가 1 (DA) 1 (44) . , k+1 (SP2) 가 1 (GLk+1) 1
 (GLk) 1 (44) 1 (DA) 가 . (TA1) k
 , 2 (TB) k (DLi) (GLk) 2 3 TFT(T3) 가 . 3 TFT(T3) 가
 2 (TB) (DB) 2 (54) .

2 , . / 1 2

가

(57)

1.

, 1 2

2.

1 , 1 2

3.

2 , 1 , 2

4.

3 , 1 , 2

, 1 1 ,
 2 2 , 가

5.

4 , 1

k(k) , 1

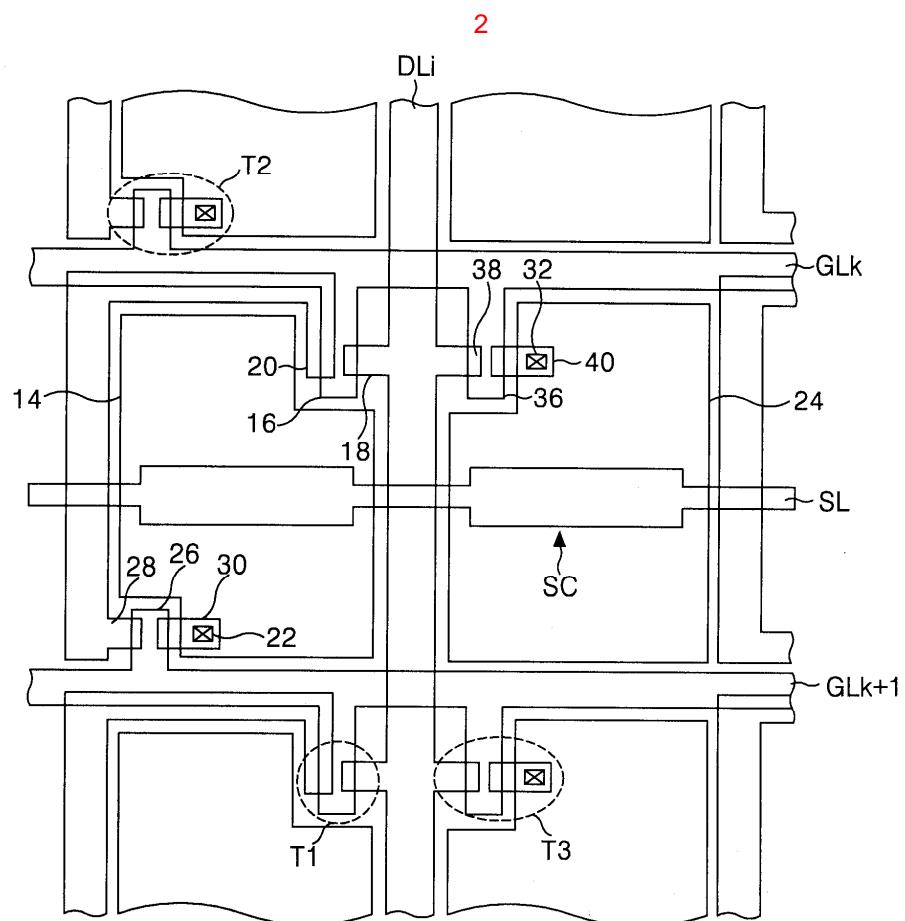
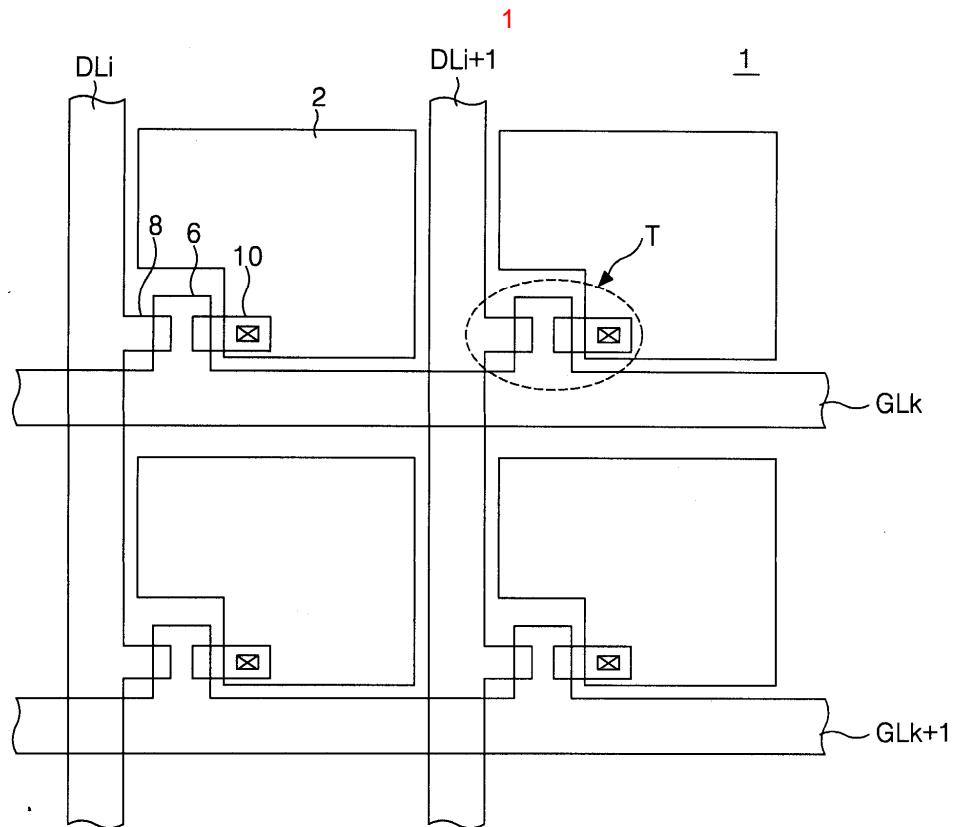
$$k+1 \quad , \quad 1 \\ 1 \quad , \quad 2 \quad , \quad 1$$

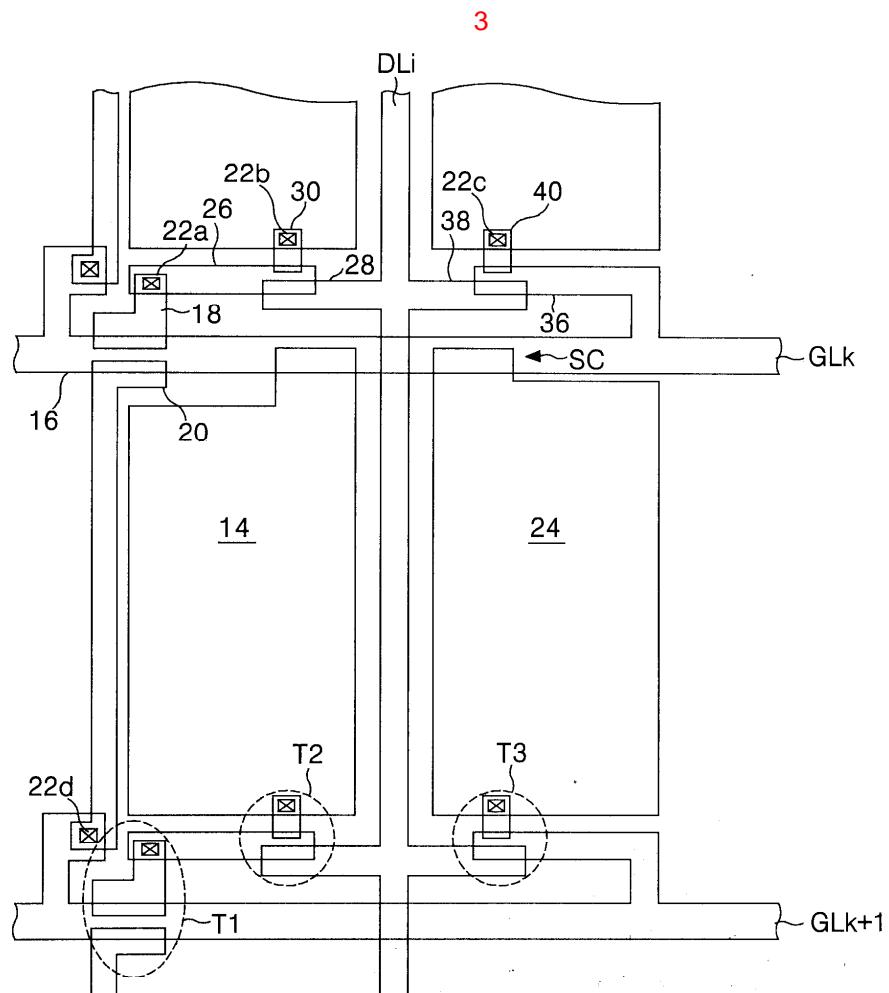
K , , Z
3 .

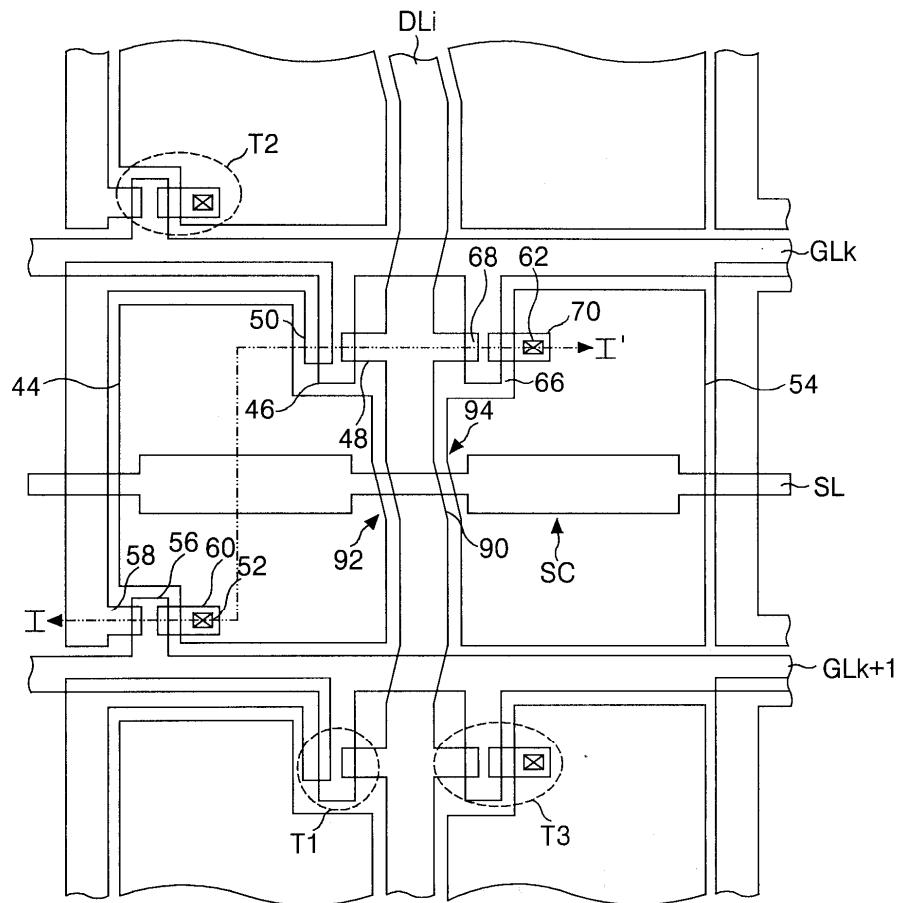
1 2 , 1 2 가 1 2

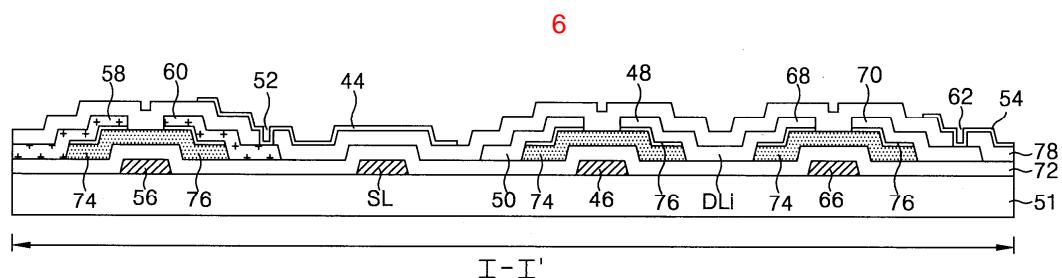
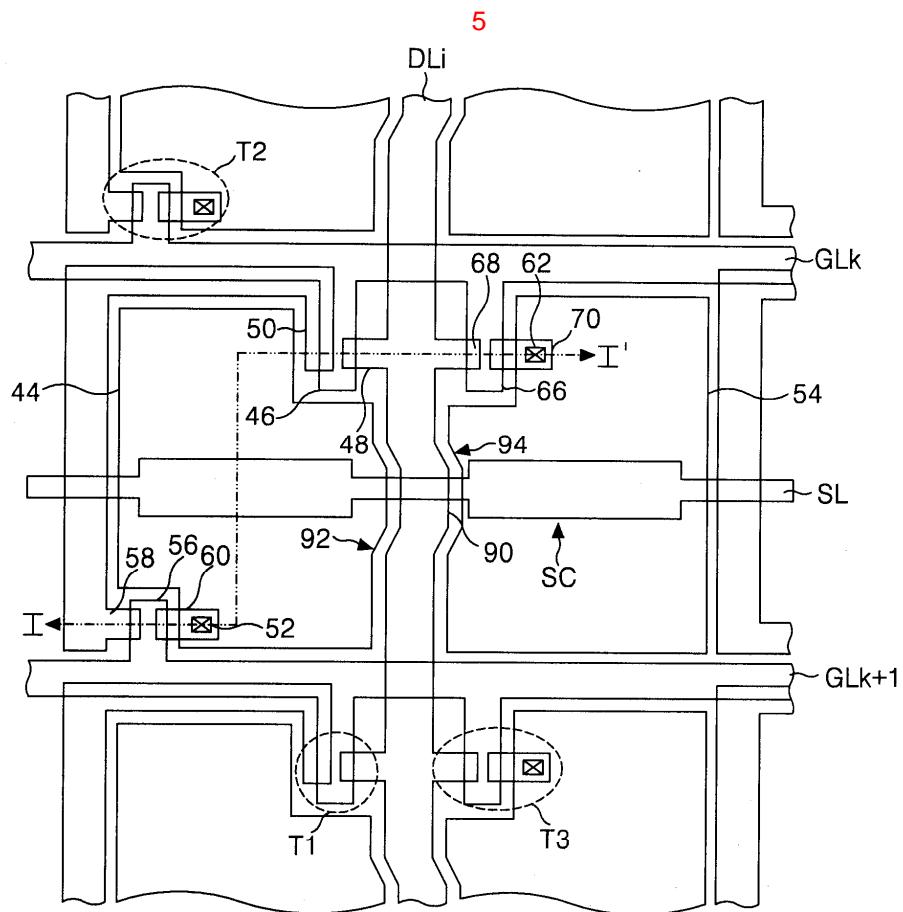
10.
9 ,
1 2 가

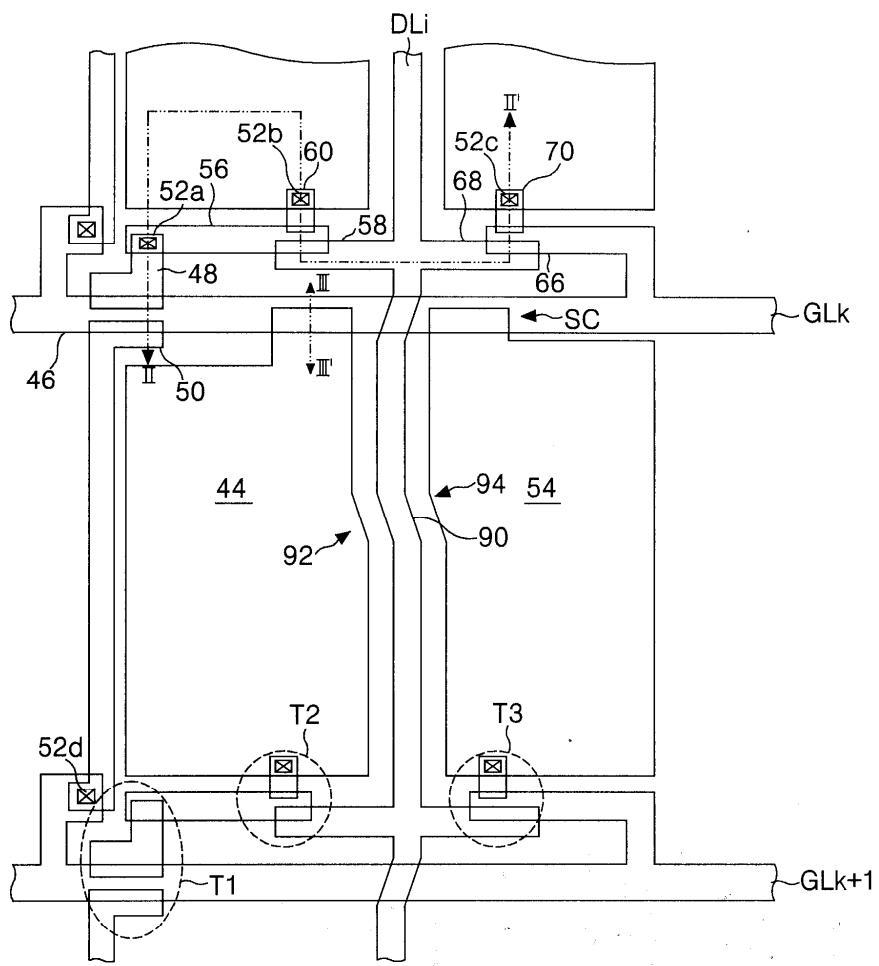
12. 11 ,



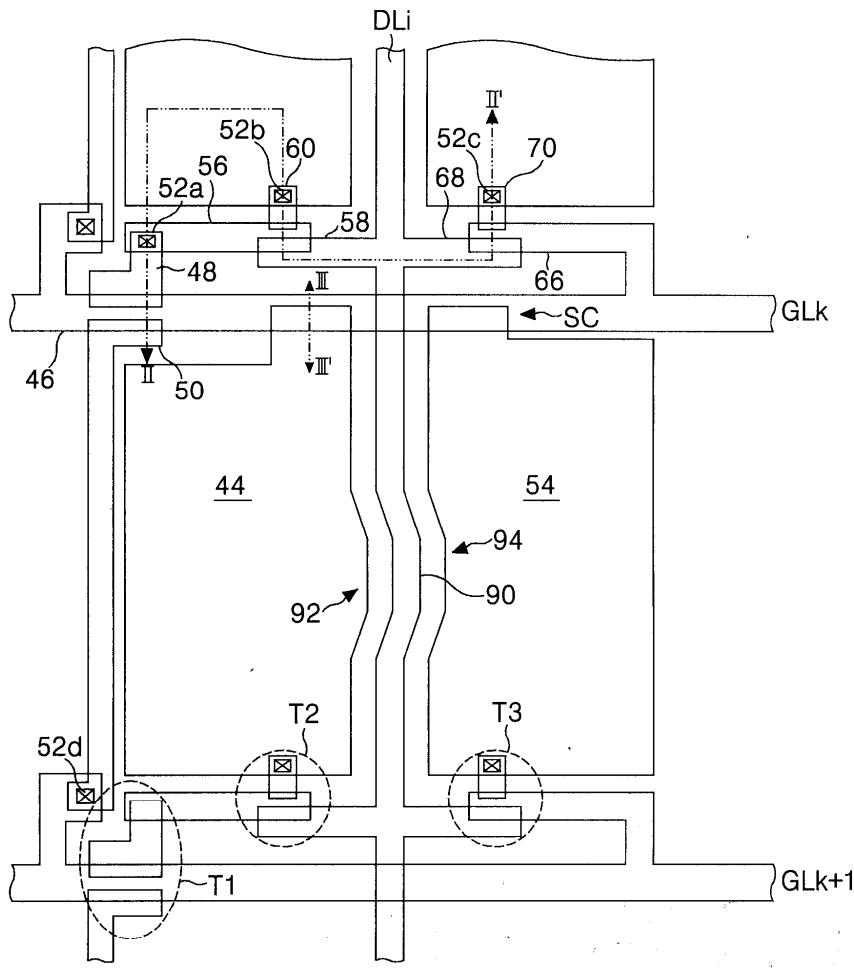




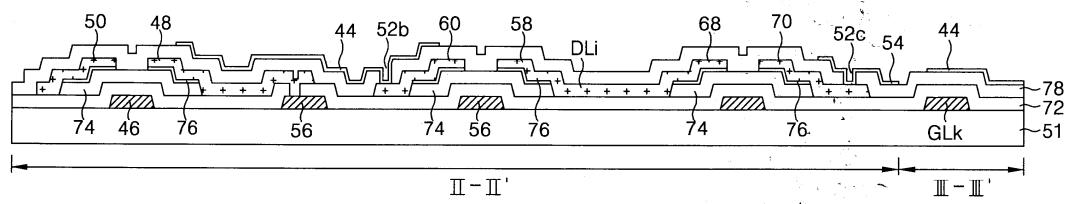


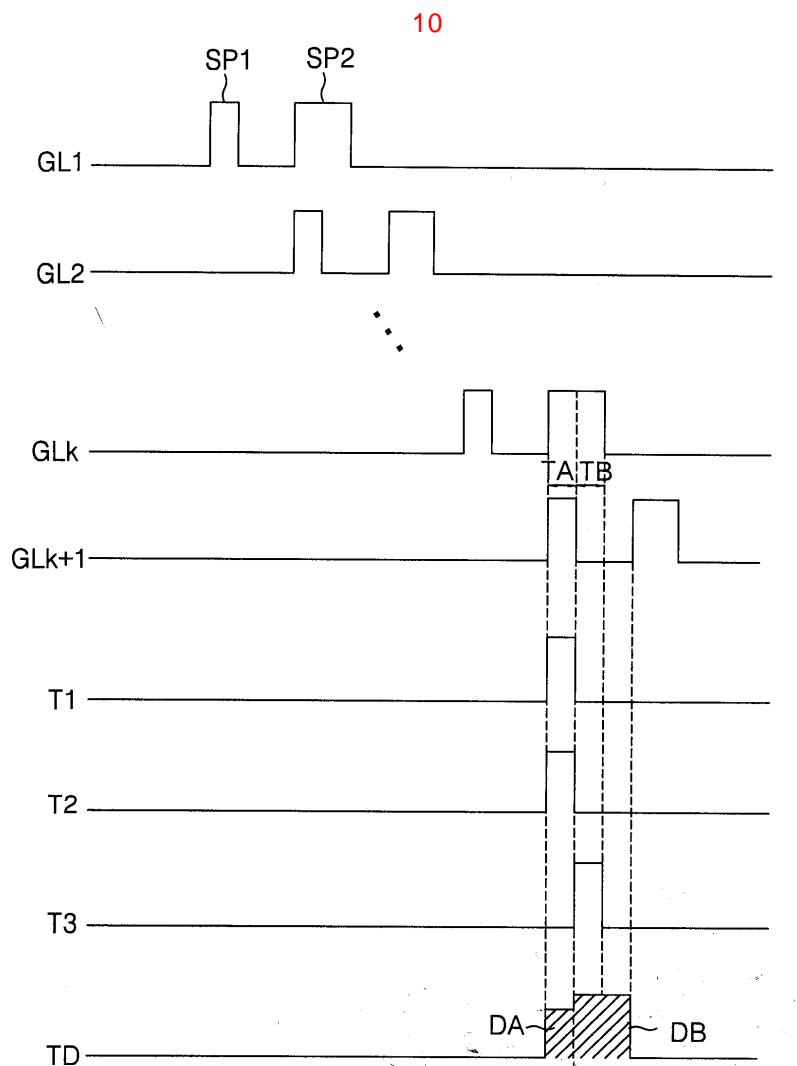


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专利名称(译)	液晶显示面板		
公开(公告)号	KR1020040061201A	公开(公告)日	2004-07-07
申请号	KR1020020087004	申请日	2002-12-30
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG显示器有限公司		
当前申请(专利权)人(译)	LG显示器有限公司		
[标]发明人	PARK KWANGSOON 박광순 SHIN SEUNGCHAN 신승찬		
发明人	박광순 신승찬		
IPC分类号	G02F1/1343		
代理人(译)	金勇 年轻的小公园		
外部链接	Espacenet		

摘要(译)

液晶显示面板技术领域本发明涉及一种液晶显示面板，其能够在不降低孔径比的情况下均衡左右相邻的不对称像素电极的开口率。根据本发明的液晶显示板的特征在于它具有弯曲的信号线和第一和第二像素电极，它们与信号线相邻形成并沿信号线弯曲以补偿不同的开口区域的。 4

