



(19) **United States**

(12) **Patent Application Publication** (10) **Pub. No.: US 2002/0113935 A1**

Yanagawa et al.

(43) **Pub. Date: Aug. 22, 2002**

(54) **LIQUID CRYSTAL DISPLAY**

(30) **Foreign Application Priority Data**

(75) Inventors: **Kazuhiko Yanagawa**, Mobara (JP);
Keiichiro Ashizawa, Mobara (JP);
Masahiro Ishii, Mobara (JP);
Masayuki Hikiba, Mobara (JP)

Feb. 5, 1999 (JP) 11-029053

Publication Classification

(51) **Int. Cl.⁷** **G02F 1/1339**

(52) **U.S. Cl.** **349/155**

Correspondence Address:
REED SMITH LLP
Suite 1400
3110 Fairview Park Drive
Falls Church, VA 22042 (US)

(57) **ABSTRACT**

The present invention is to provide the multi-domain system IPS (In-Plane Switching mode) liquid crystal display which has high contrast and does not have an improper image display. According to an aspect of the present invention, there is provided a black matrix formed between a pair of substrates and formed over a plurality of drain lines and gate lines, a plurality of spaces arranged under the black matrix, wherein a plurality of liquid crystal molecules lined to a boundary direction of the black matrix in "OFF" state, and having a first group and a second group in "ON" state, wherein a switching angle of the second group is opposite to a switching angle of the first group.

(73) Assignee: **Hitachi, Ltd.**

(21) Appl. No.: **10/106,954**

(22) Filed: **Mar. 28, 2002**

Related U.S. Application Data

(63) Continuation of application No. 09/497,996, filed on Feb. 4, 2000.

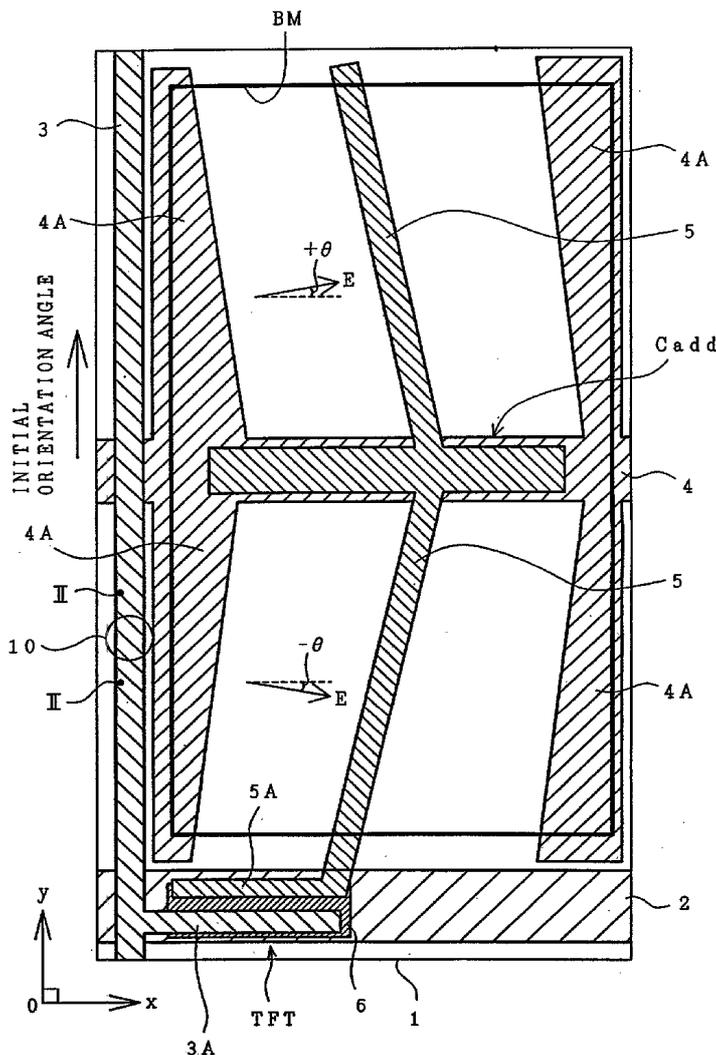


FIG. 1

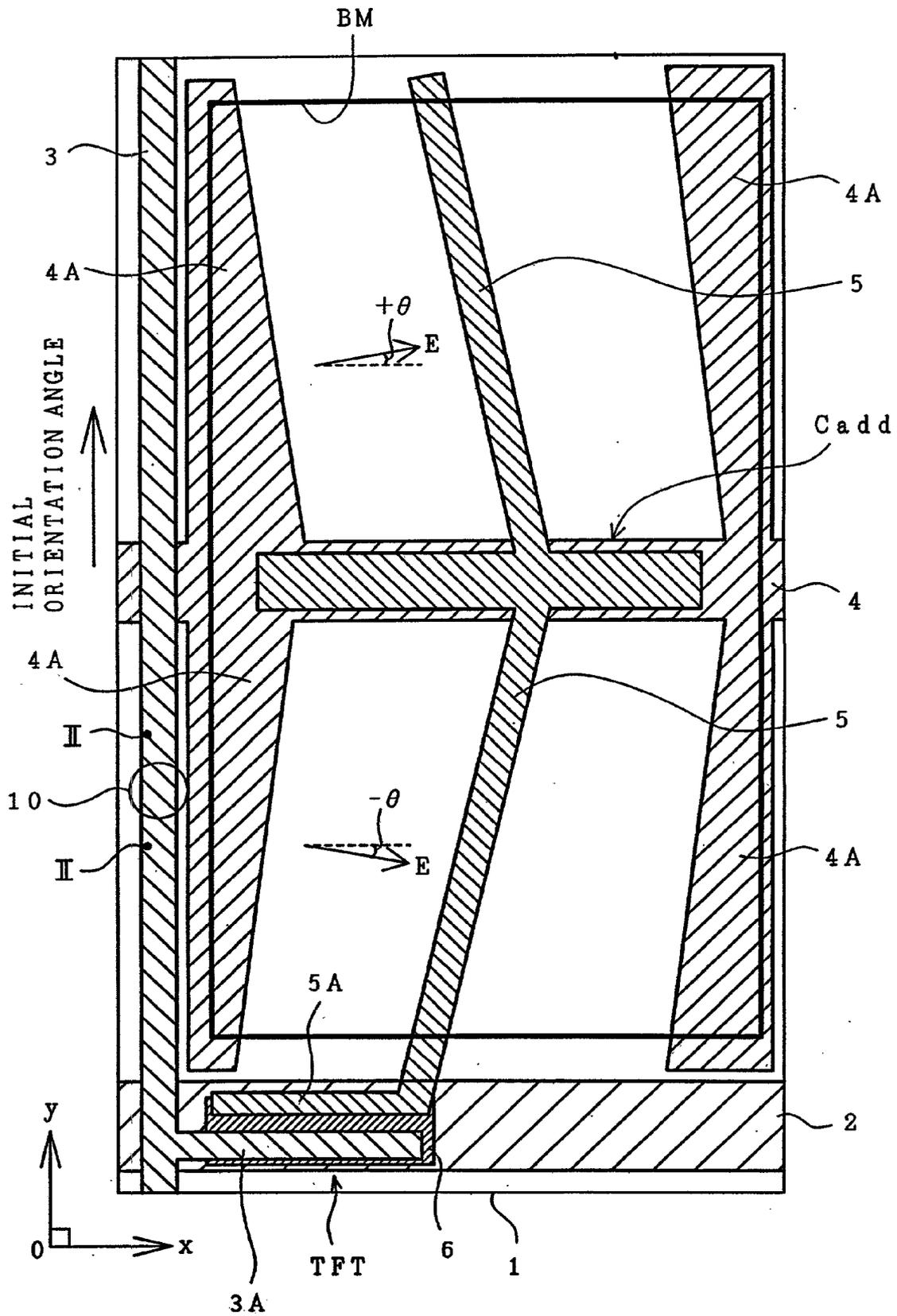


FIG. 2

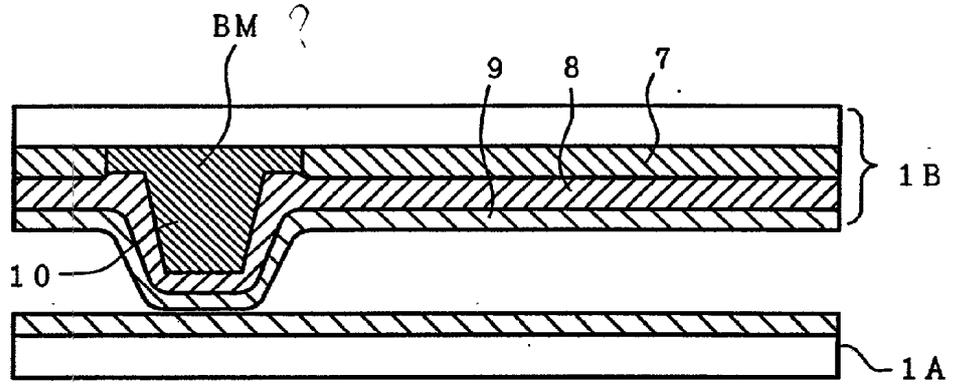


FIG. 3

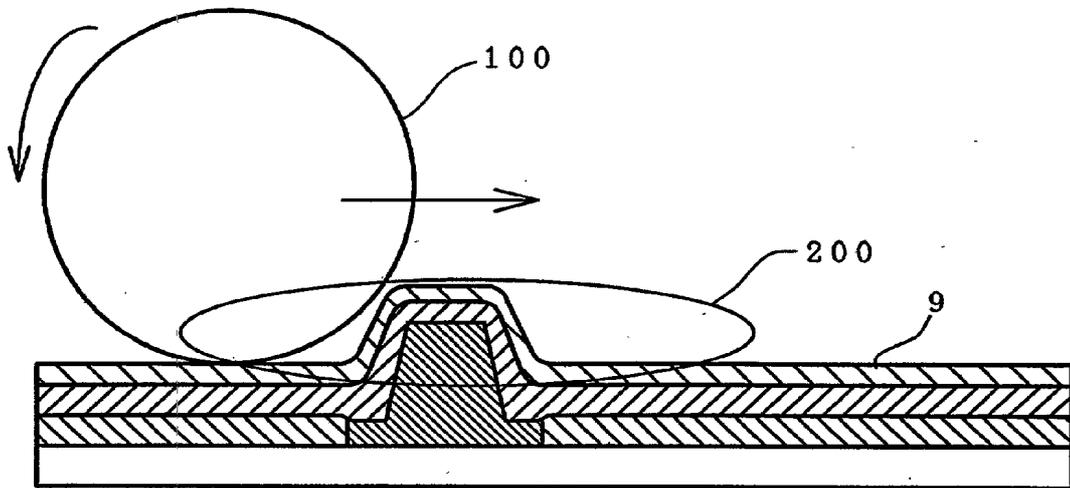


FIG. 4

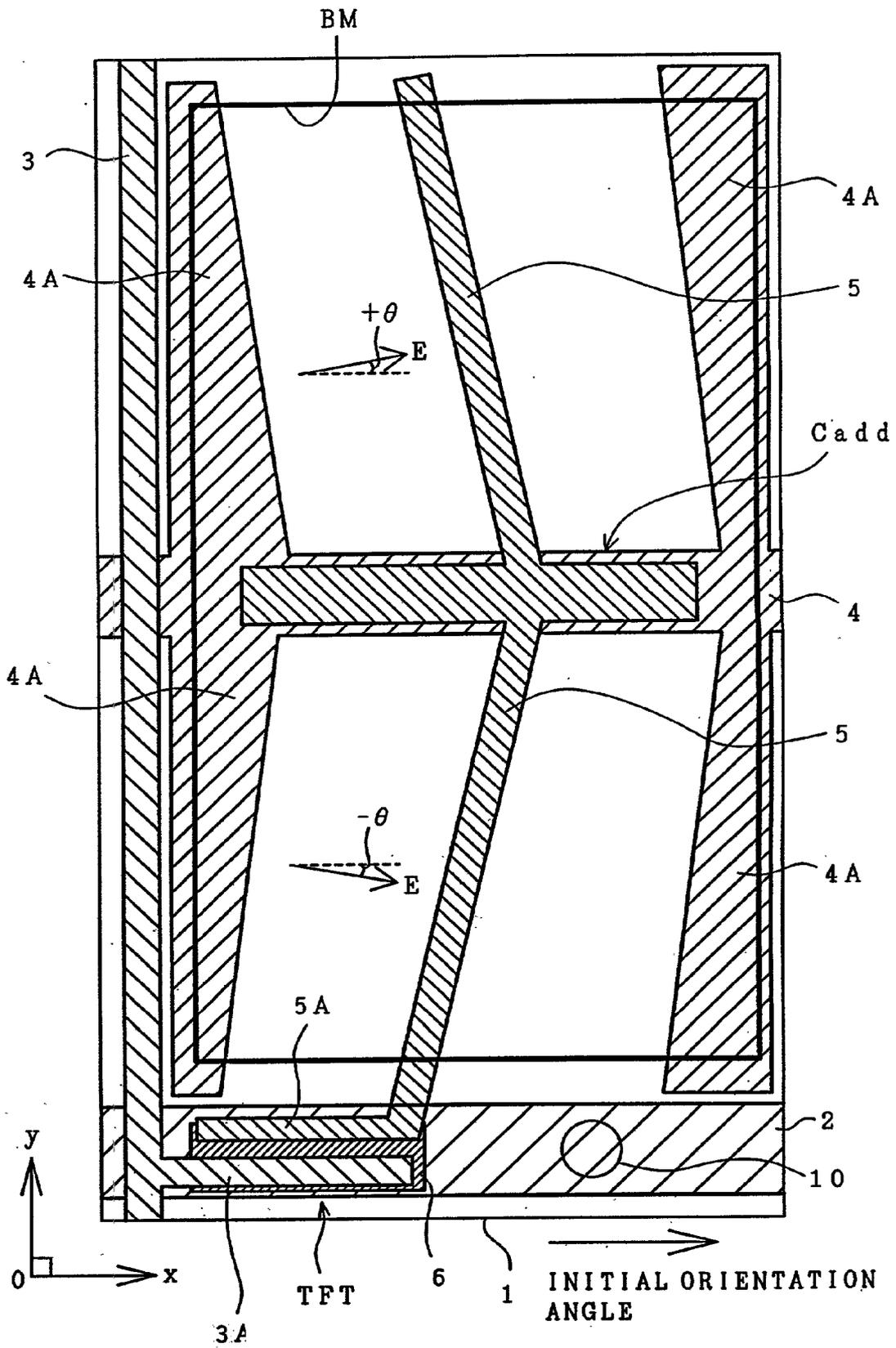


FIG. 5

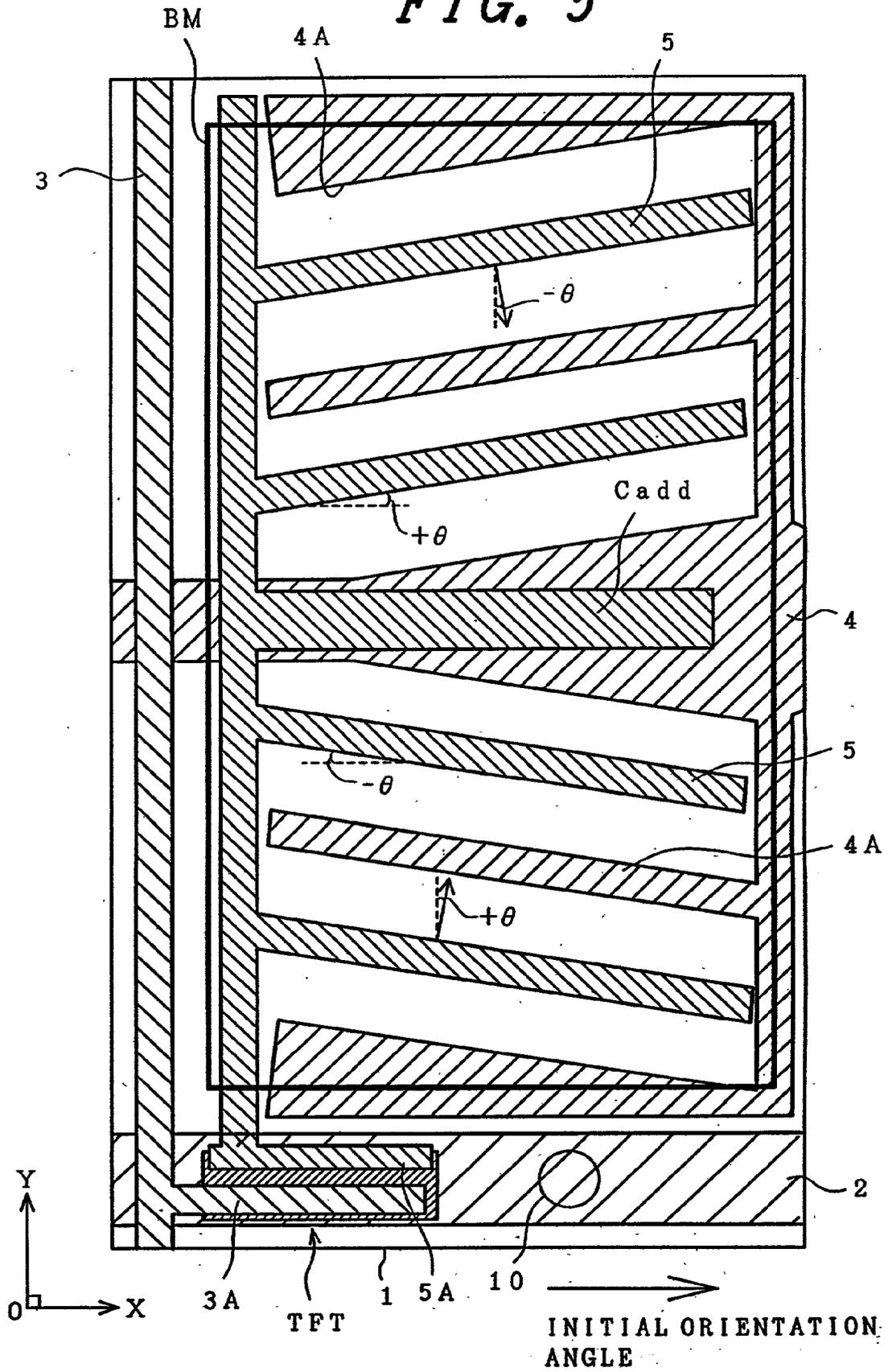


FIG. 6

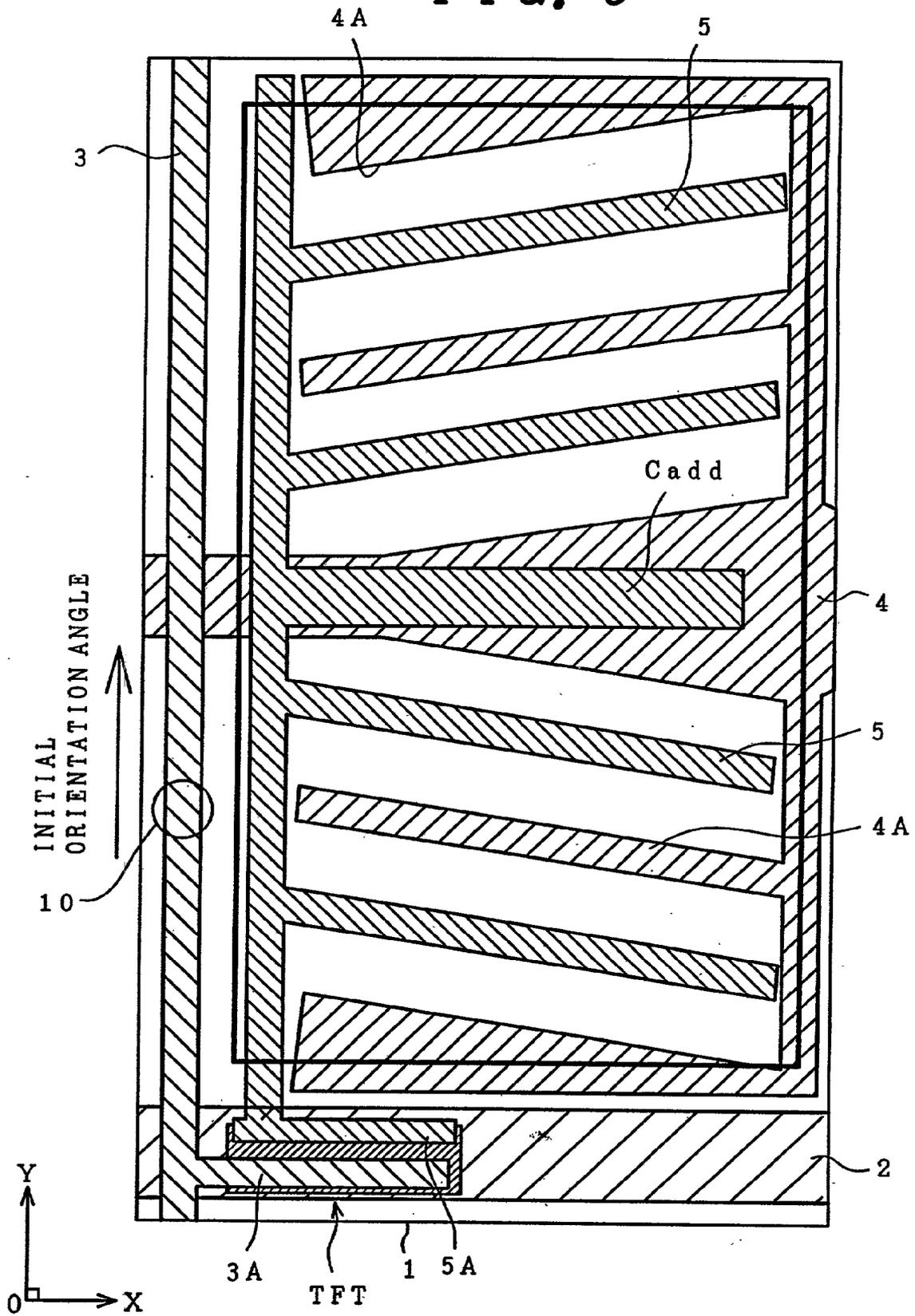


FIG. 7A

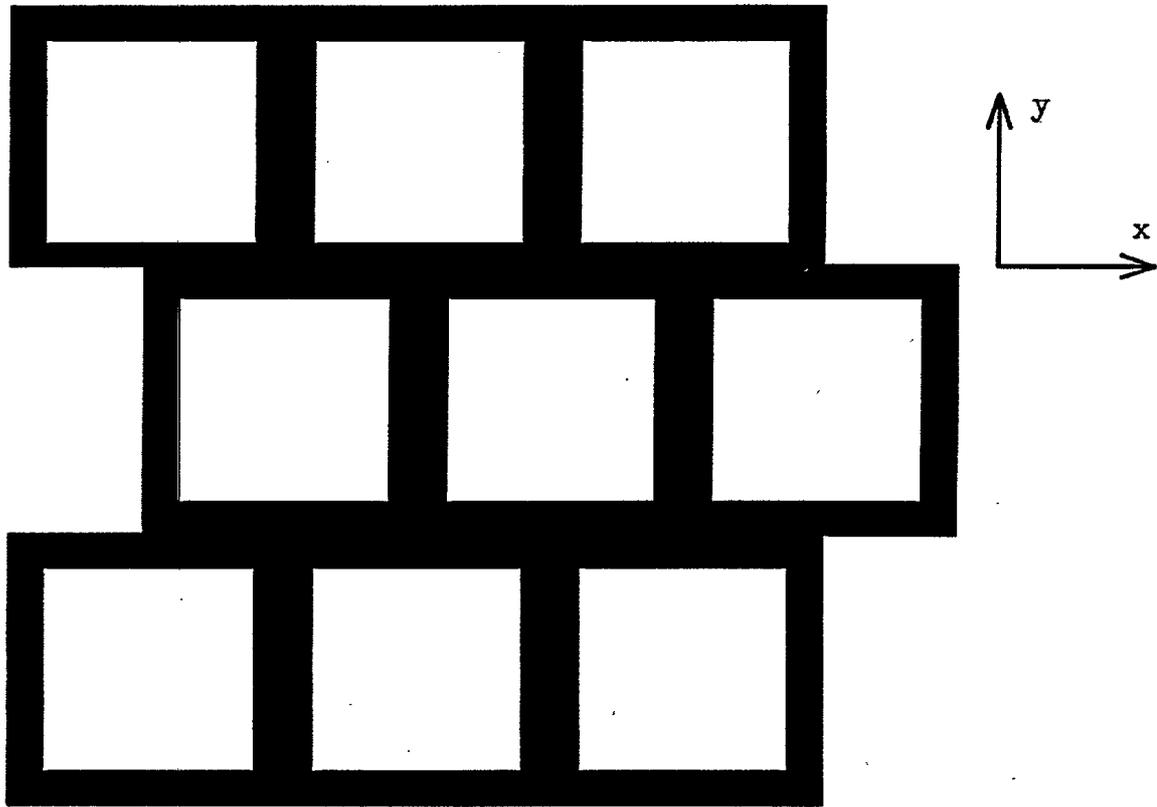


FIG. 7B

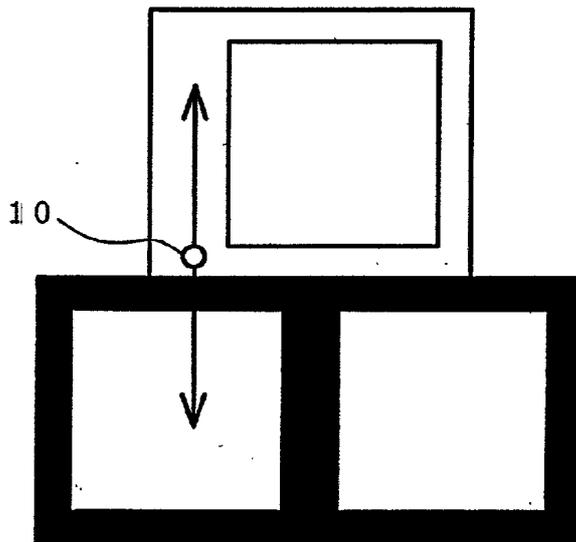


FIG. 8

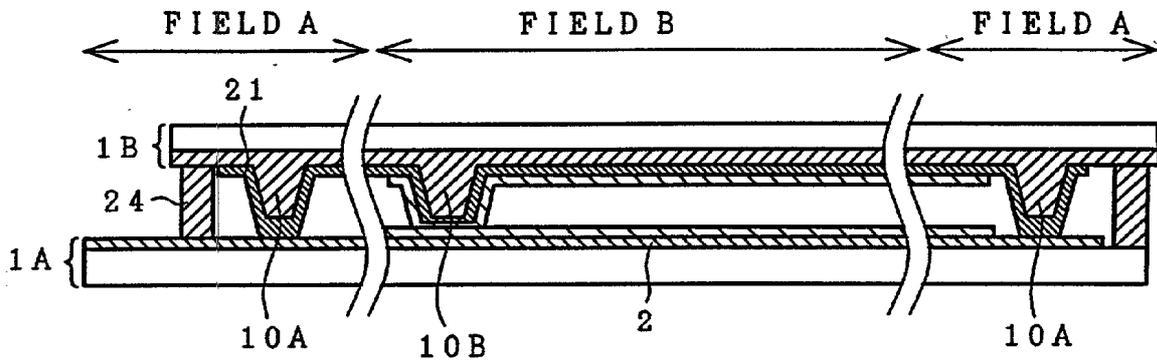


FIG. 9

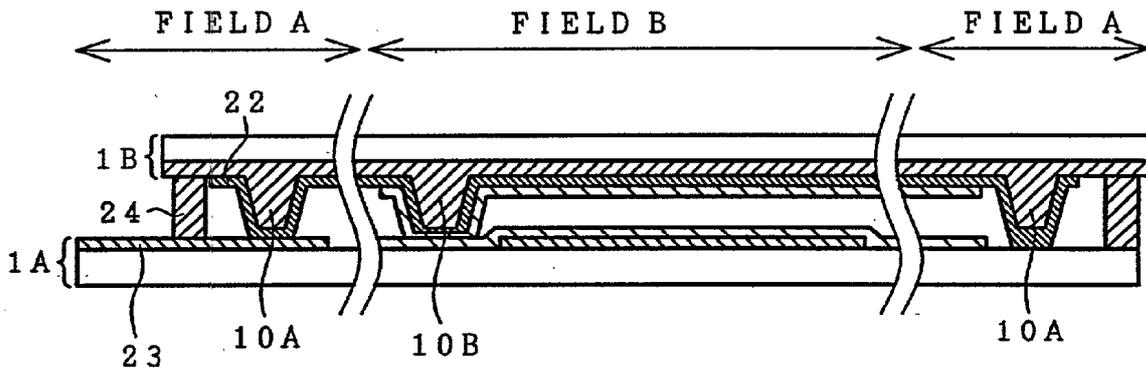


FIG. 10

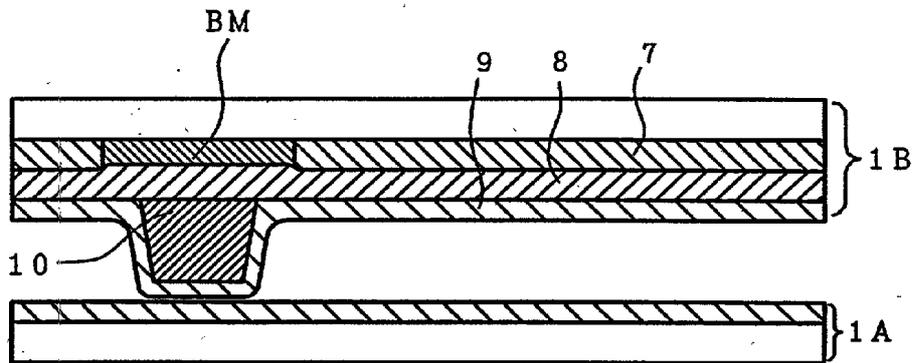


FIG. 11A

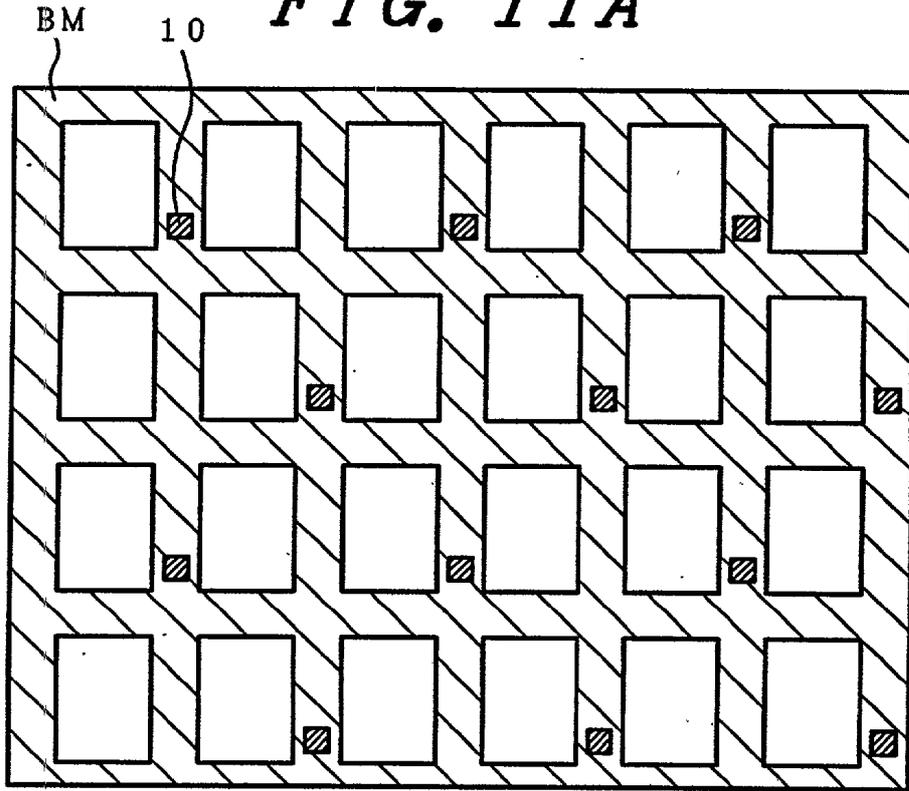


FIG. 11B

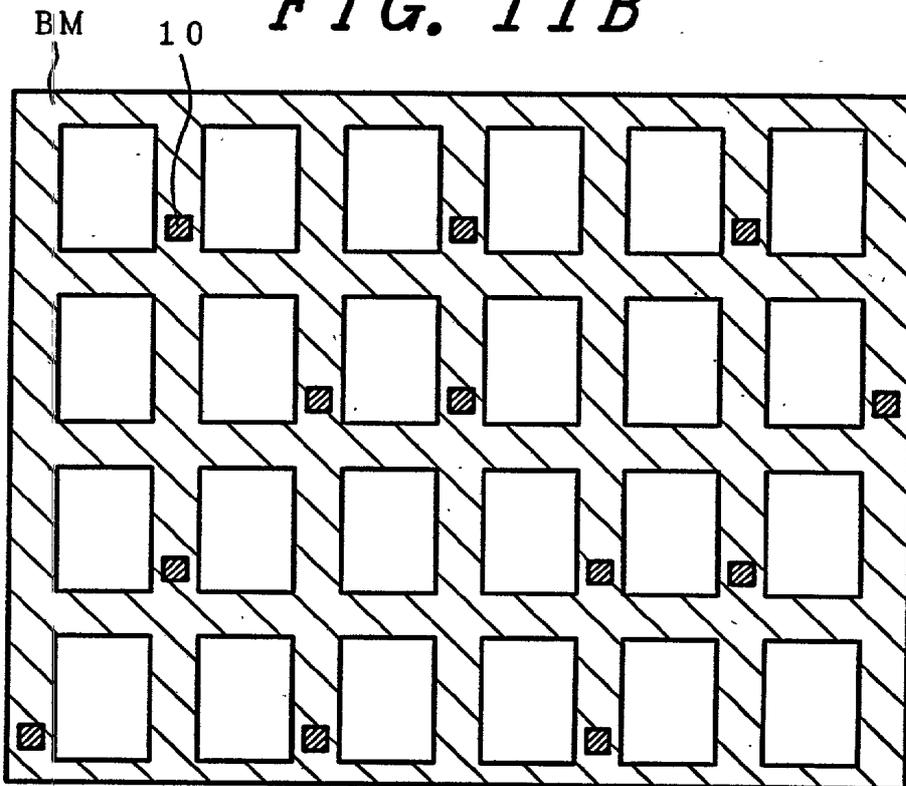


FIG. 12

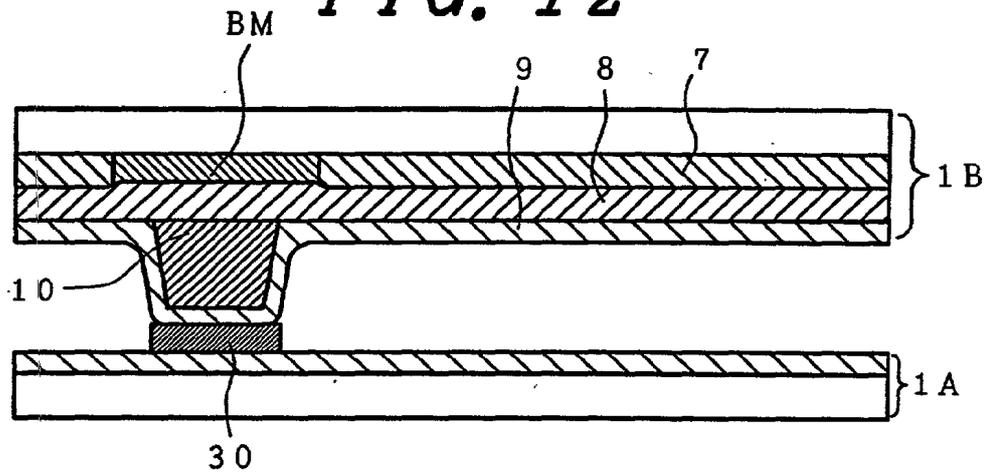


FIG. 13A



FIG. 13B

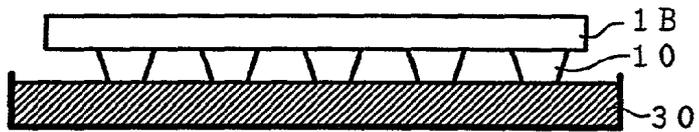


FIG. 13C

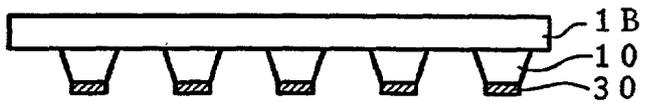


FIG. 13D

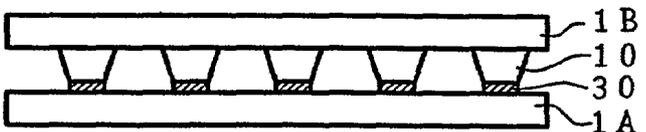


FIG. 13E

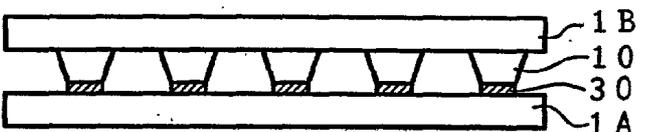


FIG. 14A

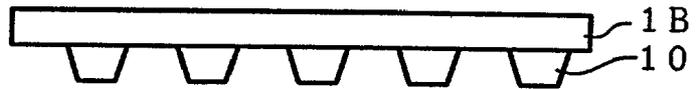


FIG. 14B

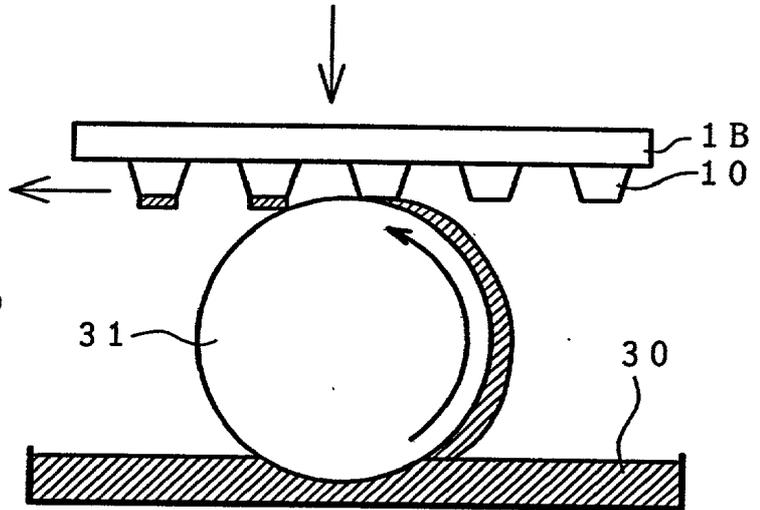


FIG. 14C



FIG. 14D

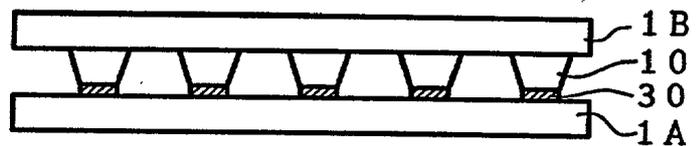


FIG. 14E

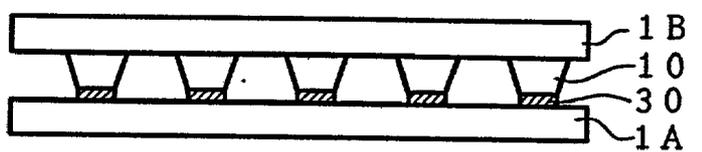


FIG. 15

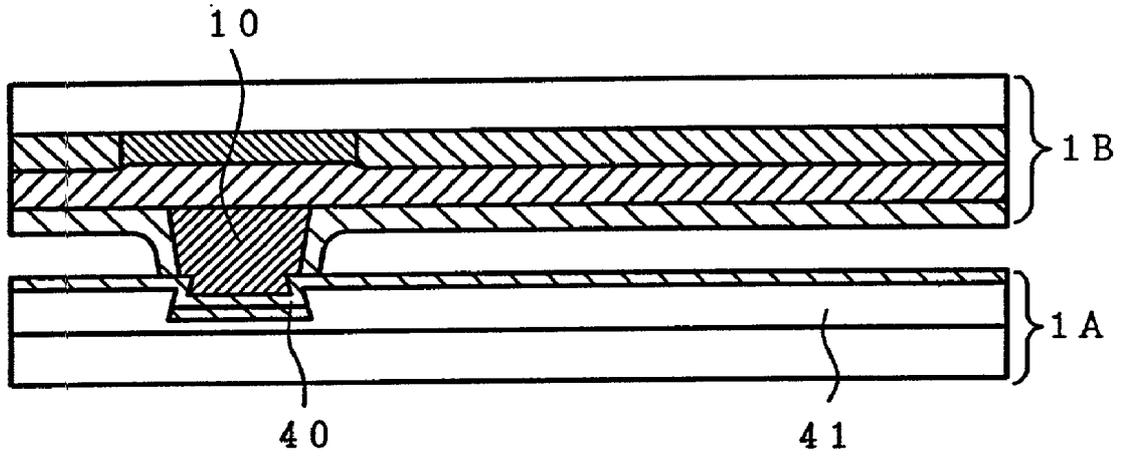


FIG. 16

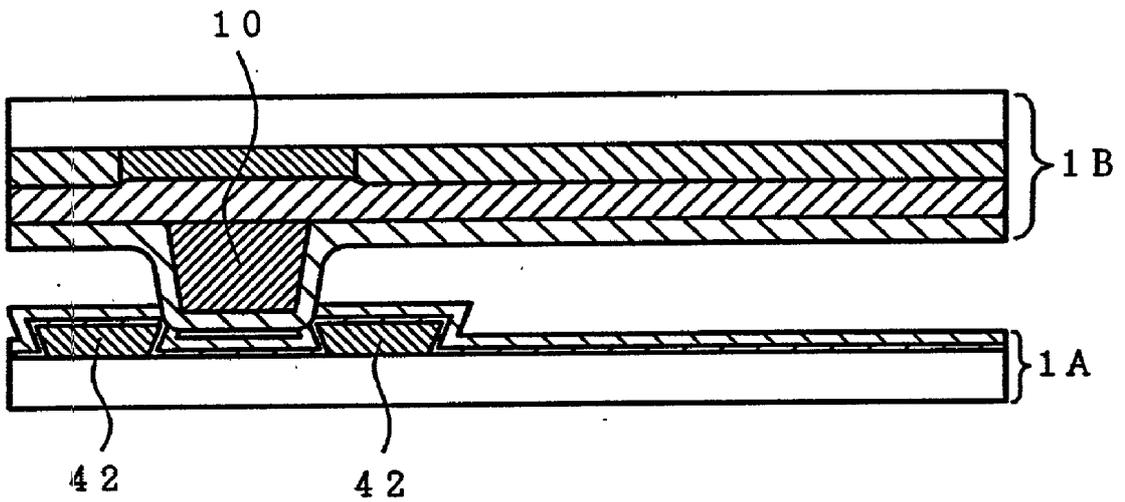


FIG. 17

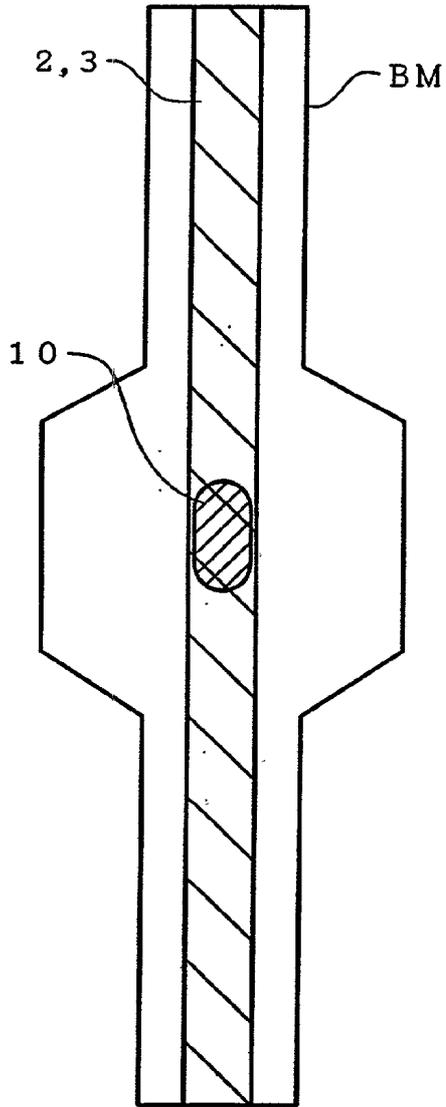


FIG. 18

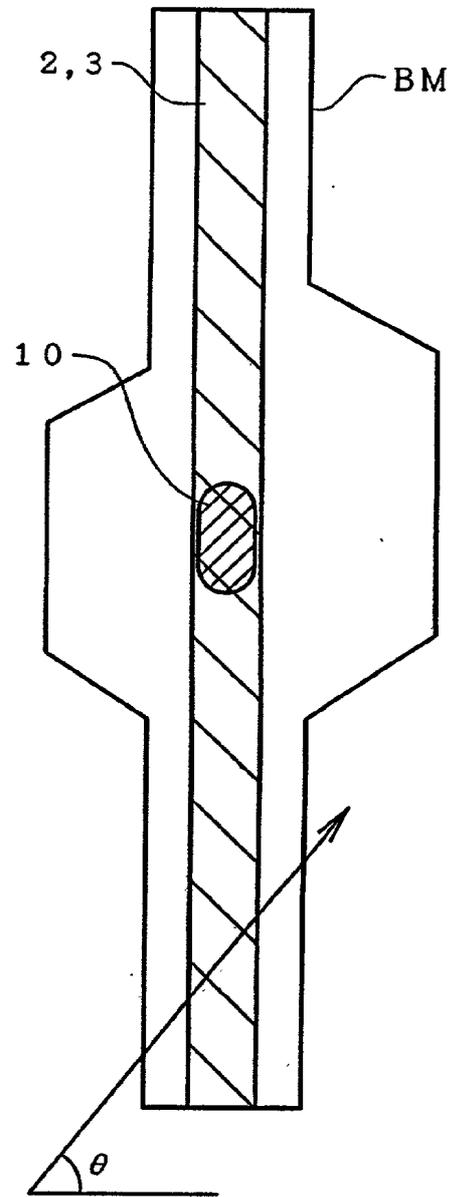


FIG. 19

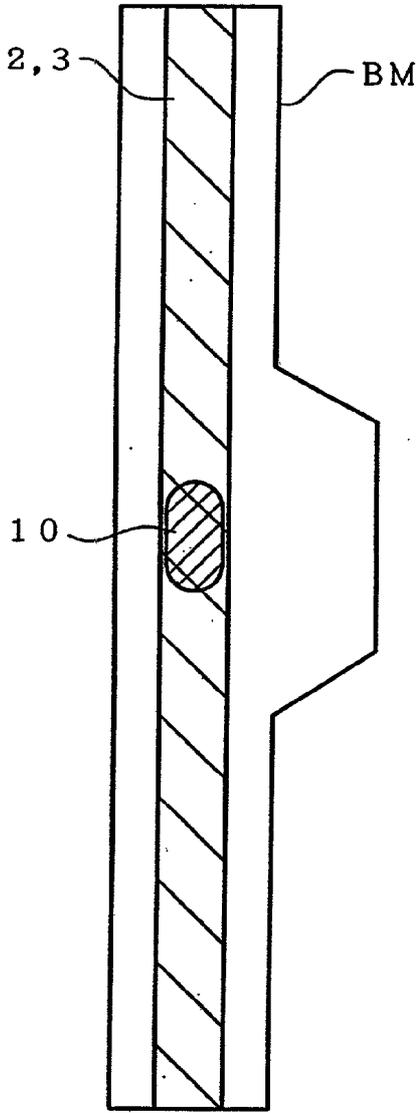


FIG. 20

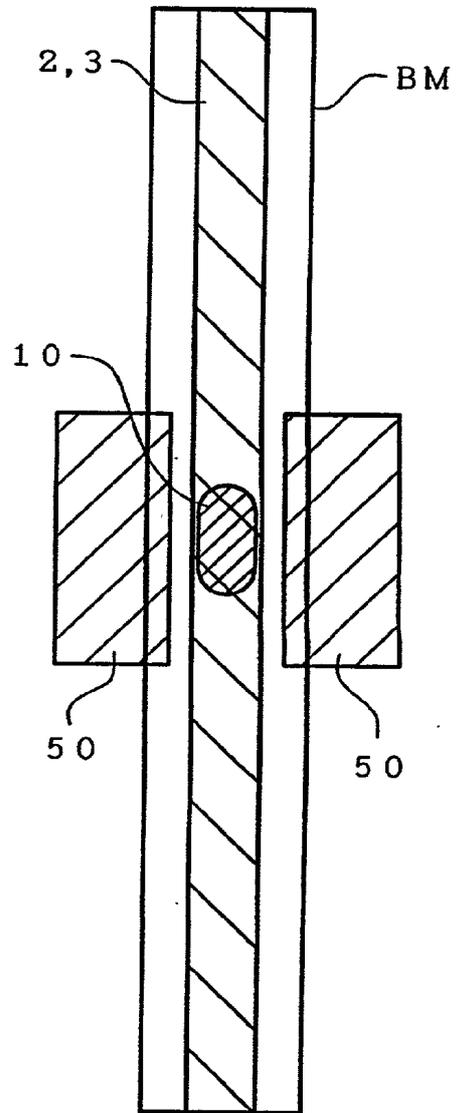


FIG. 21

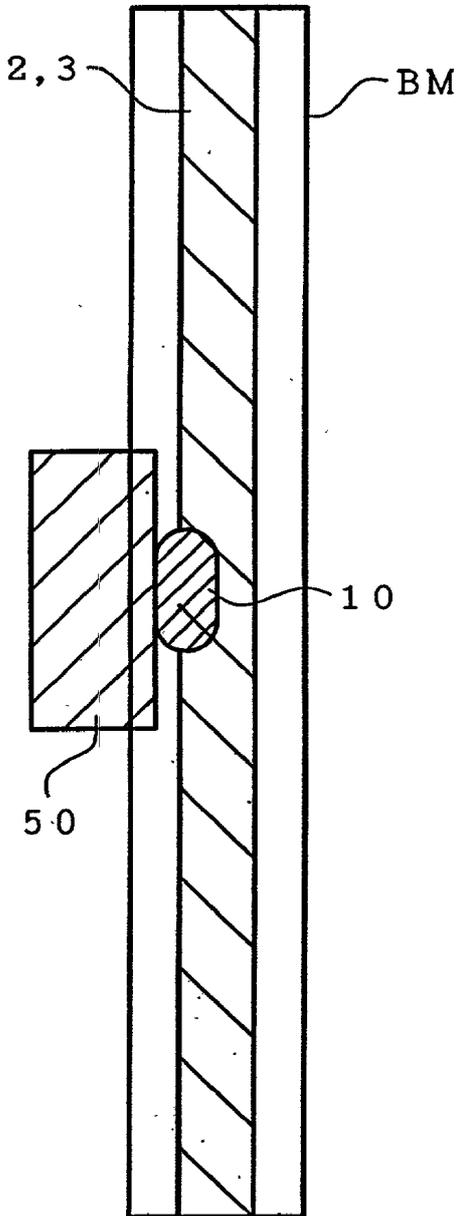


FIG. 22

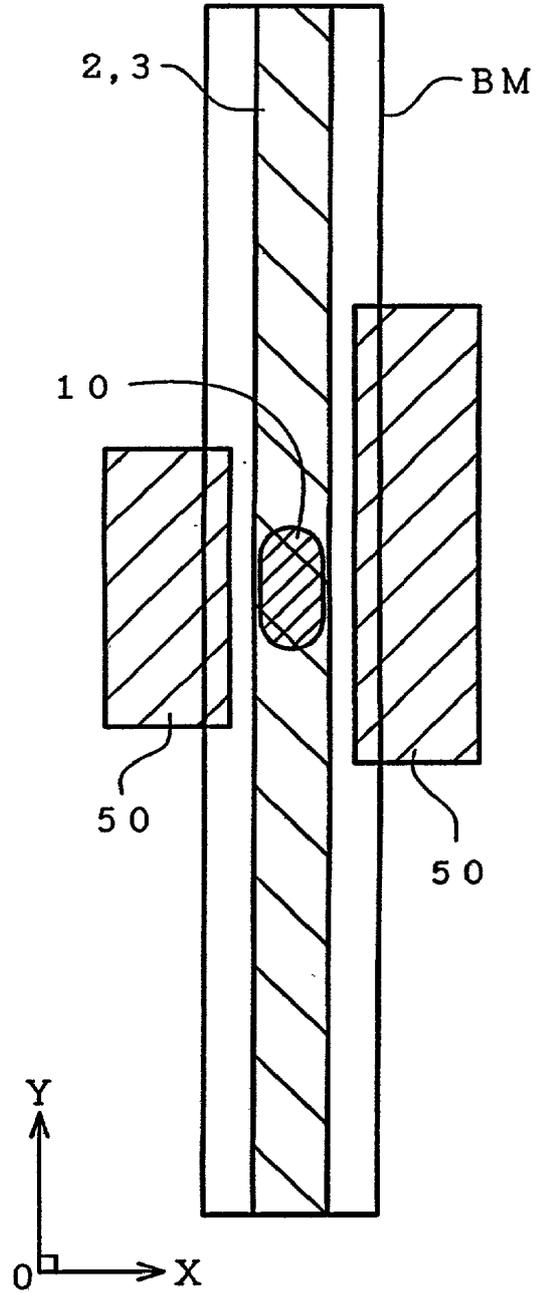


FIG. 23

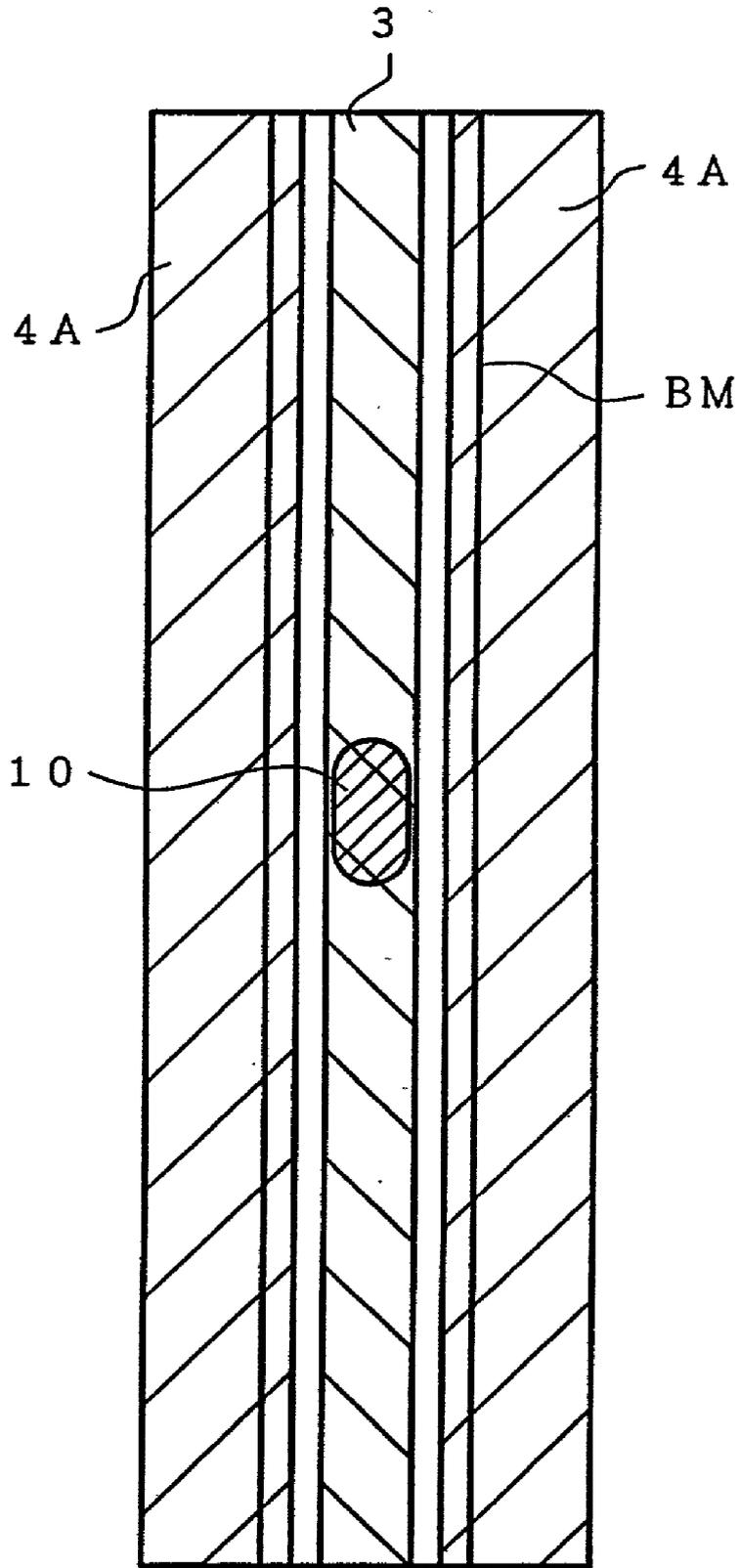


FIG. 24A

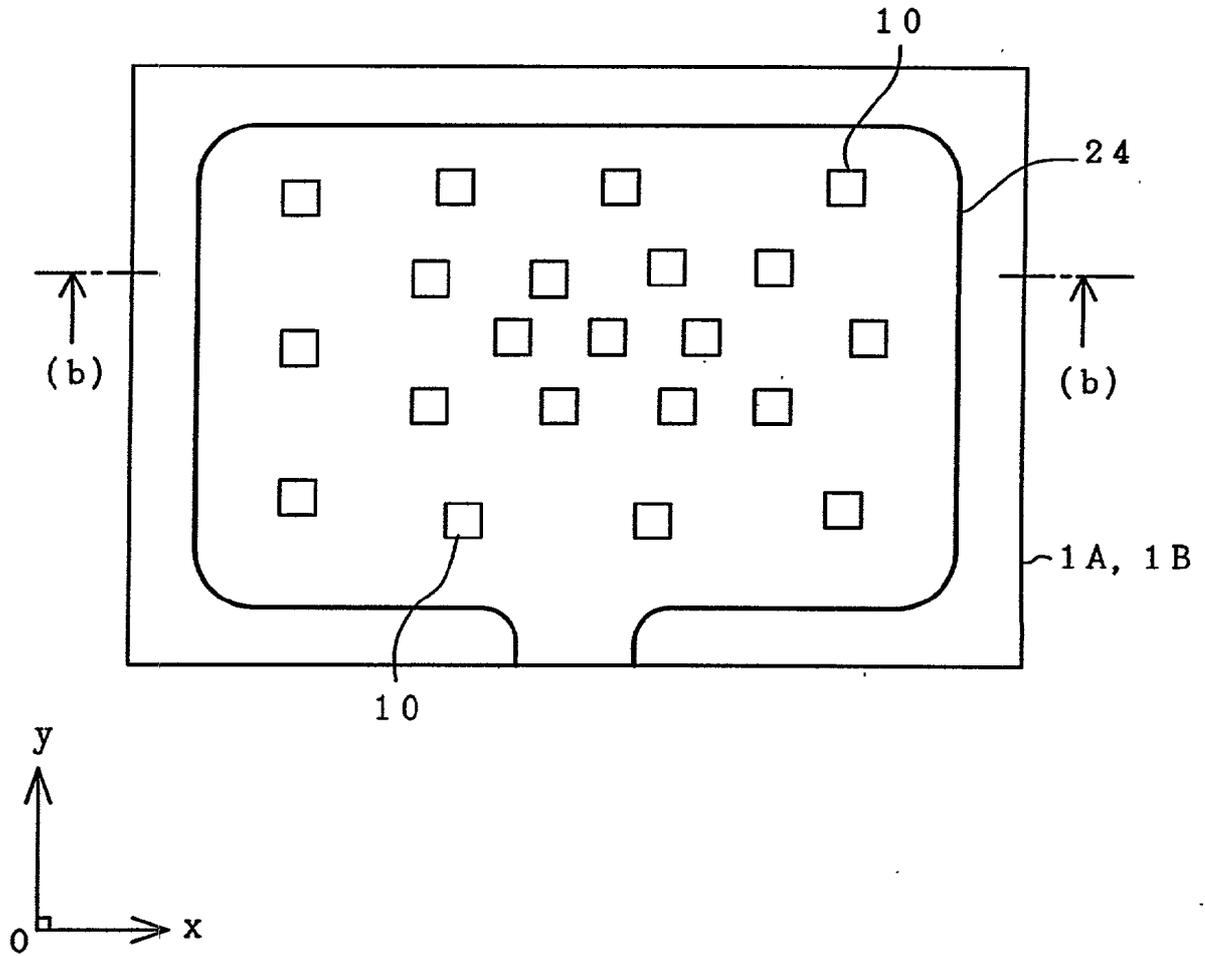


FIG. 24B

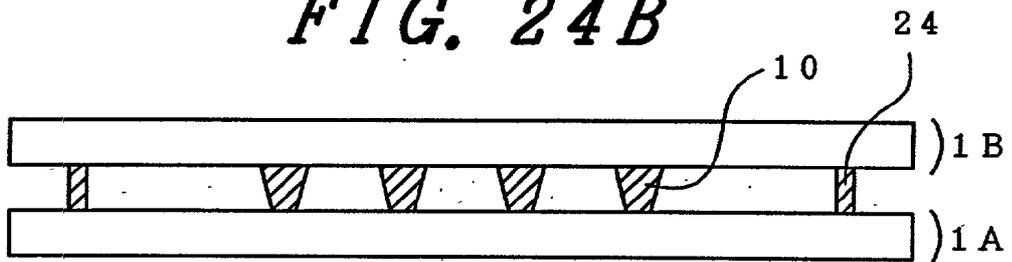


FIG. 25

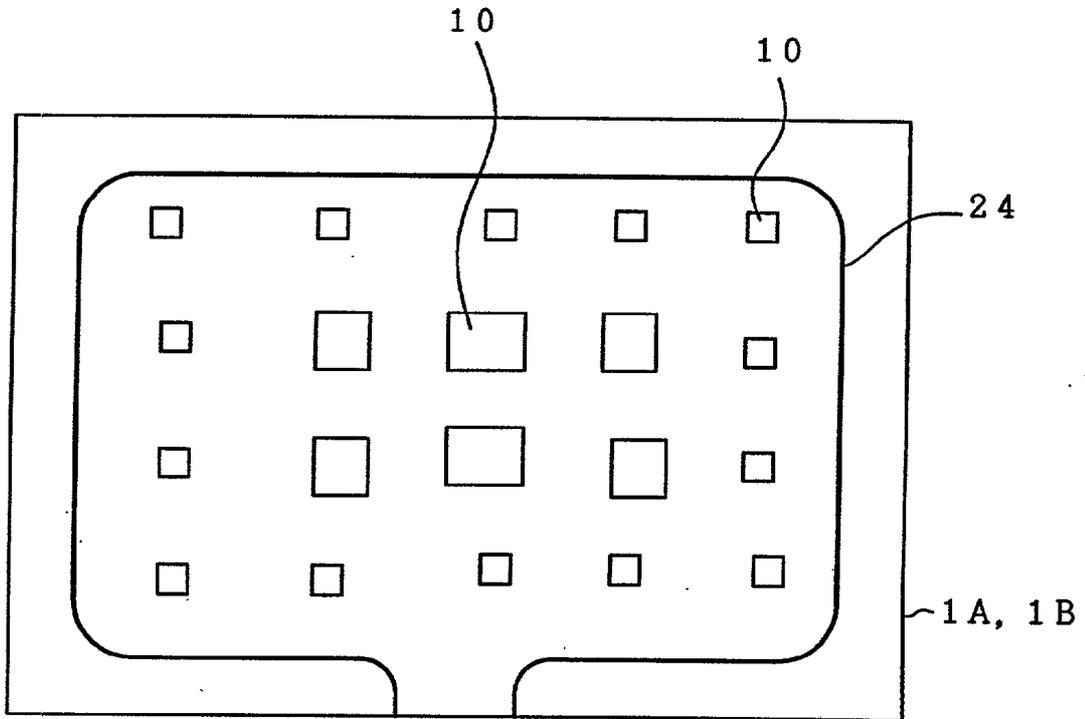


FIG. 26

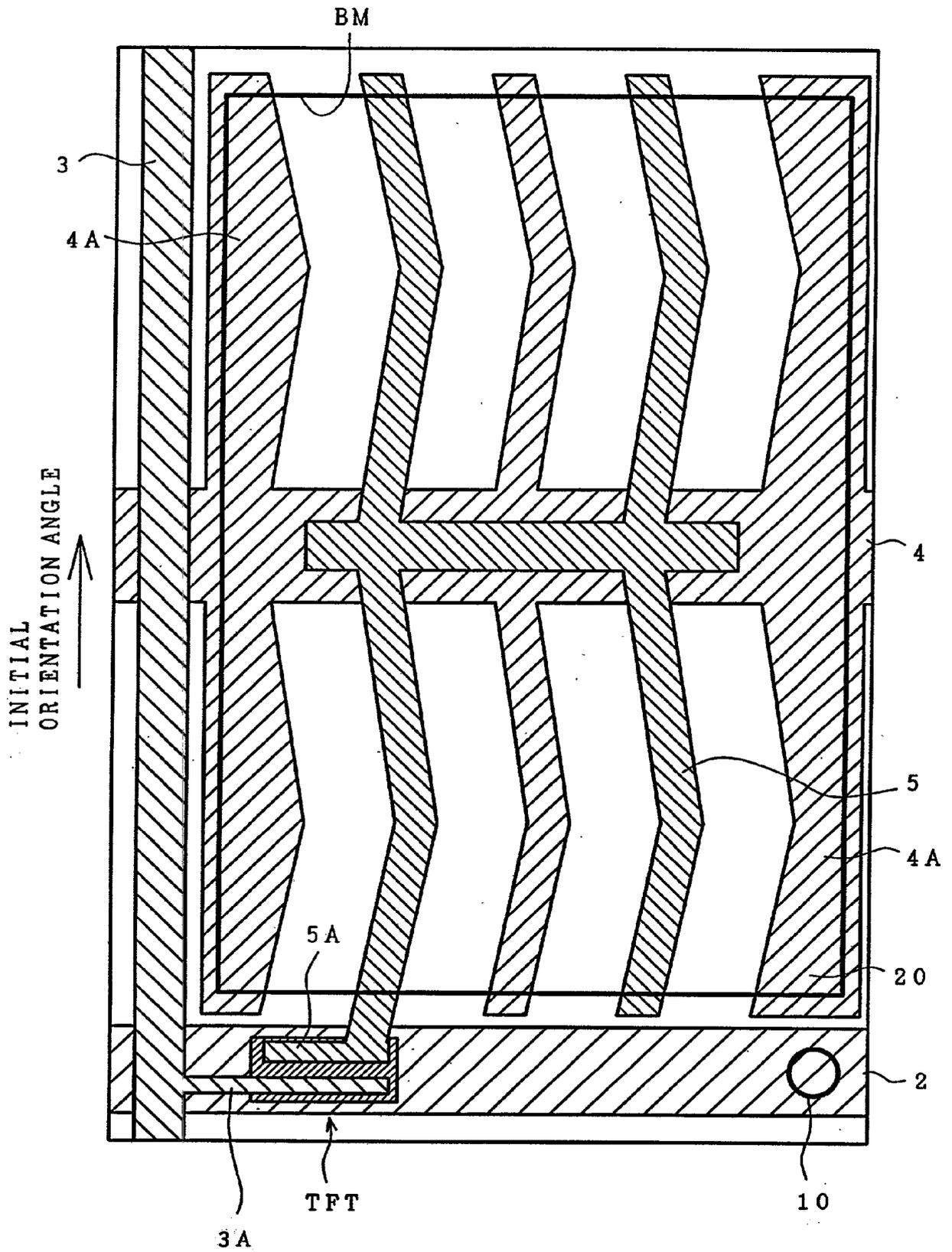


FIG. 27

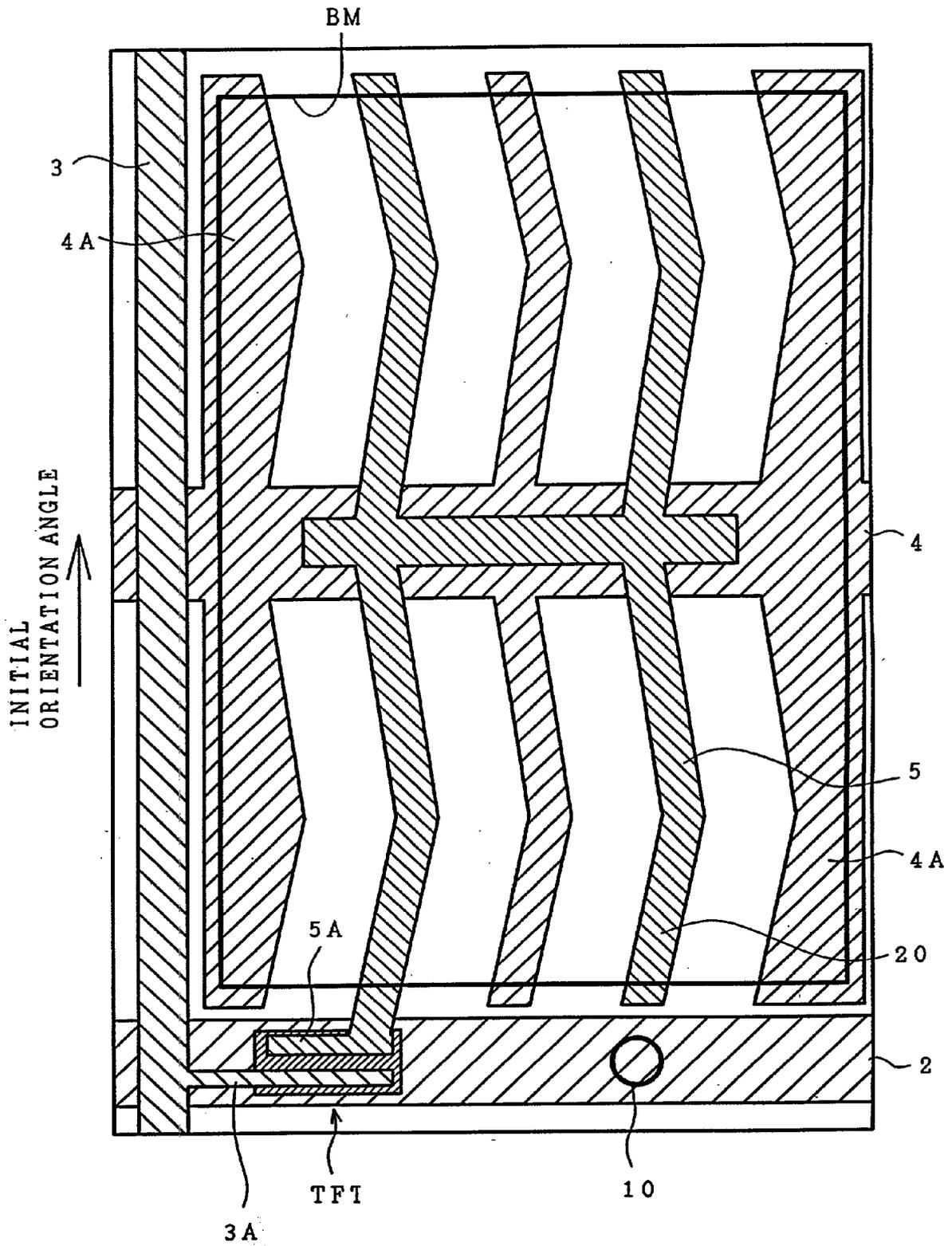


FIG. 28

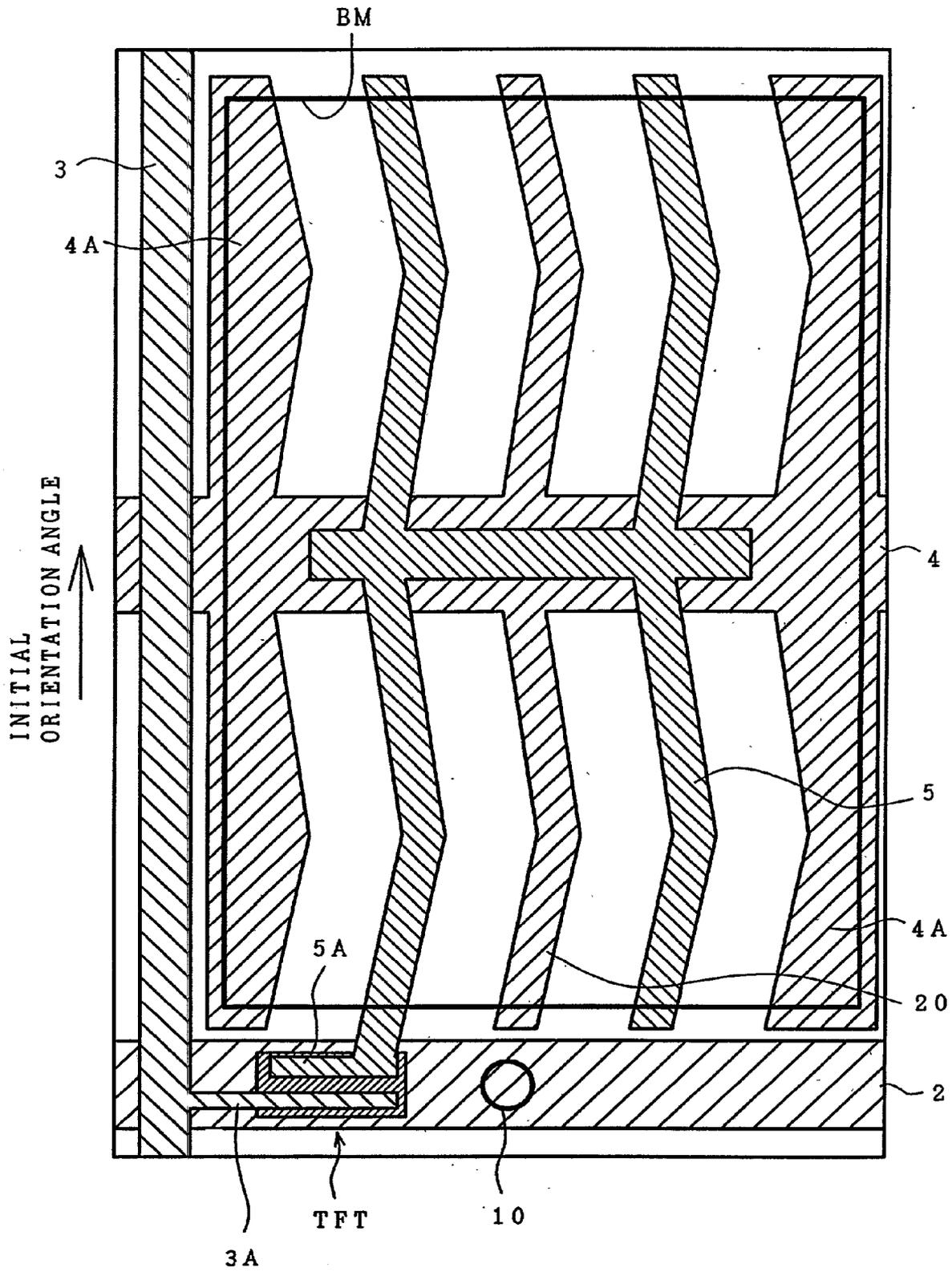


FIG. 29

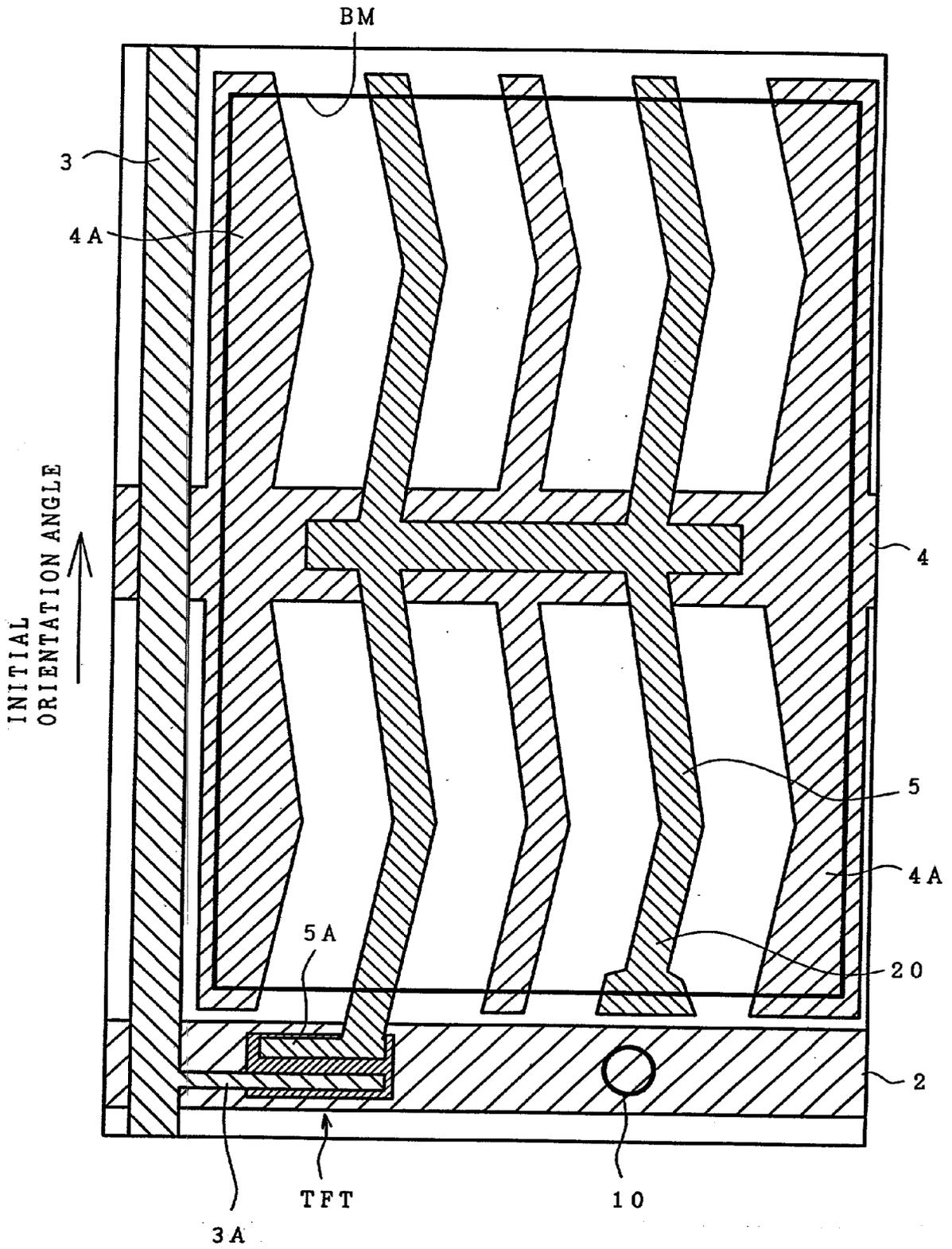
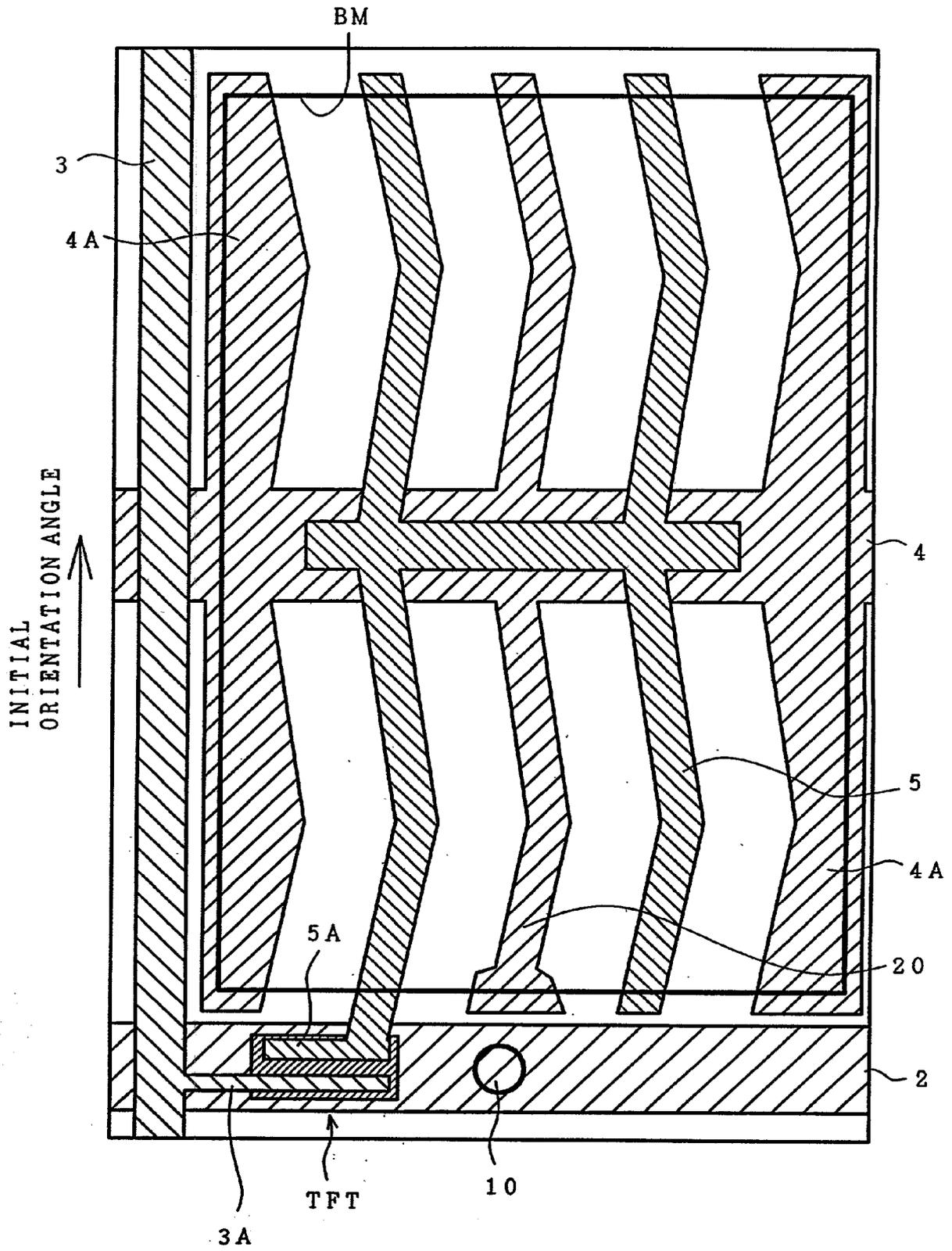


FIG. 30



LIQUID CRYSTAL DISPLAY

BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] The present invention relates to a liquid crystal display device. More specifically, the invention relates to a liquid crystal display device with high contrast characteristics of the multi-domain system using spacers.

[0003] 2. Description of the Prior Art

[0004] Active matrix liquid crystal display devices using active elements as represented by thin-film transistors (TFT) have now been widely used as display terminals of OA equipment since they are light in weight and have picture quality equivalent to that of cathode-ray tubes.

[0005] The display system of the liquid crystal display devices can be roughly divided into two. One is a system in which liquid crystals are sandwiched by two substrates having transparent electrodes and are driven by a voltage applied to the transparent electrodes, and light incident upon the liquid crystals and transmitted through the transparent electrodes is modulated to achieve display.

[0006] Another one is a system in which liquid crystals are driven by an electric field which is nearly in parallel with the surface of a substrate between two electrodes that are formed on the same substrate, and light incident upon the liquid crystals through a gap between the two electrodes is modulated to provide display.

[0007] Features of the latter system have been disclosed in, for example, U.S. Pat. Nos. 5,576,867, 5,599,285, 5,598,285, 5,786,876, 5,956,111 and Japanese Patent No. 02907137. Moreover features of a multi-domain system IPS (In-Plane Switching mode) liquid crystal display have been disclosed in, for example, U.S. Pat No. 5,745,207

[0008] In general, an orientation process that prepares an orientation film for controlling liquid crystal molecules is done by rubbing using a rotating rubbing roller on the surface of an orientation film. However, when spaces are formed on an orientation layer, an orientation process can cause alignment defects as a result of the differences in surface level. Specifically, when spacers are formed in the display region, alignment defect can result in an improper image display.

SUMMARY OF THE INVENTION

[0009] Accordingly, an object of the present invention is to provide multi-domain system IPS (In-Plane Switching mode) liquid crystal display which have high contrast and do not suffer from improper image display. This invention provide a remarkable high contrast display (350:1) as compared to current model displays (200:1).

[0010] According to one aspect of the present invention, there is provided a black matrix formed between a pair of substrates and formed over a plurality of drain lines and gate lines, a plurality of spacers arranged under the black matrix, wherein a plurality of liquid crystal molecules aligned to a boundary direction of the black matrix in the OFF state, and, having a first group and a second group in the ON state, wherein a switching angle of the second is opposite to a switching angle of the first group.

[0011] According to another aspect of the present invention, there is provided a black matrix formed between a pair of substrates and formed over a plurality of drain lines and gate lines, a plurality of spacers arranged under the black matrix, wherein a plurality of liquid crystal molecules having Y direction of an initial orientation angle, and twisted to +X direction and -X direction when a driving voltage is applied.

[0012] According to another aspect of the present invention, there is provided a plurality of liquid crystal molecules having a positive dielectric constant anisotropy and interposed between a pair of orientation layers, a black matrix formed over another of a pair of substrates in a matrix shape, a plurality of spacers formed between the black matrix and one of the pair of orientation layers, wherein a plurality of liquid crystal molecules having Y direction of an initial orientation angle, and twisted to +X direction and -X direction when a driving voltage is applied.

[0013] According to another aspect of the present invention, there is provided a plurality of liquid crystal molecules having a negative dielectric constant anisotropy and interposed between said pair of orientation layers, a black matrix formed over another of a pair of substrates in a matrix shape, a plurality of spacers formed between the black matrix and one of a pair of orientation layers, wherein said plurality crystal molecules having X direction of an initial orientation angle, and twisted to +Y direction and -Y direction when driving voltage is applied.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014] FIG. 1 is a plan view of the pixel of a liquid crystal display according to a first embodiment of the present invention.

[0015] FIG. 2 is a sectional view of the pixel of a liquid crystal display cut along the line II-II in 1. FIG. 3 is a sectional view of the spacer in illustration of a alignment defect on an orientation layer.

[0016] FIG. 4 is a plan view of the pixel of a liquid crystal display according to a second embodiment of the present invention.

[0017] FIG. 5 is a plan view of the pixel of a liquid crystal display according to a third embodiment of the present invention.

[0018] FIG. 6 is a plan view of the pixel of a liquid crystal display according to a fourth embodiment of the present invention.

[0019] FIG. 7 is a plan view of the pixel of a liquid crystal display according to a fifth embodiment of the present invention.

[0020] FIG. 8 is a sectional view of the liquid crystal display according to a seventh embodiment of the present invention.

[0021] FIG. 9 is a sectional view of the liquid crystal display according to a eighth embodiment of the present invention.

[0022] FIG. 10 is a sectional view of the spacer of a liquid crystal display according to a tenth embodiment of the present invention.

[0023] FIG. 11A is a plan view of the pixel of a liquid crystal display according to a ninth embodiment of the present invention.

[0024] FIG. 11B is a plan view of the pixel of a liquid crystal display according to a tenth embodiment of the present invention.

[0025] FIG. 12 is a sectional view of the spacer of a liquid crystal display according to an eleventh embodiment 11 of the present invention.

[0026] FIG. 13 is a flow chart of a sectional view of A spacer illustrating the steps A to E of fabricating a substrate 1B.

[0027] FIG. 14, is a flow chart of a sectional view of a, spacer illustrating the steps A to E of fabricating a substrate 1B.

[0028] FIG. 15 is a sectional view of the spacer of A liquid crystal display according to a fourteenth embodiment of the present invention.

[0029] FIG. 16 is a sectional view of the spacer of a liquid crystal display according another invention of the fourteenth embodiment.

[0030] FIG. 17 is a sectional view of the spacer of a liquid crystal display according to a fifteenth embodiment of the present invention.

[0031] FIG. 18 is a plan view of the pixel of a liquid crystal display according to a sixteenth embodiment of the present invention.

[0032] FIG. 19 is a plan view of the pixel of a liquid crystal display according to another invention of the sixteenth embodiment.

[0033] FIG. 20 is a plan view of the pixel of a liquid crystal display according to a seventh embodiment of the present invention.

[0034] FIG. 21 is a plan view of the pixel of a liquid crystal display according to an eighteenth embodiment of the present invention.

[0035] FIG. 22 is a plan view of the pixel of a liquid crystal display according to a nineteenth embodiment of the present invention.

[0036] FIG. 23 is a plan view of the pixel of a liquid crystal display according to a twenty-first embodiment of the present invention.

[0037] FIG. 24 is a plan view of the pixel of a liquid crystal display according to a twenty-second embodiment of the present invention.

[0038] FIG. 25 is a plan view of the pixel of a liquid crystal display according to a twenty-third embodiment of the present invention.

[0039] FIG. 26 is a plan view of the pixel of a liquid crystal display according to a twenty-fifth embodiment of the present invention.

[0040] FIG. 27 is a plan view of the pixel of a liquid crystal display according to a twenty-sixth embodiment of the present invention.

[0041] FIG. 28 is a plan view of the pixel of a liquid crystal display according to a twenty-seventh embodiment of the present invention.

[0042] FIG. 29 is a plan view of the pixel of a liquid crystal display according to a twenty-eighth embodiment of the present invention.

[0043] FIG. 30 is a plan view of the pixel of a liquid crystal display according to a twenty-ninth embodiment of the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0044] This present invention will hereunder be described in more detail.

[0045] [Embodiment 1]

[0046] FIG. 1 is a plan view of the pixel of a liquid crystal display according to a first embodiment of the present invention. In the first embodiment, a plurality of liquid crystal molecules having a positive dielectric constant anisotropy, and FIG. 1 shows one pixel of a plurality of pixels forming in matrix shape. Each pixel is defined in a region where two adjacent gate lines 2 and two adjacent drain lines 3 intersect. And each pixel includes a thin-film transistor TFT, a capacitor Cadd, a pixel electrode 5, a counter electrode 4A and a counter voltage line 4. The gate lines 2 and the counter voltage lines 4 extend in the X direction in the drawing and are arranged in the Y direction. The drain lines 3 extend in the Y direction and are arranged in the X direction. The pixel electrode 5 is connected to the thin-film transistor TFT. The material of the gate line 2 is selected from a group of chromium, aluminum and molybdenum, and the counter voltage line 4 is formed by the same material with the gate line 2.

[0047] A counter voltage is supplied to the counter electrodes 4A via the counter voltage line 4 from an external circuit. And the counter electrodes 4A and the counter voltage line 4 have a shape of a letter H in the each pixels. The pixel electrode 5 and the counter electrode 4A are opposed to each other, and the optical state of the liquid crystals LC (ON state) is controlled by an electric field between each pixel electrode 5 and the counter electrode 4A, thereby to control the display. The pixel electrodes 5 and the counter electrodes 4A are formed like comb teeth, and extend in a slender form in the Y direction.

[0048] Here, the pixel electrode 5 and the counter electrode 4A have a herringbone shape for the multi-domain system. The counter electrode 4A maintains the distance with the pixel electrode 5 by increasing the width of the counter electrode 4A. Hence, the face of the drain line 3 side of the counter electrode 4A is formed parallel with the drain line 3, and the face of the pixel electrode 5 side of the counter electrode 4A is formed parallel with the pixel electrode 5.

[0049] Therefore, the direction of the electric field which is disposed in the upper pixel region of the counter voltage line 4 in a pixel between the pixel electrode 5 and the counter electrode 4A is different from the lower one pixel region. In FIG. 1, the direction of the electric field which is disposed at the upper pixel region has the angle of + θ with respect to the direction parallel to the counter voltage line 4, but the

direction of the electric field which is disposed at the lower one has the angle of $-\theta$ with respect to the direction parallel to the counter voltage line 4.

[0050] The above-mentioned shape provides liquid crystal molecules to switch in reverse directions at each pixel region, and a light transmittance change at each pixel.

[0051] In this invention, it can prevent the inversion of luminance when the view point of the observer is at an angle with respect to the slanting direction of the liquid crystal display panel. In the first embodiment, one pixel has the difference angle of $\pm\theta$ dividing upper and lower pixel region, but it allows two adjoining two pixels to have difference angles of $+\theta$ and $-\theta$, respectively.

[0052] Thin film transistor TFT consists of the gate line 2, a drain electrode 3A, a source electrode 5A, a semiconductor layer 6 and a gate insulator (silicon nitride). An insulator is formed on the gate line 2 as a gate insulator, and the semiconductor layer 6 whose material is amorphous silicon formed on the gate insulator. A drain electrode 3A and a source electrode 5A are formed on the semiconductor layer 6; thin film transistor TFT becomes an inverted stagger type thin film transistor. The drain electrode 3A and a source electrode 5A are same layer, and the drain electrode 3A contact with the drain line, and the source electrode 5A contact with the pixel electrode, respectively.

[0053] In the first embodiment, an initial orientation angle is the Y direction, so a rubbing direction is also the Y direction. The absolute value of the angle between the drain line 3 and the direction of the electric field E is larger than the absolute value of the angle between the counter voltage line 4 and the direction of the electric field E. A capacitance Cadd consists of the pixel electrode 5, the counter voltage line 4 and the gate insulator. To form the capacitance Cadd on the counter voltage line 4, the capacitance Cadd can be larger than any region in a pixel. The capacitance Cadd functions to improve the data retentivity. A protective film PAS (silicon nitride) is formed over the transparent substrate 1A after forming a plurality of thin film transistors TFT and pixels for preventing to touch with liquid, crystal molecules. Furthermore, an orientation layer which controls an initial orientation angle of liquid crystal molecules is formed over the protective film PAS. The orientation layer is surface rubbed with a rubbing roller along with the Y direction after coating synthetic resins thereon.

[0054] A color filter substrate 1B consists of a black matrix BM, a color filter 7 and an orientation layer 9. In FIG. 1, a boundary of the black matrix BM is shown as a plan view, and in FIG. 2, shows a sectional view of the black matrix BM. Here, the orientation layer 9 of the color filter substrate 1B is a surface rubbed with rubbing roller in the Y direction after coating synthetic resins thereon the same as the orientation layer of the transparent substrate 1A. Also, the orientation direction of each orientation layer of the transparent substrate 1A side and that of the color filter substrate 1B are the same Y direction. Therefore, a plurality of liquid crystal molecules are aligned to a boundary direction of the black matrix in the OFF state, and having the upper pixel region (a first group) and the lower pixel region (a second group) in the ON state, wherein a switching angle of the second group is opposite to a switching angle of the first group.

[0055] A spacer 10 is formed between the transparent substrate 1A and the color filter substrate 1B and formed over the drain line 3 to keep a gap therebetween.

[0056] FIG. 2 is a sectional view of the pixel of a liquid crystal display cut along the line II-II in FIG. 1. The black matrix BM is formed on the color filter substrate 1B which faces with liquid crystal molecules, the spacer 10 is made of the black matrix BM, the spacers 10 function to keep a gap between the transparent substrate 1A side and the color filter substrate 1B.

[0057] The black matrix BM is formed by the twice selective etching of a photo lithography method, the first selective etching to make the spacer 10 after coating black matrix material very thick, and the second selective etching to make a hole for the color filter 7. The black matrix BM is formed with either the drain line 3 or the gate line 2 thereon; a light transmittance region between the pixel electrode 5 and the counter electrode 4A are formed in an open region of the black matrix BM. An end of the pixel electrode 5 and the edge of the counter electrode 4A are covered with the black matrix BM, because this region is not the uniform electric field area between the pixel electrode 5 and the counter electrode 4A.

[0058] The open region of the black matrix BM would be able to improve an aperture ratio if it is large enough. On the other hand, the open region of the black matrix should be designed to cover the unnecessary electric field between the drain line 3 and the counter electrode 4A, and the improper electric field between the pixel electrode 5 and the edge of counter electrode 4A. The color filter 7 is former at the open region of the black matrix BM. Then, the over coating film 8 is formed over the color filter 7. Finally, the orientation layer 9 is formed over the over coating film 8.

[0059] FIG. 3 is a sectional view of the spacer 10 in illustration of an alignment defect on an orientation layer. The orientation layer 9 has its surface rubbed with rubbing roller 100 in the right direction (in the Y direction in FIG. 1). Here, an area 200 is not orientated and causes an alignment defect with the spacer 10. Hence, in the area 200, the rubbing roller 100 out of contact with surface of the orientation layer 9, is not rubbed with the rubbing roller 100. Therefore, in this invention, the area 200 brings in the Y direction by using the multi-domain system IPS, and covers the area 200 by the black matrix. As a result, this combination provides remarkably high contrast (350:1) for a liquid crystal display.

[0060] Moreover, the spacer 10 is available to form over the counter voltage line 4 as other embodiment. As the counter voltage line 4 can be designed wider than the gate line 2, the drain line 3 and another electrode, the area 200 can be covered by the counter voltage line 4 easily.

[0061] [Embodiment 2]

[0062] FIG. 4 is a plan view of the pixel of a liquid crystal display according to a second embodiment of the present invention. In the second embodiment, a plurality of liquid crystal molecules having a negative dielectric constant anisotropy, rubbing direction (initial orientation angle) is in the X direction, so the spacer 10 is disposed over the gate line 2. As a result, even if alignment defects are formed the area 200 on the gate line 2, the area 200 is hid by the black

matrix BM. A plurality of liquid crystal molecules have the X direction for an initial orientation angle, and are twisted to +Y direction and -Y direction while a driving voltage is applied.

[0063] [Embodiment 3]

[0064] FIG. 5 is a plan view of the pixel of a liquid crystal display according to a third embodiment of the present invention. In the embodiment, a plurality of liquid crystal molecules having a positive dielectric constant anisotropy, the pixel electrodes 5 and the counter electrodes 4A are formed in the direction X which are parallel with the gate line 2. In the third embodiment, an rubbing direction (initial orientation angle) is in the X direction.

[0065] In FIG. 5, the direction of the electric field which is disposed at the upper pixel region has the angle of $-\theta$ with respect to the direction perpendicular to the counter voltage line 4, but the direction of the electric field which is disposed at the lower pixel region has the angle of $+\theta$ with respect to the direction perpendicular to the counter voltage line 4. The above-mentioned shape provides the liquid crystal molecules with the ability to switch in reverse directions at each pixel region, and a light transmittance change in a pixel.

[0066] As each counter electrode 4A is arranged parallel with each pixel electrode 5, each of the counter electrodes 4A has a wider portion than the pixel electrodes 5. The absolute value of the angle between the drain line 3 and the direction of the electric field E is smaller than the absolute value of the angle between the gate line 2 and the direction of the electric field E. In this embodiment, an observer does not recognize the inversion of luminance when the view point of the observer is at an angle with respect to the slanting direction of the liquid crystal display panel.

[0067] A rubbing direction (initial orientation angle) of the transparent substrate 1A and the color filter substrate 1B are in the same X direction as the direction of gate line 2, so the spacer 10 is disposed over the gate line 2. As a result, even if alignment defects are made in the area 200 on the gate line 2, the area 200 is hidden by the black matrix BM in this embodiment.

[0068] This embodiment achieves both advantages of preventing the inversion of luminance when the view point of the observer is at an angle with respect to the slanting direction of the liquid crystal display panel, and of providing remarkably high contrast (350:1) for the liquid crystal display.

[0069] [Embodiment 4]

[0070] FIG. 6 is a plan view of the pixel of a liquid crystal display according to a fourth embodiment of the present invention. In the fourth embodiment, a plurality of liquid crystal molecules having a negative dielectric constant anisotropy; a rubbing direction (initial orientation angle) is in the Y direction. The spacer 10 is disposed over the drain line 3. As a result, even if alignment defects are made in the area 200 on the drain line 3, the area 200 is hidden by the black matrix BM this embodiment.

[0071] That is, it can prevent the inversion of luminance when the view point of the observer is at an angle with respect to the slanting direction of the liquid crystal display panel.

[0072] This embodiment achieves the of advantages both of preventing the inversion of luminance when the view point of the observer is at an angle with respect to the slanting direction of the liquid crystal display panel, and of providing remarkably high contrast (350:1) for the liquid crystal display.

[0073] [Embodiment 5]

[0074] FIG. 7 is a plan view of the pixel of a liquid crystal display according to a fifth embodiment of the present invention. FIG. 7A shows a plan view of an arrangement of a plurality of pixel. A black frames shows the black matrix BM, it's open region corresponds to each pixel. The arrangement in FIG. 7A is called the delta arrangement, one group of pixels in the X direction (a row direction) is offset to the side by $\frac{1}{2}$ pitch relative to another group of pixels. As the delta arrangement, with pixels of red, green and blue adjacent each other, is useful for a color active matrix liquid crystal display. In the fifth embodiment 5, the spacer 10 is formed over the gate line 2, and, the rubbing direction (initial orientation angle) is in the X direction. As a result, even if alignment defects are made the are made in the area 200 on the gate line 2, the area 200 is hidden by the black matrix BM. FIG. 7B shows a plan view of the delta arrangement, when the spacer 10 is formed over the drain line 3 and the rubbing direction (initial orientation angle) is in the Y direction. In this case, alignment defects inside the pixel region, cause an improper image display.

[0075] [Embodiment 6]

[0076] The delta arrangement may be designed such that one group pixels of the Y direction (a column direction) is offset to one side by $\frac{1}{2}$ pitch relative to another group of pixels. In a sixth embodiment, the spacer 10 is formed over the drain line 3, the rubbing direction (initial orientation angle) is in the direction along with the drain line 3.

[0077] [Embodiment 7]

[0078] FIG. 8 is a sectional view of the liquid crystal display according to a seventh embodiment of the present invention. FIG. 8 shows a sectional view of the spacer 10 of a liquid crystal display cut along the gate line 2 in FIG. 1. The spacer 10 consists of the spacer 10A which is formed at both ends of the gate line 2. (FIELD A), and the spacer 10B which is formed at a pixel region (FIELD B). A sealing member 24 seals between the transparent substrate 1A and the color filter substrate 1B. Furthermore, a conductive layer 21 is formed under the spacer 10A and 10B forming on the color filter substrate 1B side, which is contacted with the gate line 2. Hence, this embodiment covers the spacer 10A with the conductive layer 21 and electrically contracts the gate line 2 with the spacer 10A. If the gate line 2 breakdown in its wiring at a part in FIELD B, scanning signals would be supported via the conductive layer 21 at FIELD A. The seventh embodiment is useful for video signals of the drain line 3 as another embodiment.

[0079] [Embodiment 8]

[0080] FIG. 9 is a sectional view of the liquid crystal display according to an eight embodiment of the present invention. FIG. 9 shows a sectional view of the spacer 10 of a liquid crystal display cut along the counter voltage line 4 in FIG. 1. The spacer 10 consists of the spacer 10A which is formed at both ends of the gate line 2 (FIELD A), and the

spacer **10B** which is formed at a pixel region (FIELD B). A sealing member **24** seals between the transparent substrate **1A** and the color filter substrate **1B**. Furthermore, another counter voltage line **22** is formed under the spacer **10A** and **10B** forming on the color filter substrate **1B** side in this embodiment. A conductive layer **23** is disposed like **FIG. 9**, and connects between the counter voltage line **22** and an external terminal for a counter, voltage. Hence, this embodiment covers the spacer **10A** with the counter voltage line **22**, and electrically contacts the conductive layer **23** for a counter voltage.

[0081] [Embodiment 9]

[0082] It is clear that the spacer **10** should be formed on the color filter substrate side to prevent any deterioration in the characteristics of a thin film transistor. In the process of the spacer forming on the same substrate with a film transistor, a step increasing for a selective etching by a photo lithography method is added, wherein the etching solvent causes deterioration of the characteristics of the thin film transistor.

[0083] However, the spacer **10** should be formed on the transparent substrate **1A** side, when the alignment between the transparent substrate, **1A**, and the color filter substrate **1B** is more important than the deterioration in the characteristics of a thin film transistor.

[0084] [Embodiment 10]

[0085] **FIG. 10** is a sectional view of the spacer of a liquid crystal display according to a tenth embodiment of the present invention. The black matrix **BM** and the color filter **7** are formed under the color filter substrate **1B** side which is opposite to the liquid crystal, an overcoating film **8** made from a thermosetting resin film is formed thereunder. The spacer **10** made from a photocurable resin film, is formed under the over coating film **8**. To make the spacer **10** from photo curable resin film, a selective etching method is excluded from the process of the spacer **10**, manufacturing steps for the color filter substrate **1B** become simple and low cost. The embodiment **10** is useful for, the transparent substrate **1A** side having the spacer **10** as another embodiment.

[0086] [Embodiment 11]

[0087] **FIG. 11A** is a plan view of the pixel of a liquid crystal display according to an eleventh embodiment of the present invention. The spacers are uniformly arranged in a plurality of pixels like the same number of pixels have the same number of the spacers. To decrease alignment defects caused by spacer, a plurality of pixels have one spacer uniformly.

[0088] [Embodiment 12]

[0089] **FIG. 11B** is a plan view of the pixel of a liquid crystal display according to a twelfth embodiment of the present invention. A plurality of pixels are dotted with a plurality of spacers. Thus, the spacers are not uniformly arranged in a plurality of pixels like **FIG. 11A**. If alignment defects is uniform, an observer could see them easily.

[0090] [Embodiment 13]

[0091] **FIG. 12** is a sectional view of the spacer of a liquid crystal display according to a thirteenth embodiment of the present invention. The spacer **10** adheres to an orientation

layer formed on the transparent substrate **1A** by an adhesive **30**. The portion where the spacer **10** adheres to an orientation layer is touched between orientation layers. In general, the bonding force between the orientation layers having the same material is weak. Consequently, the adhesive **30** including the Si coupling material is needed between orientation layers.

[0092] **FIG. 13** is a flow chart of sectional views of a spacer illustrating the steps A to E of fabricating a color filter substrate **1B**.

[0093] [Step 1]

[0094] **FIG. 13A** shows a color filter substrate **1B** having the spacer **10** coating an orientation material. [Step 2]

[0095] First, the color filter substrate **1B** is moved near a vessel filled with adhesive material **30** like **FIG. 13B**. Then, the color filter substrate **1B** is positioned to apply the top of the spacer **10** with adhesive material.

[0096] [Step 3]

[0097] **FIG. 13C**. shows, the spacer **10** having the adhesive **30**.

[0098] [Step 4]

[0099] Second, the transparent substrate **1A** and-the color filter substrate **1B** are positioned facing each other shown in **FIG. 13D**.

[0100] [Step 5]

[0101] Finally, by applying heat to the pair of substrates, the adhesive **30** begins going solidify. (**FIG. 13E**).

[0102] **FIG. 14A** is a flow chart of sectional views of a spacer illustrating the steps A to E of fabricating a substrate **1B** according to another embodiment.

[0103] [Step 1]

[0104] **FIG. 14A** shows a color filter substrate **1B** having the spacer **10** formed on an orientation material.

[0105] [Step 2]

[0106] First, the color filter substrate **1B** is coated by the roller **31** soaking in the a vessel filled with adhesive material shown in **FIG. 14B**. As a result, the color filter substrate **1B** is formed with the tops of the spacer **10** covered with adhesive material.

[0107] [Step 3]

[0108] **FIG. 14C** shows the spacer **10** having the adhesive **30**.

[0109] [Step 4]

[0110] Second, the transparent substrate **1A**, and the color filter substrate **1B** are positioned facing each other shown in **FIG. 14D**.

[0111] [Step 5]

[0112] Finally, by applying heat to the pair of substrates, then the adhesive **30** begins to solidify. (**FIG. 14E**).

[0113] [Embodiment 14]

[0114] **FIG. 15** is a sectional view of the spacer of a liquid crystal display according to a fourteenth embodiment of the present. invention A concave **40** is formed on the transparent

substrate 1A as a counter tapered shape of a protective film 41 whereby adheres the spacer 10 with the protective film 41 via the concave 40. In the embodiment, the concave 40 is used instead of the adhesive 30.

[0115] FIG. 16 is a sectional view of the spacer of a liquid crystal display according to another invention of the fourteenth embodiment. A concave 42 is formed on the transparent substrate 1A with the gate line 2 or the drain line 3, wherein the concave 42 fits with the spacer 10. In this embodiment, the concave 42 is used instead of the adhesive 30 or the concave 40. Moreover, an area between the concaves 42 may be formed larger than the width of the spacer 10, as the sealing material 24 holds a gap of substrates correspond to the perpendicular direction. Therefore, the concaves 42 function as holding material for a gap of substrates correspond to the parallel direction.

[0116] [Embodiment 15]

[0117] FIG. 17 is a sectional view of the spacer of a liquid crystal display according to a fifteenth embodiment of the present invention. The spacer 10 is formed over the drain over line 3, the black matrix BM's pattern adjacent to the spacer 10 is formed wider than the other portions. In the embodiment 15, a rubbing direction of the orientation layer follows with the drain line 3. When an alignment defect is so large that the black matrix BM can not hide it perfectly, the embodiment 15 is useful thereby.

[0118] [Embodiment 16]

[0119] FIG. 18 is a plan view of the pixel of a liquid crystal display according to a sixteenth embodiment of the present invention. In this embodiment, a rubbing direction of an orientation layer has an angle θ , whereby alignment defects of the orientation layer caused by the spacer 10 also appear at an angle θ . As alignment defects of the orientation layer are related to the rubbing direction, therefore the right black matrix BM's pattern is wider than the left black matrix BM's pattern.

[0120] In other words, the black matrix BM's pattern covering the spacer 10 is needed as an outline whose shape is a circle with the center of the spacer, the radius of the circle depending on the occurrence of alignment defects.

[0121] FIG. 19 is a plan view of the pixel of a liquid crystal display according another invention of the sixteenth embodiment. It allows the black matrix BM's pattern covering the spacer 10 to form at one side.

[0122] [Embodiment 17]

[0123] FIG. 20 is a plan view of the pixel of a liquid crystal display according to a seventeenth embodiment of the present invention. The spacer 10 is formed over the gate line 2 or the drain line 3. A shield layer 50 is disposed adjacently both sides of the spacer 10. In this embodiment, the shield layer 50 is different layer from the black matrix BM, the gate line 2 and the drain line 3. The rubbing direction of the orientation layer is the direction along with the gate line 2 or the drain line 3. As alignment defects of the orientation layer are related to the rubbing direction, the shield layer 50 is arranged wider than the field occurring alignment defects. Moreover, it allows the shield layer 50 to form the same layer with the gate line 2 or the drain line 3, so as to be united with the gate line 2 or the drain line 3. When the shield layer 50 is different layer from the drain line

3, the shield layer 50 is made of the same material of the counter voltage line 4. When the shield layer 50 is different layer from the gate line 2, the shield layer 50 is made of the same material of the drain line 3. In another embodiment, it allows a rubbing direction of an orientation layer to have an angle θ , wherein the shield layer 50 is formed wider than the field occurring alignment defect.

[0124] [Embodiment 18]

[0125] FIG. 21 is a plan view of the pixel of a liquid crystal display according to an eighteenth embodiment of the present invention. The spacer 10 is formed over the gate line 2 or the drain line 3, and a shield layer 50 is disposed at the one side of the spacer 10. In more detail, the shield layer 50 is only formed on the side occurring alignment defect. In this way, an aperture ratio of a pixel can be improved more than embodiment 17.

[0126] [Embodiment 19]

[0127] FIG. 22 is a plan view of the pixel of a liquid crystal display according to a nineteenth embodiment of the present invention. The shield layer 50 is disposed adjacent both sides to the spacer 10. In this embodiment, the right side of the shield layer 50 is longer in the Y direction than the left side.

[0128] As alignment defects of the orientation layer are related to the rubbing direction, the shield layer 50 is longer in the right than in the left. In other words, the shield layer 50 is needed as an outline whose shape is a circle with the center of the spacer, the radius of the circle depending on the occurrence of alignment defect.

[0129] [Embodiment 21]

[0130] FIG. 23 is a plan view of the pixel of a liquid crystal display according to a twenty-first embodiment of the present invention. In the embodiment. 17-20, the shield layer 50 only functions as light shielding material, but this embodiment makes use of the shield layer 50 as any electrodes. In FIG. 23, the counter electrodes 4A which is disposed on the both sides of the drain line 2 have the function as the shield layer 50. If the width of the counter electrodes 4A is formed enough to hide alignment defects perfectly, the initial orientation angle would be designed not to depend on the presence of alignment defects. The reliability of the design improves by this embodiment.

[0131] [Embodiment 22]

[0132] FIG. 24A is a plan view of the pixel of a liquid crystal display according to a twenty-second embodiment of the present invention. FIG. 24B is a sectional view of the pixel of a liquid crystal display cut along the line b-b in FIG. 24A. A pair of the transparent substrate 1A and the color filter substrate 1B face each other, the sealing material 24 is formed between the substrates as a frame to keep a gap between the substrates thereby. The spacer 10 is arranged inside the sealing material 24 to keep a gap at the display region thereby. The spacer 10 is formed on on of a pair of the substrates, every spacer having the same square measurement where the spacer touches the other of the pair of substrates.

[0133] In addition, the number of spacers 10 which are disposed at the center of the display region is more than at any other portion of the display region. Thus, the density of

the spacers forming near the sealing material **24** is less than the density of the spacers forming at the center of the display region. By definition, the density of spacers is number of spacers per 1 cm² or 1 mm². So, the spacers which are disposed far from the sealing material **24** are required for the gap maintenance force in proportion to the size of the liquid crystal display panel. In general, the thickness of a substrate for LCD is 0.7 mm or less, the large size LCD substrate bends easily by virtue of its weight. In this embodiment, many spacers **10** keep a gap between substrates, preventing bending in large size LCD panels.

[0134] The spacer **10** is formed like the twenty-second embodiment by the selective etching of a photo-lithography method. Moreover, the density of the spacer from the center to the edge of the substrate should change smoothly, and not suddenly. If the density of the spacer changes suddenly, the gap between substrates would not be uniform. In this embodiment, it allows the density of the spacer to be different from the center portion in the direction and the Y direction. Hence, the density of the spacer at the center portion in the X direction is more than in the Y direction to prevent A substrate from bending efficiently.

[0135] [Embodiment. 23]

[0136] FIG. 25 is a plan view of the pixel of a liquid crystal display according to a twenty-third embodiment of the present invention. The spacers **10** are arranged uniformly, but the width of the spacers **10** disposed at the center of a display region is wider than other spacers **10** in other region. Hence, a square measure where the spacers touch the substrates is different between the center region and the edge region. The spacer **10** is formed like the twenty-second embodiment by a selective etching of a photo-lithography method. Moreover, it allows the material strength of spacers to be different between the center region and the edge region.

[0137] [Embodiment 24]

[0138] In a twenty-fourth embodiment, the spacer **10** is not only formed in the pixel whose color filter is green. Rather, the spacer **10** is formed selectively in the pixels whose color filters are red or blue. Because green is the highest light transmittance color among red, green and blue, an observer can recognize an improper image display in green pixel easily.

[0139] [Embodiment 25]

[0140] FIG. 26 is a plan view of the pixel of a liquid crystal display according to a twenty-fifth embodiment of the present invention. In this embodiment, a portion **20** functions as a shield material to hide alignment defects which are caused by the rubbing in the initial orientation direction. Moreover, in this embodiment, the portion **20** combines the shield material and the counter electrode **4A** which is arranged at the side of the pixel. Therefore, the material of the counter electrode **4A** is selected from the group consisting of aluminum, chromium, a metal alloy including aluminum or chromium as a main component, and so on.

[0141] [Embodiment 26]

[0142] FIG. 27 is a plan view of the pixel of a liquid crystal display according to a twenty-sixth embodiment of the present invention. In this embodiment, a portion **20** functions as a shield material to hide alignment defects

which are caused by the rubbing in the initial orientation direction. Moreover, in this embodiment, the portion **20** combines the shield material and the pixel electrode **5**. Therefore, the material of the pixel electrode **5** is selected from the group consisting of aluminum, chromium, a metal alloy including aluminum or chromium as a main component, and so on.

[0143] [Embodiment 27]

[0144] FIG. 28 is a plan view of the pixel of a liquid crystal display according to a twenty-seventh embodiment of the present invention. In this embodiment, a portion **20** functions as a shield material to hide alignment defects which are caused by the rubbing in the initial orientation direction. Moreover, in this embodiment, the portion **20** combine the shield material and the counter electrode **4A** which is arranged at the center of the pixel. Therefore, the material of the counter electrode **4A** is selected from the group of aluminum, chromium, a metal alloy including aluminum or chromium as a main component, and so on.

[0145] [Embodiment 28]

[0146] FIG. 29 is a plan view of the pixel of a liquid crystal display according to a twenty-eighth embodiment of the present invention. In this embodiment, a portion **20** functions as a shield material to hide alignment defects which are caused by the rubbing in the initial orientation direction. Moreover, in this embodiment, the portion **20** combines the shield material and the pixel electrode **5**. Therefore, the material of the pixel electrode **5** is selected from the group consisting of aluminum, chromium, a metal alloy including aluminum or chromium as a main component, and so on. Furthermore, the portion **20** of the pixel electrode **5** is wider than another portion for increasing shield area and improving the precision of the design of a TFT cell.

[0147] [Embodiment 29]

[0148] FIG. 30 is a plan view of the pixel of a liquid crystal display according to a twenty-ninth embodiment of the present invention. In this embodiment, a portion **20** functions as a shield material to hide alignment defects which are caused by the rubbing in the initial orientation direction. Moreover, in this embodiment, the portion **20** combines the shield material and the counter electrode **4A** which is arranged at the center of a pixel. Therefore, the material of the counter electrode **4A** is selected from the group of consisting of aluminum, chromium, a metal alloy including aluminum or chromium as a main component, and so on. Furthermore, the portion **20** of the counter electrode **4A** is wider than another portion for increasing shield area and improving the precision of the design of a TFT cell. In this way, this invention is to provide a multi-domain system IPS (In-Plane Switching mode) liquid crystal display which has high contrast and does not have an improper image display. Thus, this invention achieves both advantages of preventing advantages the inversion of luminance when the view point of the observer is at an angle with respect to the slanting direction of the liquid crystal display panel, and providing remarkably high contrast (350:1) and high aperture ratio for a color active matrix liquid crystal display.

What is claimed is:

1. A liquid crystal display device comprising:
 - a pair of substrates;
 - a liquid crystal layer interposed between said pair of substrates;
 - a plurality of first lines and second lines formed on one of said pair of substrates, each of said first lines is wider than each of said second lines; and
 - at least a pixel with a spacer being selectively formed on each of the first lines,
 - wherein said spacer is wider than said second lines.
2. A liquid crystal display device according to claim 1, wherein a black matrix is formed over said each first line, and said black matrix is wider than said each first line.
3. A liquid crystal display device according to claim 1, wherein said spacer is narrower than said first line.
4. A liquid crystal display device according to claim 2, wherein said spacer is narrower than said first line.
5. A liquid crystal display device according to claim 1, wherein said first line is a gate line and said second line is a drain line.
6. A liquid crystal display device according to claim 4, wherein said first line is a gate line and said second line is a drain line.
7. A liquid crystal display device according to claim 1, wherein said spacer is arranged between neighboring second lines.
8. A liquid crystal display device according to claim 4, wherein said spacer is arranged between neighboring second lines.
9. A liquid crystal display device comprising:
 - a pair of substrates;
 - a liquid crystal layer interposed between said pair of substrates;
 - a plurality of gate lines and drain lines formed on one of said pair of substrates, each of said gate lines is wider than each of said drain lines; and
 - a plurality of pixels formed by adjoining gate lines and drain lines, each of the pixels is formed with at least one spacer, wherein
 - said spacer is arranged selectively over one of the gate lines, and said spacer is wider than said drain lines.
10. A liquid crystal display device according to claim 9, wherein said spacer is narrower than said gate lines.
11. A liquid crystal display device according to claim 10, wherein a black matrix is formed over said gate lines, and said black matrix is wider than said gate lines.
12. A liquid crystal display device according to claim 11, wherein said spacer is arranged between neighboring drain lines.
13. A liquid crystal display device comprising:
 - a pair of substrates;
 - a liquid crystal layer interposed between said pair of substrates;
 - a plurality of first lines and second lines formed on one of said pair of substrates, each of said first lines is wider than each of said second lines; and
 - at least a pixel with a spacer being selectively formed on each of said first lines or each of said second lines,
 - wherein said spacer is wider than said second lines.
14. A liquid crystal display device according to claim 13, wherein said spacer is arranged over each of said second lines.
15. A liquid crystal display device according to claim 14, wherein said spacer is narrower than said first lines.
16. A liquid crystal display device according to claim 14, wherein at least one electrode is arranged next to each of the second lines, and said spacer extends over said electrode.
17. A liquid crystal display device according to claim 15, wherein at least one electrode is arranged next to each of the second lines, and said spacer extends over said electrode.
18. A liquid crystal display device according to claim 14, wherein a black matrix is formed over each of said second lines, and said black matrix is wider than each of said second lines.
19. A liquid crystal display device according to claim 15, wherein a black matrix is formed over each of said second lines, and said black matrix is wider than each of said second lines.
20. A liquid crystal display device according to claim 14, wherein a black matrix is formed over each of said second lines,
 - at least an electrode is arranged next to said second lines, and
 - at least one edge of said black matrix overlaps said electrode.
21. A liquid crystal display device according to claim 15, wherein a black matrix is formed over each of said second lines,
 - at least an electrode is arranged next to said second lines, and
 - at least one edge of said black matrix overlaps said electrode.

* * * * *

专利名称(译)	液晶显示器		
公开(公告)号	US20020113935A1	公开(公告)日	2002-08-22
申请号	US10/106954	申请日	2002-03-28
[标]申请(专利权)人(译)	株式会社日立制作所		
申请(专利权)人(译)	HITACHI, LTD.		
当前申请(专利权)人(译)	松下液晶显示CO., LTD.		
[标]发明人	YANAGAWA KAZUHIKO ASHIZAWA KEIICHIRO ISHII MASAHIRO HIKIBA MASAYUKI		
发明人	YANAGAWA, KAZUHIKO ASHIZAWA, KEIICHIRO ISHII, MASAHIRO HIKIBA, MASAYUKI		
IPC分类号	G02F1/1339 G02F1/1335 G02F1/1337 G02F1/1343 G02F1/136 G02F1/1368		
CPC分类号	G02F1/1303 G02F1/133512 G02F1/1339 G02F1/13394 G02F1/134363 G02F2201/40 G02F2202/28		
优先权	1999029053 1999-02-05 JP		
其他公开文献	US6667790		
外部链接	Espacenet USPTO		

摘要(译)

本发明提供一种多域系统IPS (面内切换模式) 液晶显示器, 其具有高对比度并且没有不适当的图像显示。根据本发明的一个方面, 提供了一种黑矩阵, 形成在一对基板之间并形成在多条漏极线和栅极线上, 多个空间布置在黑矩阵下, 其中多个液晶分子在“关”状态下排列到黑矩阵的边界方向, 并且使第一组和第二组处于“开”状态, 其中第二组的切换角度与第一组的切换角度相反。

