

(10) **Patent No.:** US 6,825,906 B2  
(45) **Date of Patent:** Nov. 30, 2004

- |           |      |         |                     |         |
|-----------|------|---------|---------------------|---------|
| 5,309,264 | A    | 5/1994  | Lien et al. ....    | 359/87  |
| 5,510,916 | A    | 4/1996  | Takahashi .....     | 359/67  |
| 5,699,139 | A    | 12/1997 | Aastuen et al. .... | 349/156 |
| 5,708,485 | A *  | 1/1998  | Sato et al. ....    | 349/2   |
| 5,882,238 | A *  | 3/1999  | Kim et al. ....     | 445/24  |
| 5,907,380 | A *  | 5/1999  | Lien .....          | 349/141 |
| 5,936,691 | A *  | 8/1999  | Kumar et al. ....   | 349/124 |
| 6,342,939 | B1 * | 1/2002  | Hirata et al. ....  | 349/143 |
| 6,356,335 | B1 * | 3/2002  | Kim et al. ....     | 349/156 |
| 6,473,142 | B2 * | 10/2002 | Kim et al. ....     | 349/84  |

- FOREIGN PATENT DOCUMENTS

- |    |             |         |                  |
|----|-------------|---------|------------------|
| EP | 0 752 611   | 1/1997  |                  |
| EP | 0 854 377   | 7/1998  |                  |
| EP | 884626 A2 * | 12/1998 | ..... G02F/1/139 |
| GB | 2 296 810   | 7/1996  |                  |
| GB | 2 321 718   | 8/1998  |                  |

- ## OTHER PUBLICATIONS

- Konovalov., "44.2L: Multi-Domain Vertically Aligned Mode", (May) 1998 SID.\*

- \* cited by examiner

- Primary Examiner*—Robert H. Kim

*Assistant Examiner*—Andrew Schechter

(74) *Attorney, Agent, or Firm*—McKenna Long & Aldrige  
LLP

- (57) **ABSTRACT**

- A multi-domain liquid crystal display device comprises first, and second substrates facing each other and a liquid crystal layer between the first and second substrates. A plurality of gate bus lines are arranged in a first direction on the first substrate and a plurality of data bus lines are arranged in a second direction on the first substrate to define a pixel region. A pixel electrode electrically is charged through the data bus line in the pixel region, a color filter layer is formed on the second substrate, and a common electrode is formed on the color filter layer. Dielectric frames are formed in the pixel region, and an alignment layer on at least one substrate between the first and second substrates.

- 13 Claims, 35 Drawing Sheets**

- (52) **U.S. Cl.** ..... **349/129**; 349/155; 349/156;  
349/160; 349/191

- (58) **Field of Search** ..... 349/129, 155,  
349/156, 160, 191

- (56)
- References Cited**

|           |     |         |                      |           |
|-----------|-----|---------|----------------------|-----------|
| 4,581,608 | A   | 4/1986  | Aftergut et al. .... | 340/704   |
| 4,867,537 | A * | 9/1989  | Aoki et al. ....     | 349/111   |
| 4,937,566 | A   | 6/1990  | Clerc .....          | 340/784   |
| 4,978,203 | A   | 12/1990 | Yamazaki et al. .... | 350/339 R |
| 5,299,041 | A * | 3/1994  | Morin et al. ....    | 349/42    |

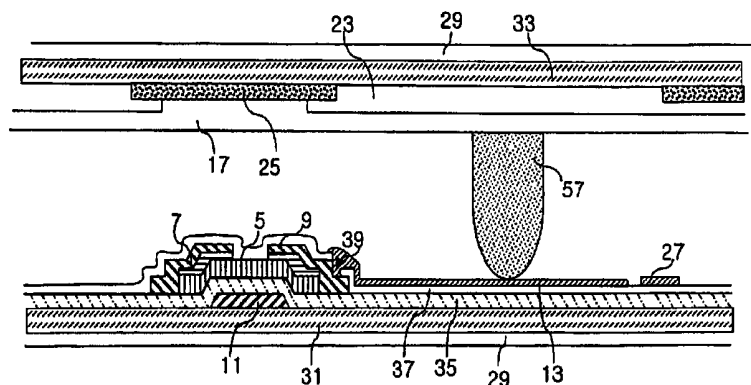


FIG. 1  
PRIOR ART

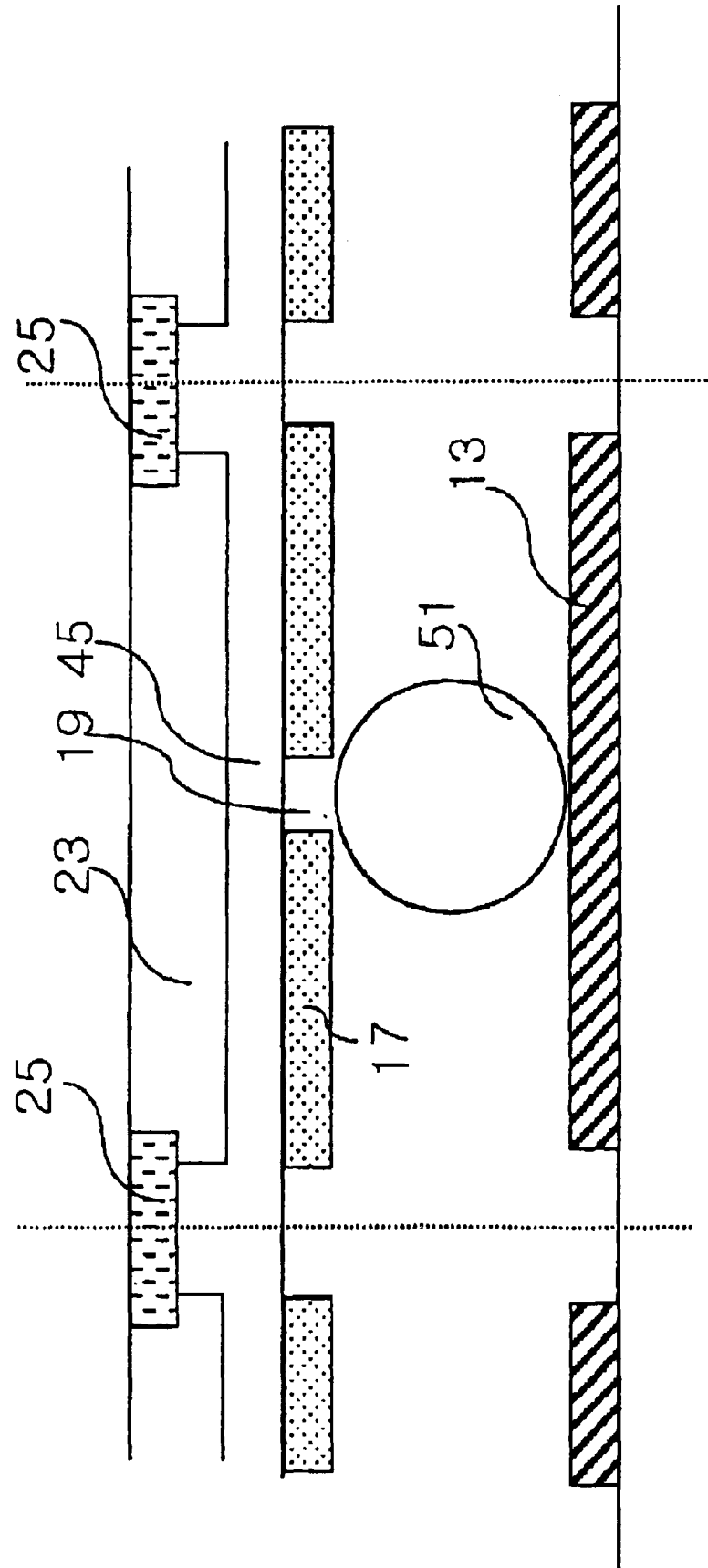
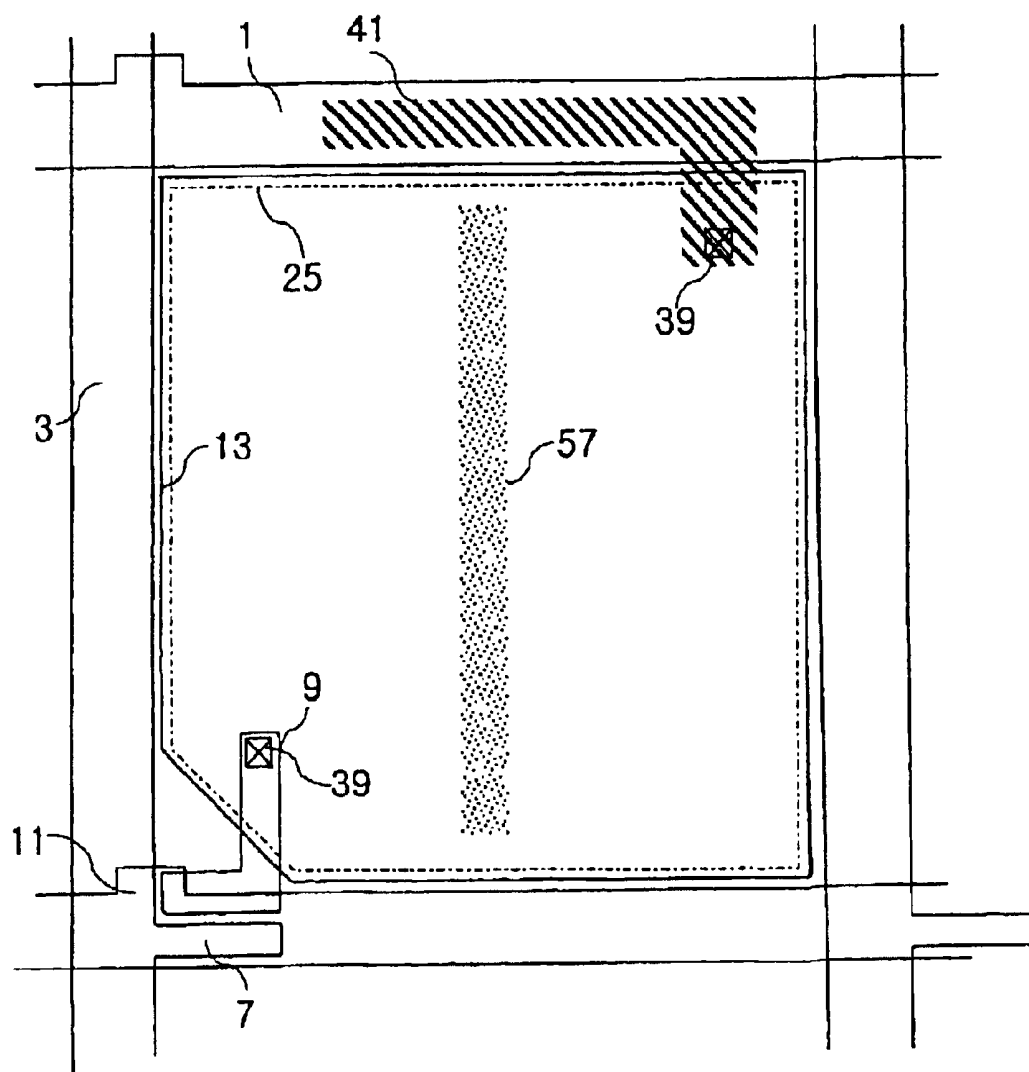


FIG. 2A



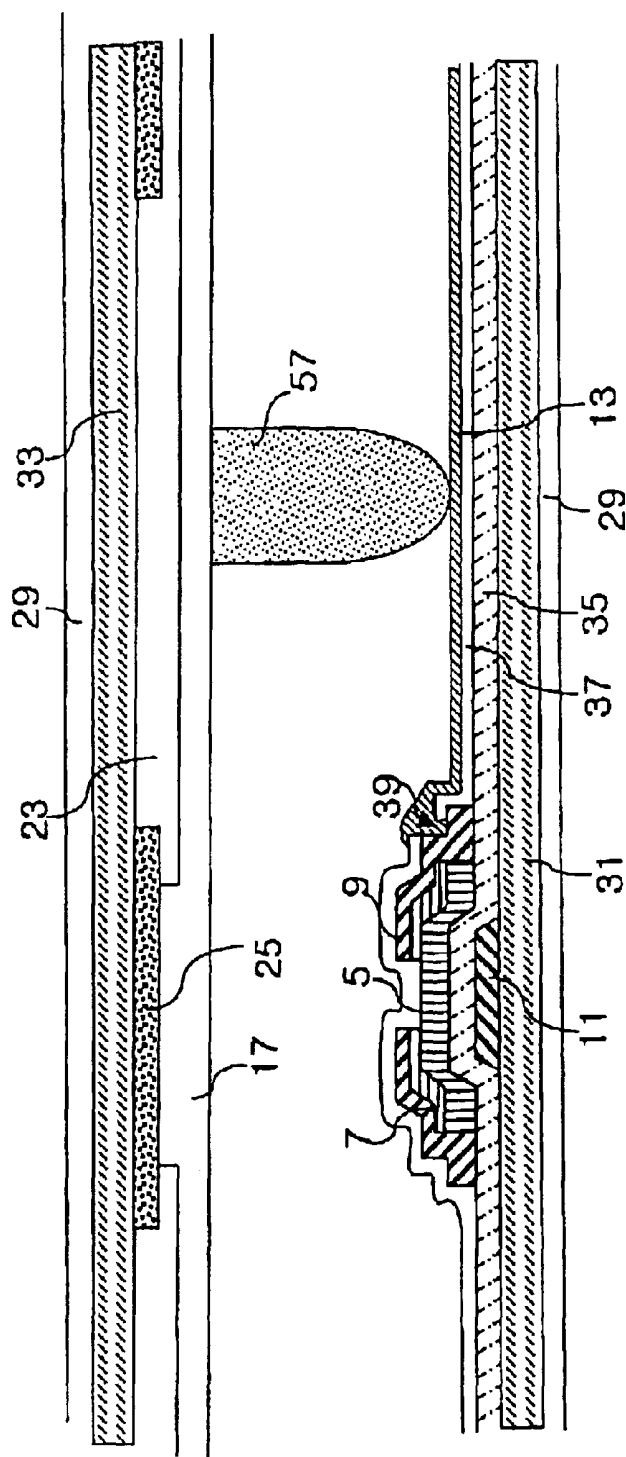
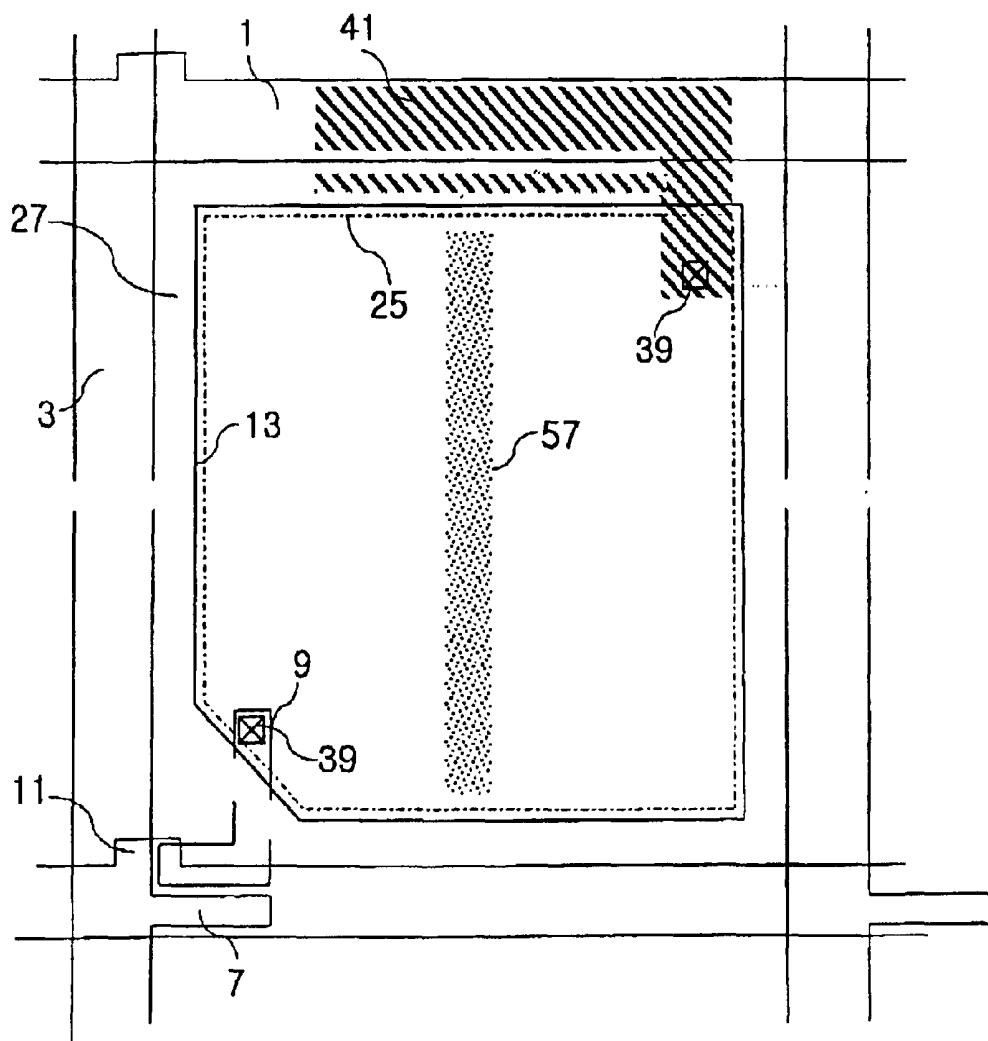


FIG. 28

FIG. 3A



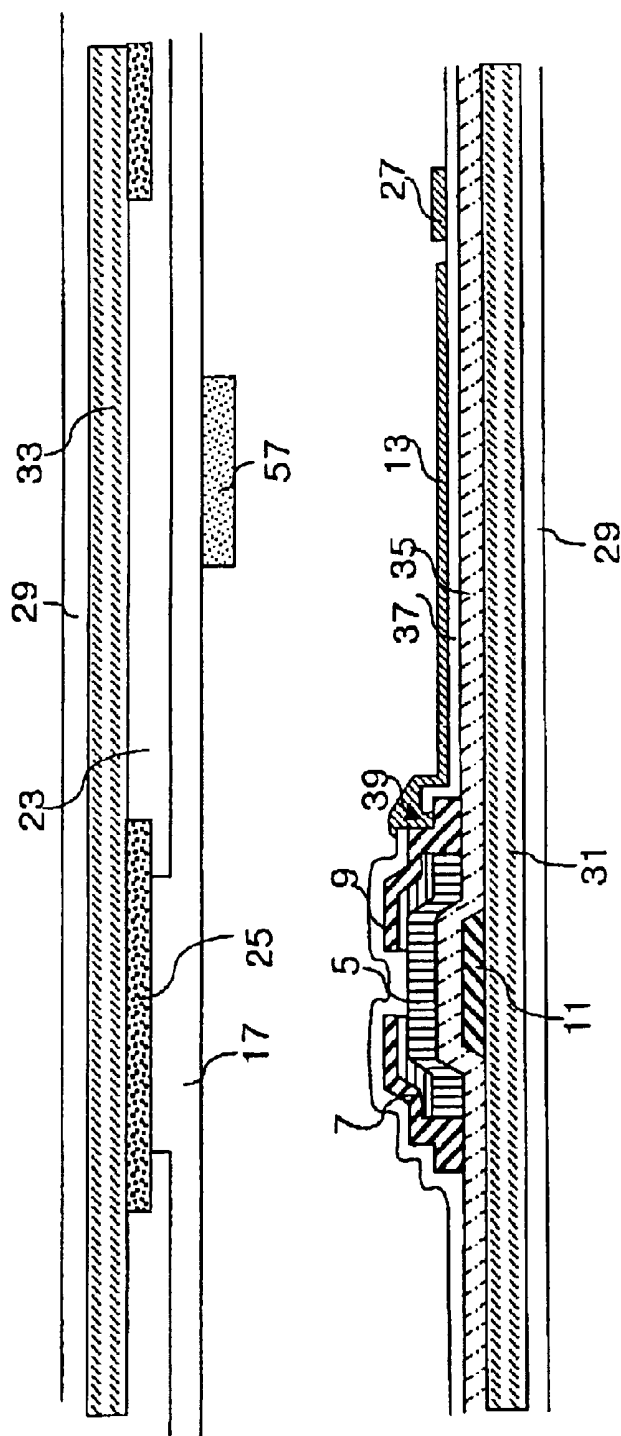


FIG. 3B

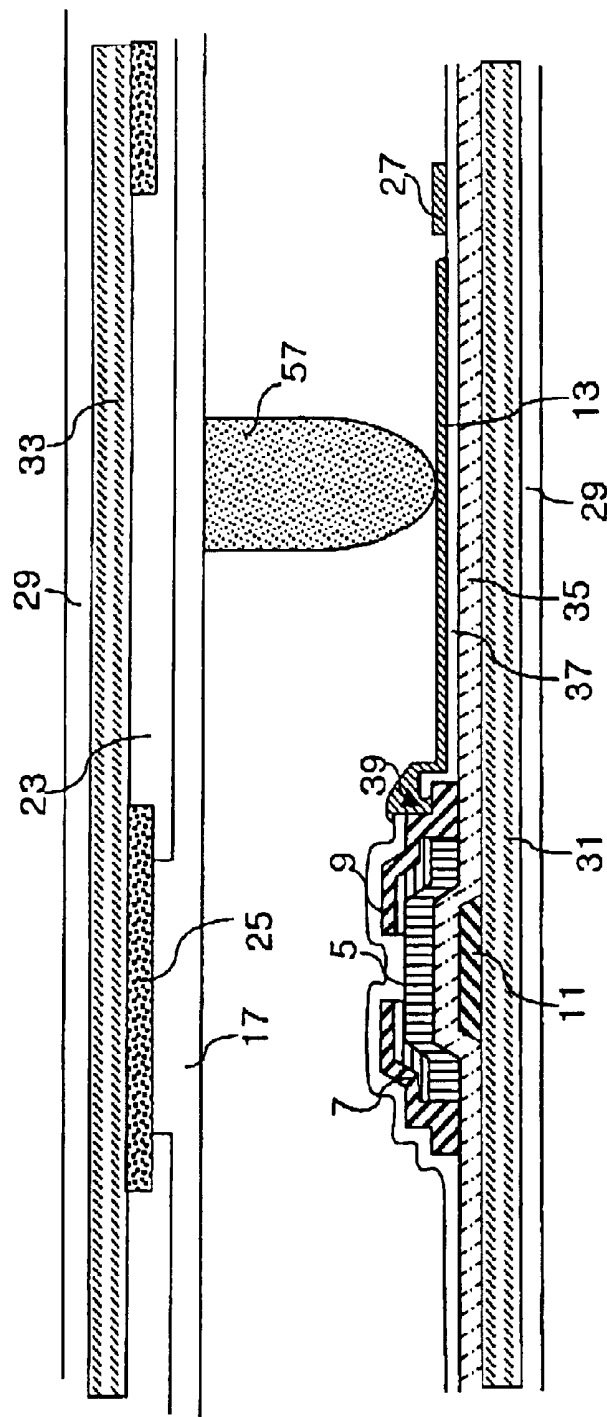
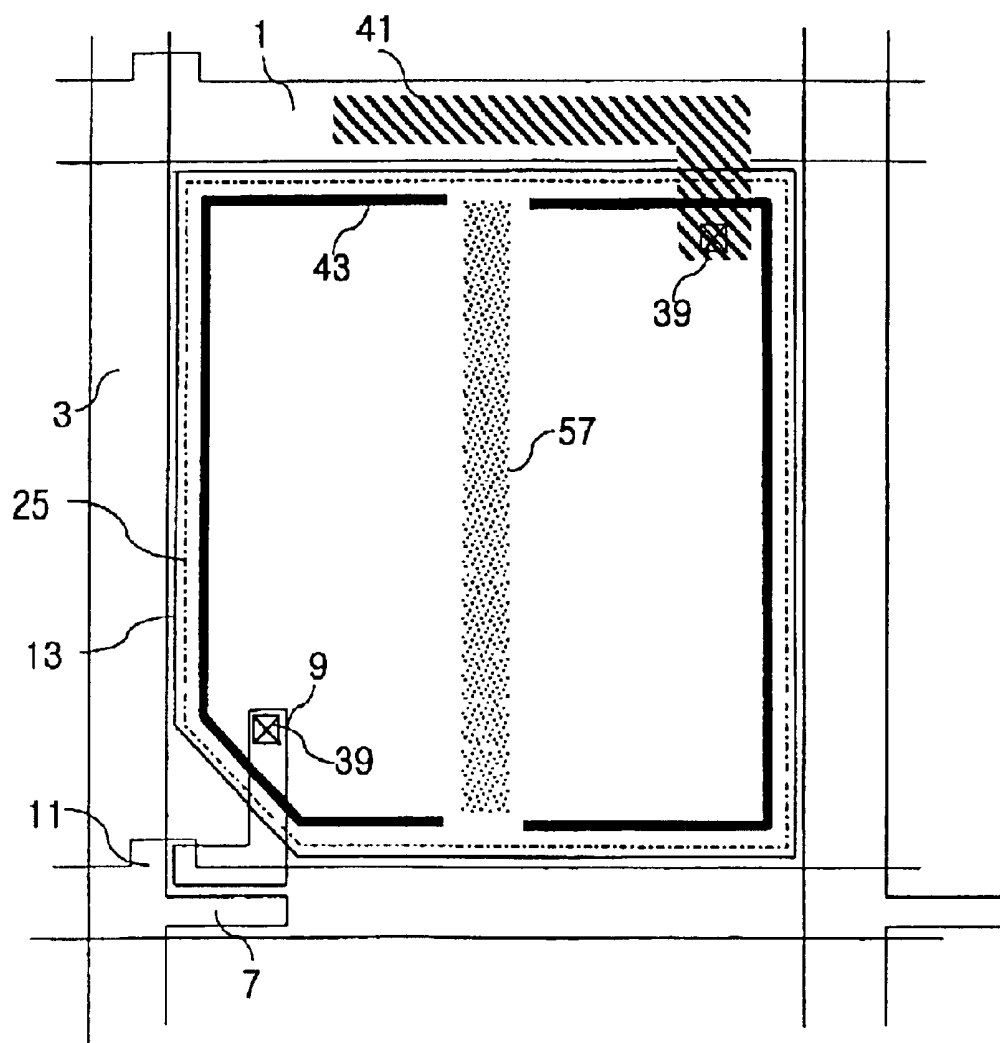


FIG. 3C

FIG. 4A



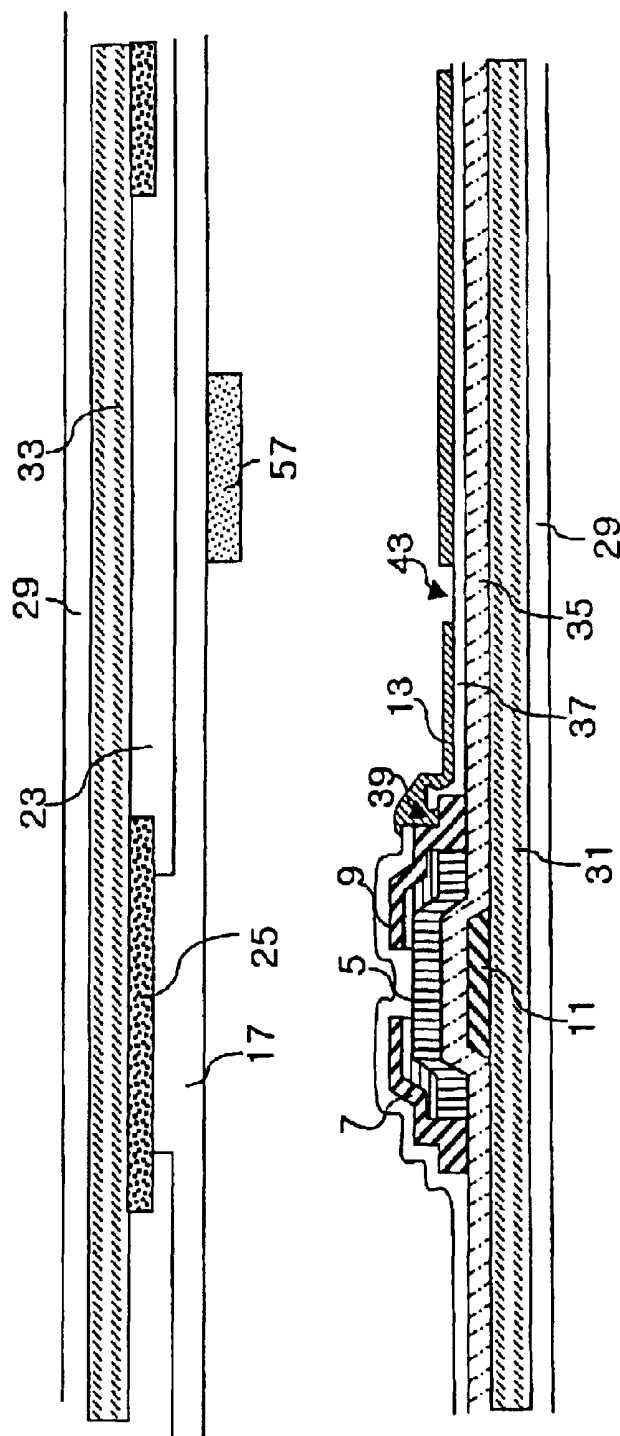


FIG. 4B

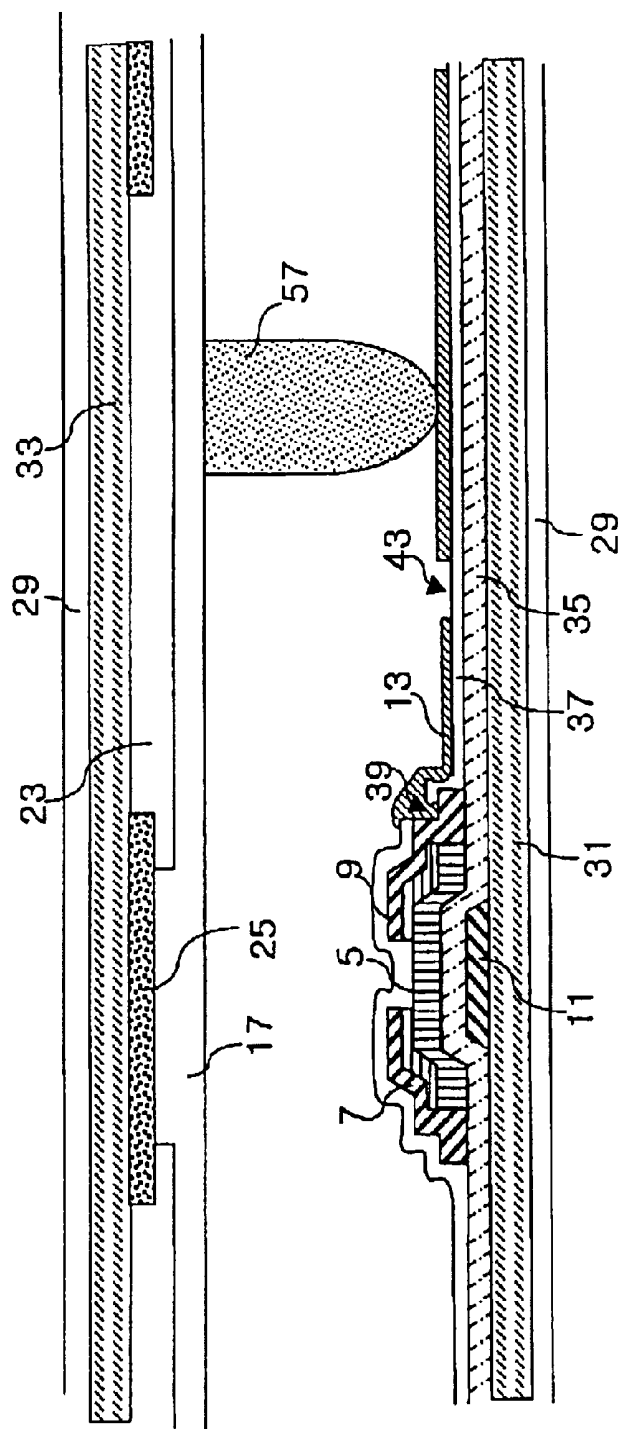


FIG. 4C



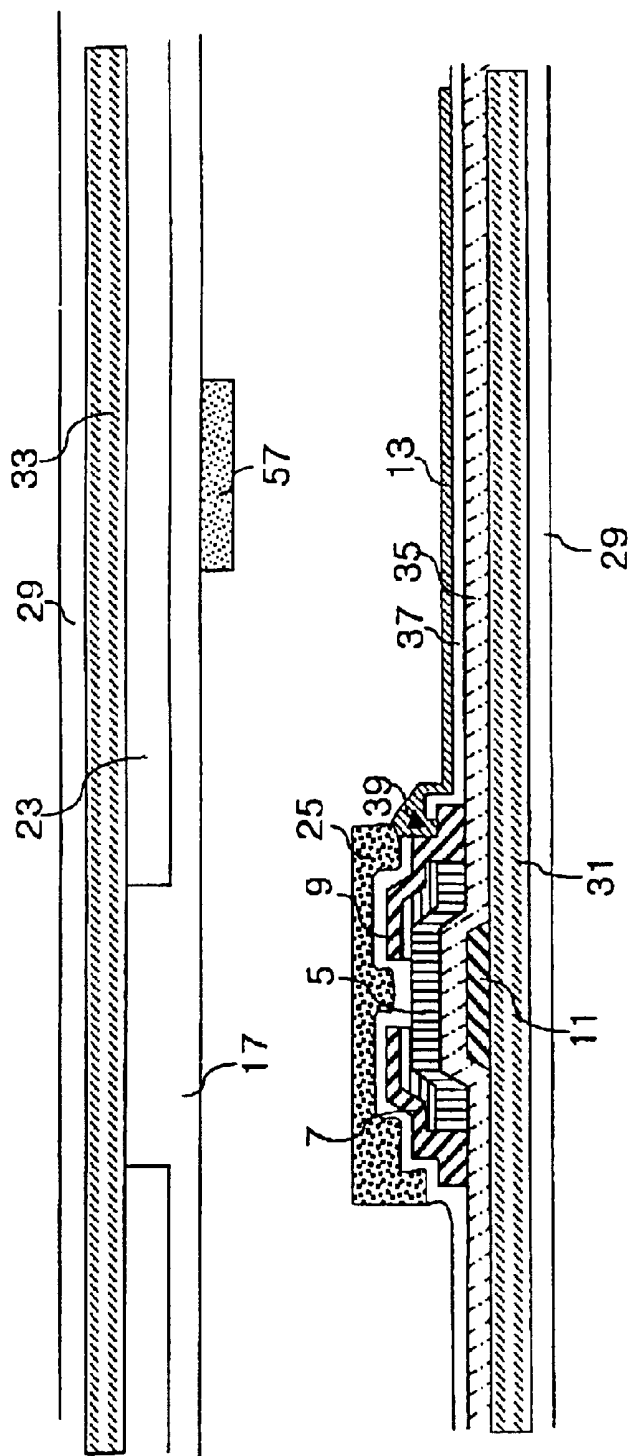


FIG. 5B

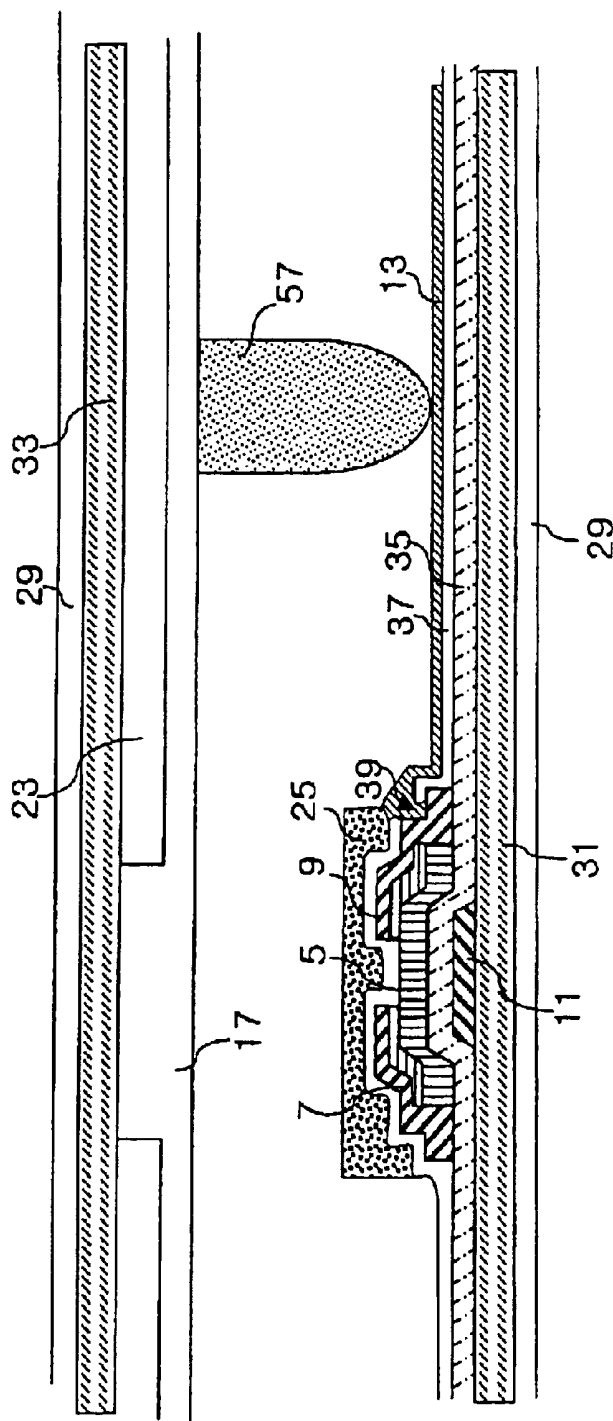


FIG. 5C

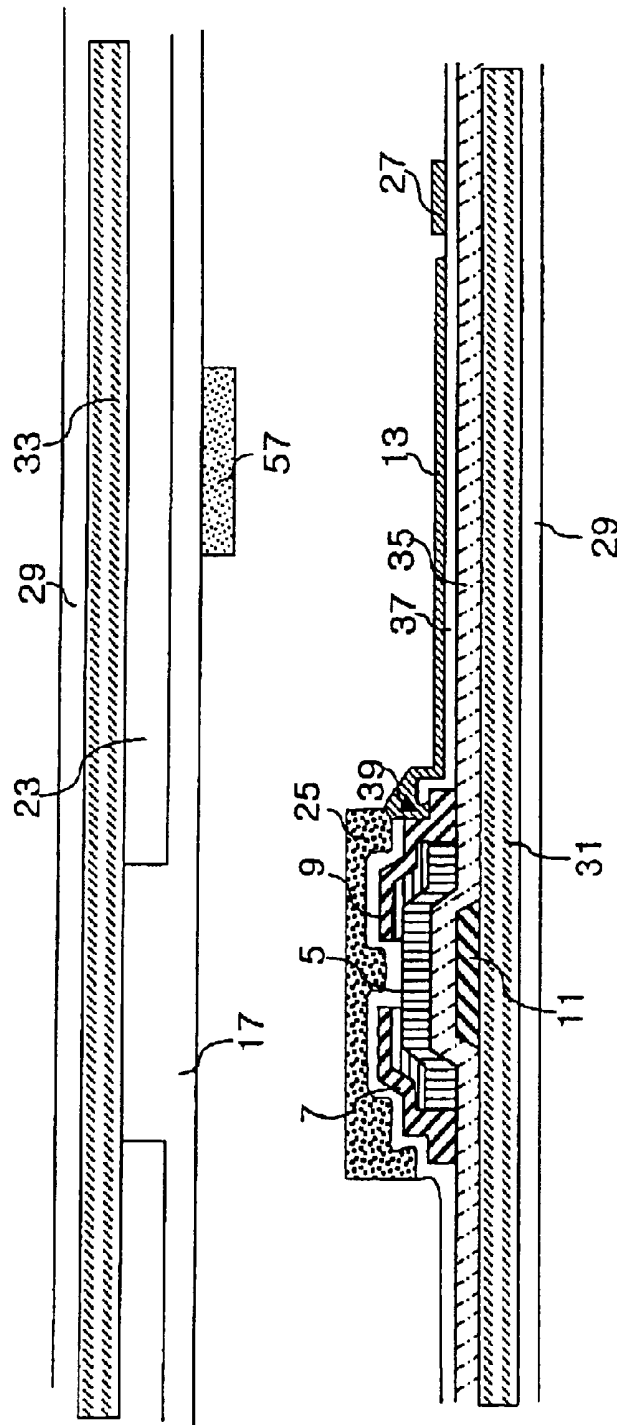


FIG. 5D

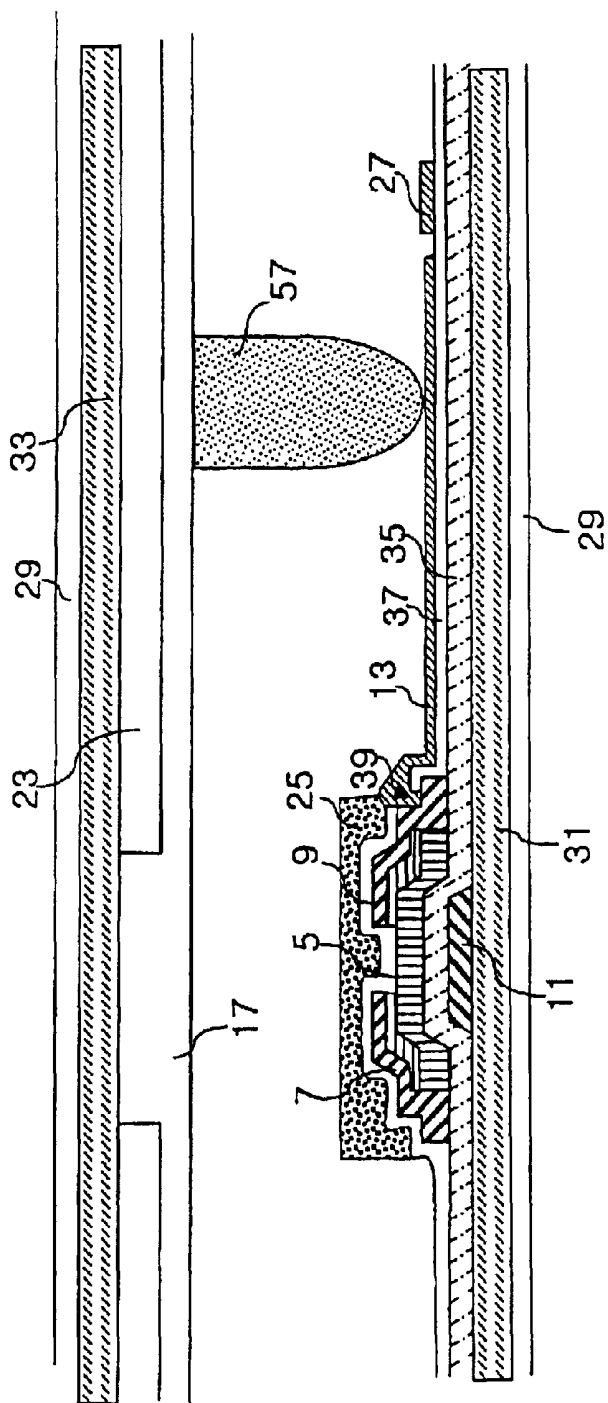


FIG. 5E

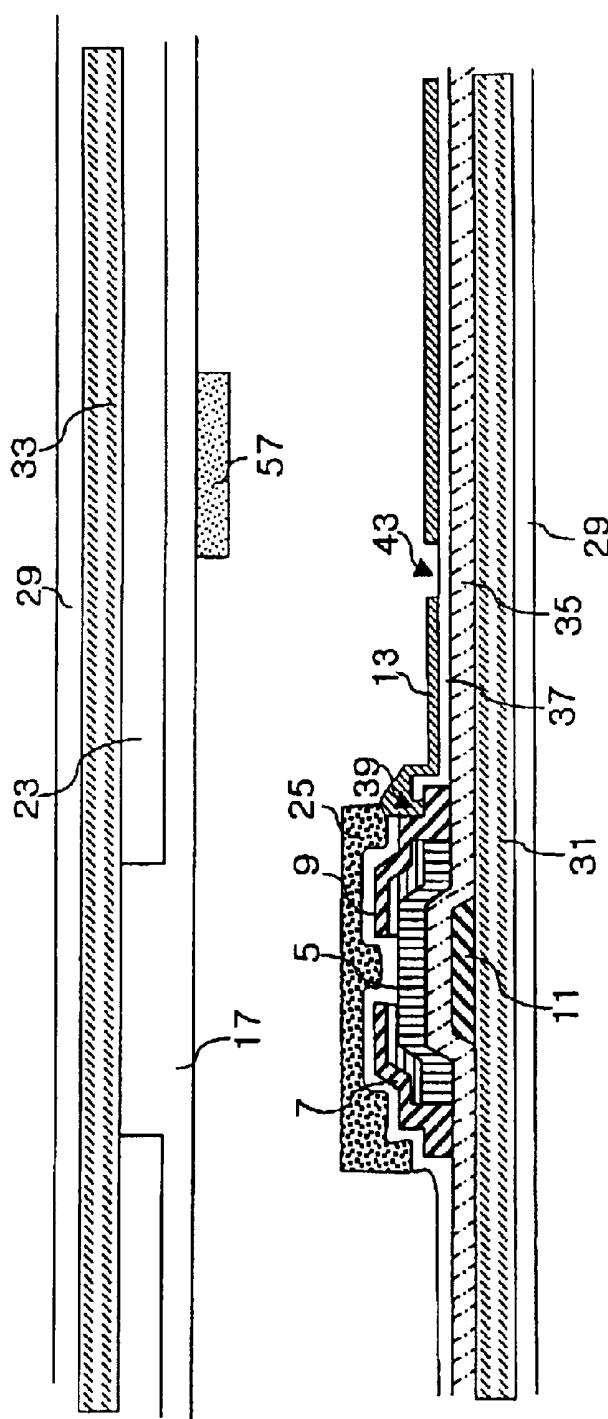


FIG. 5F

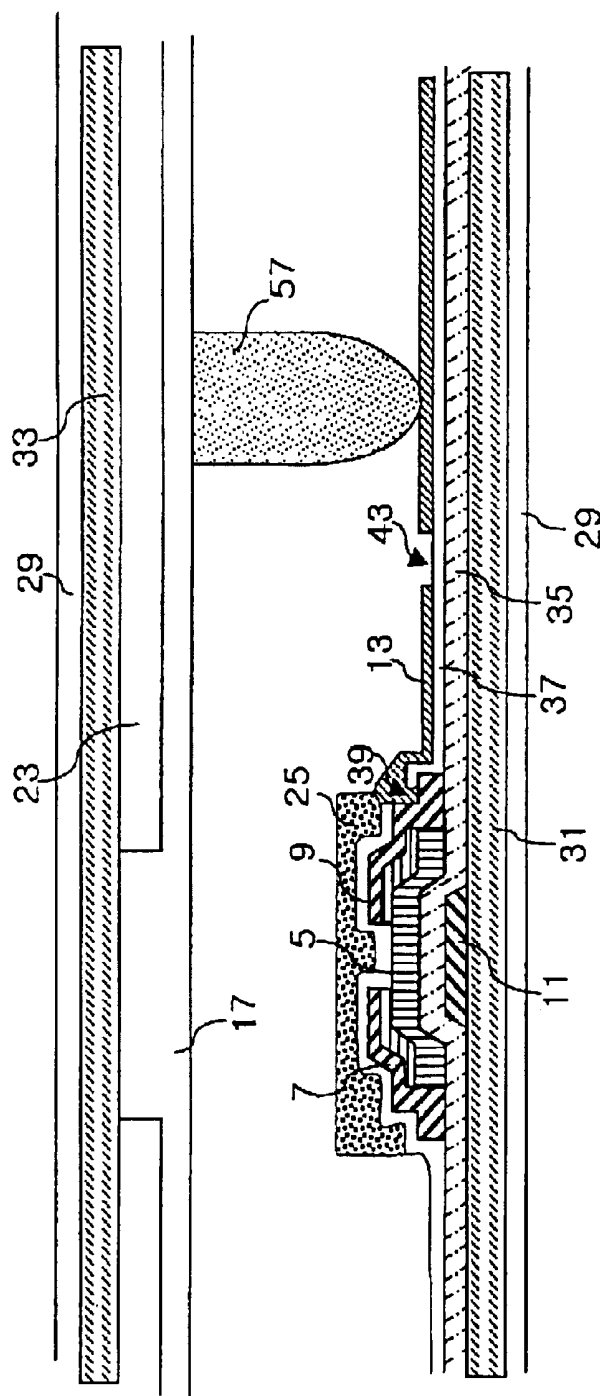


FIG. 5G

FIG. 6C

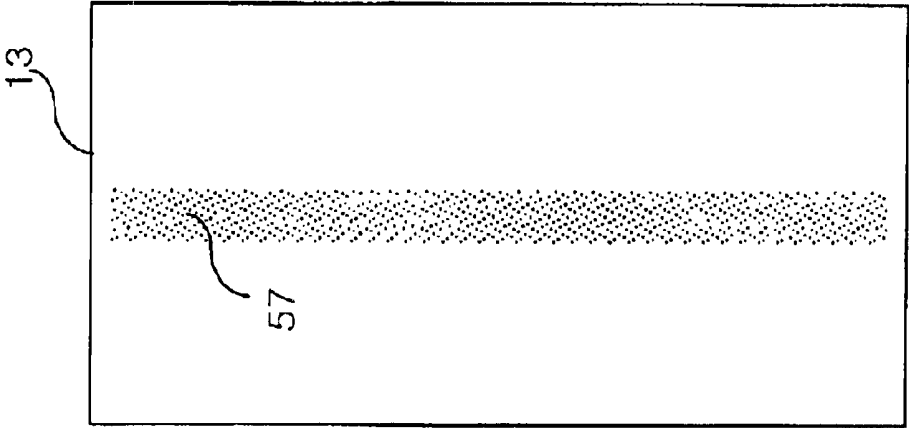


FIG. 6B

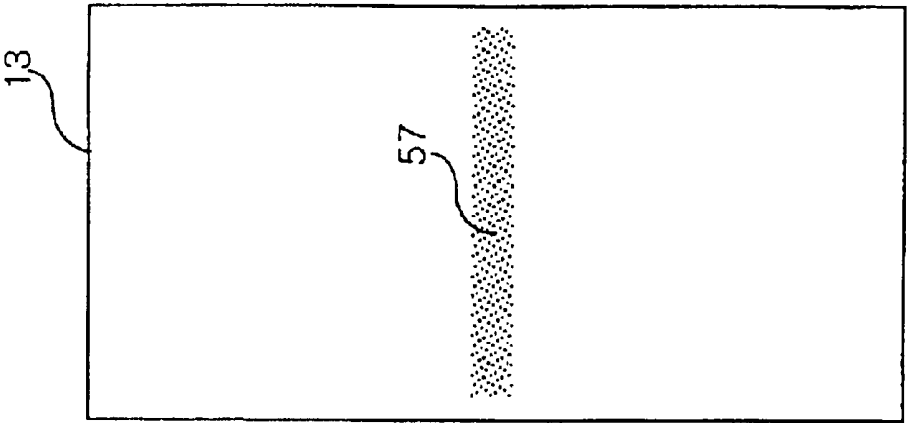
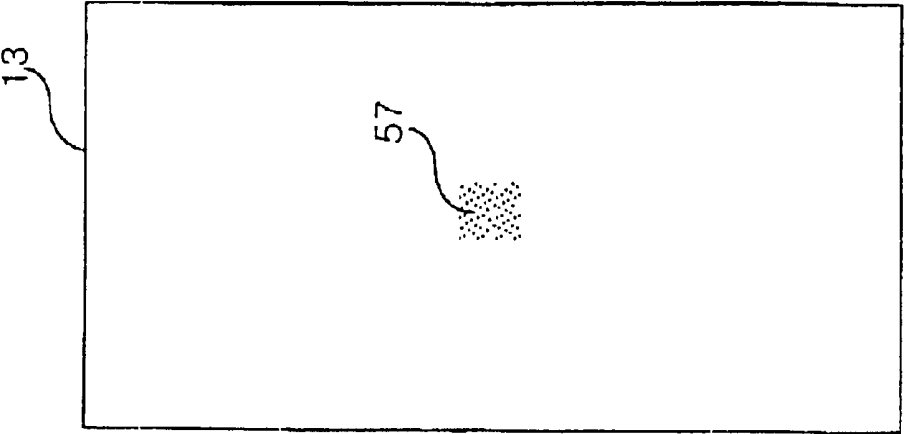


FIG. 6A



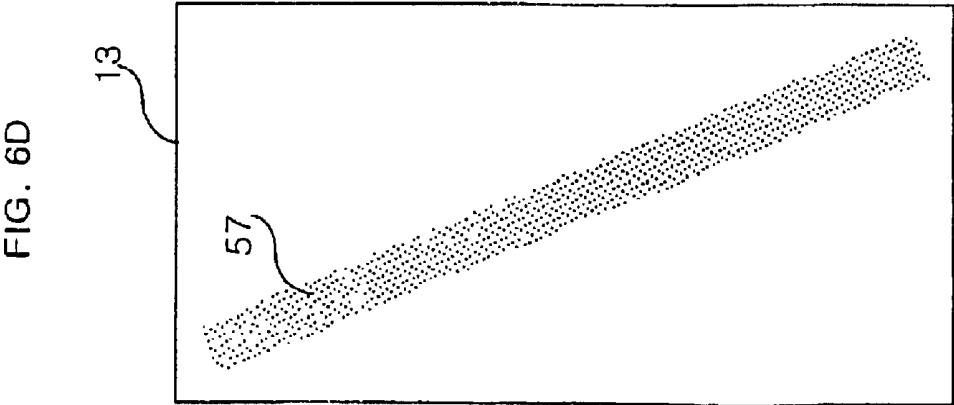
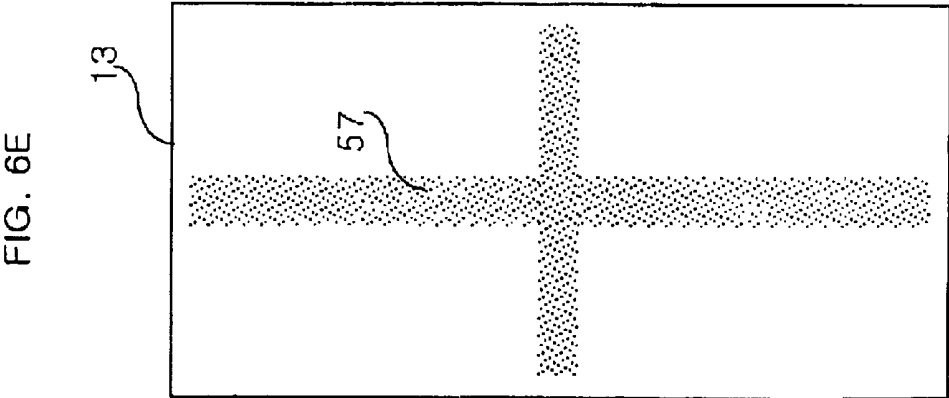


FIG. 6G

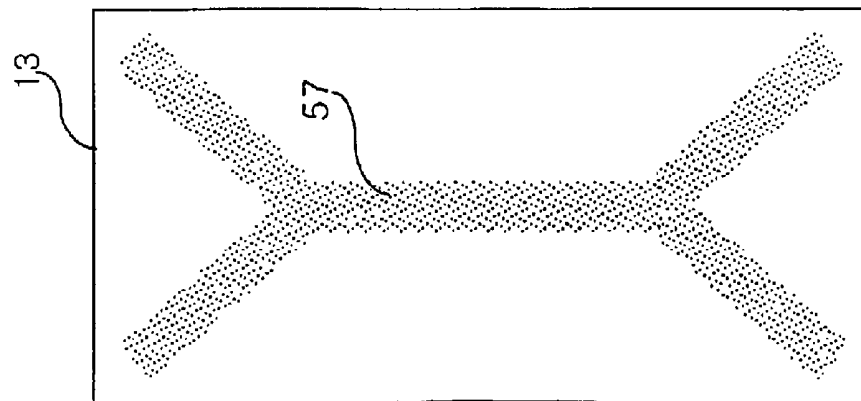


FIG. 6F

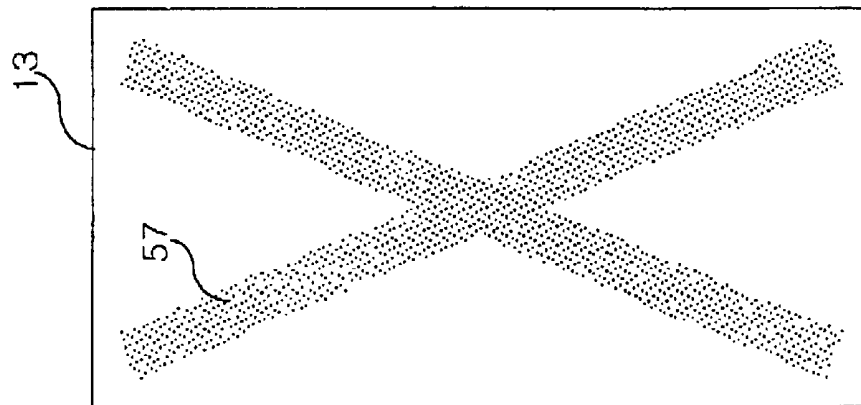


FIG. 7B

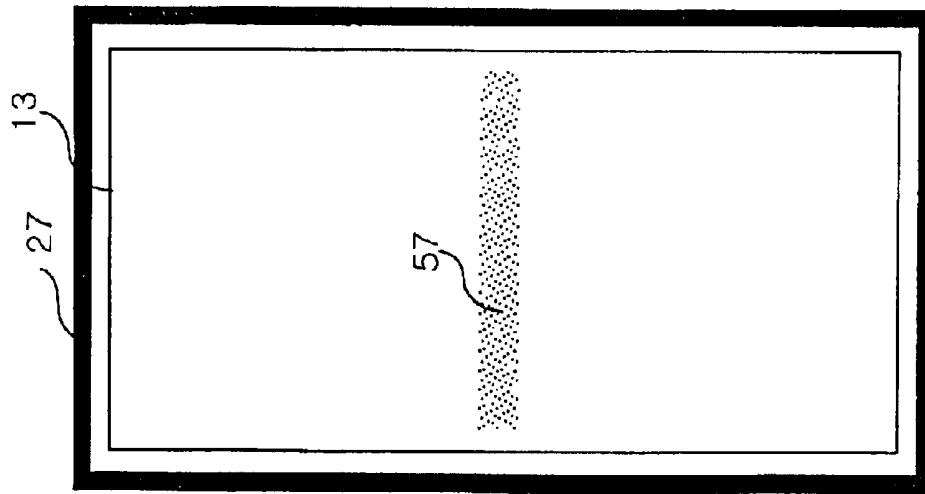


FIG. 7A

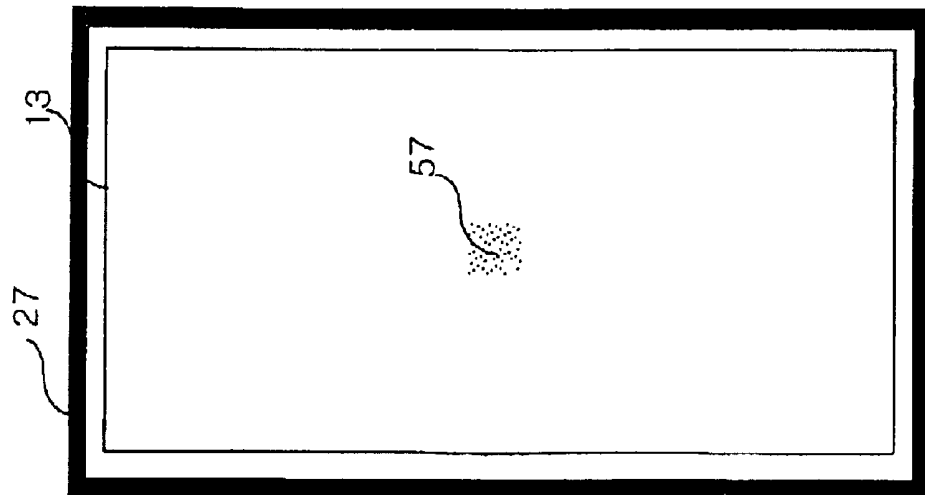


FIG. 7D

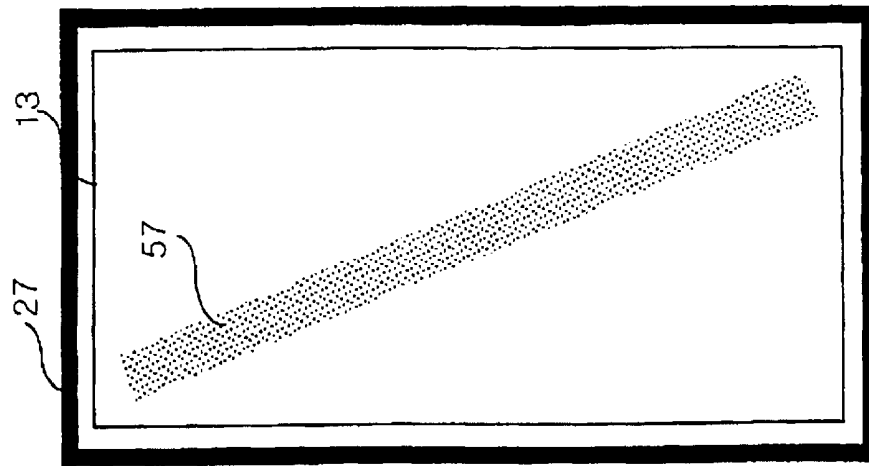


FIG. 7C

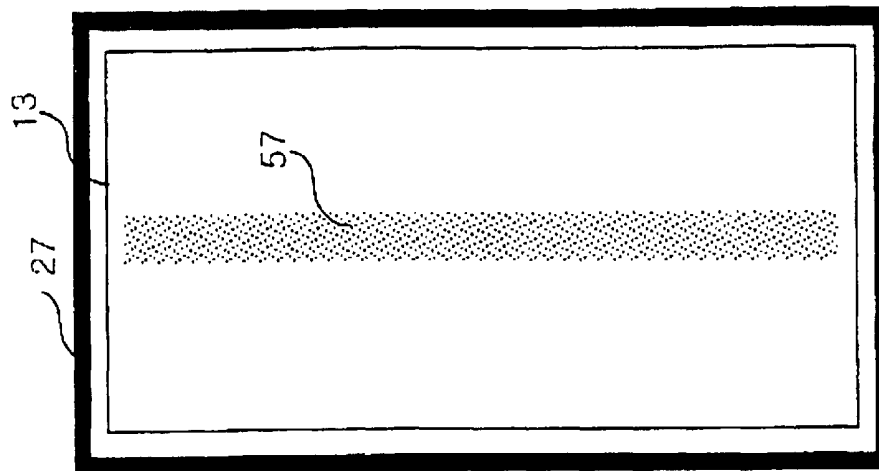


FIG. 7G

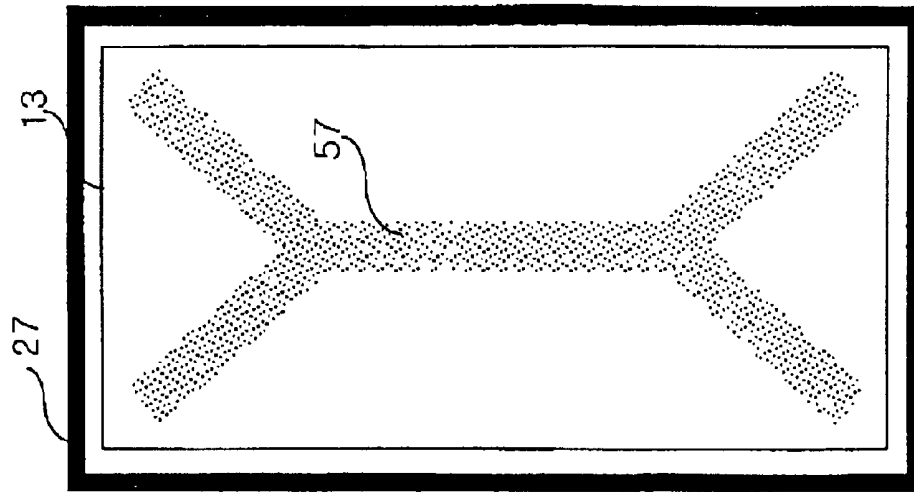


FIG. 7F

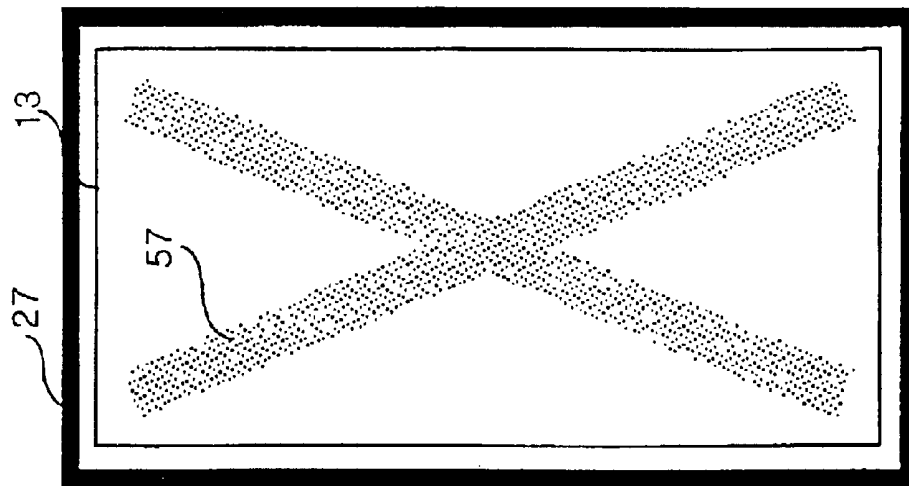


FIG. 7E

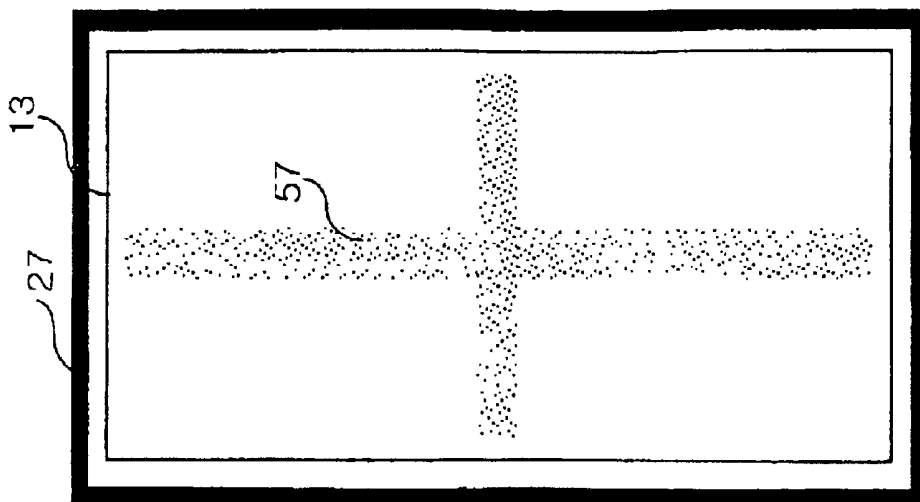


FIG. 8A

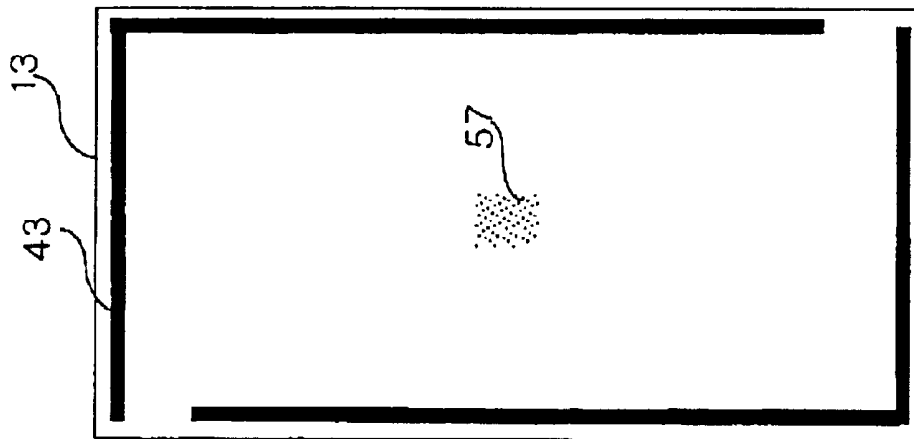


FIG. 8B

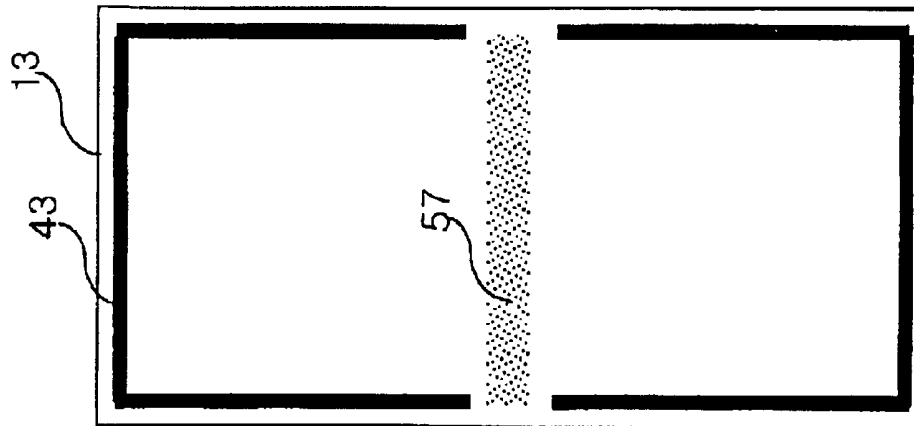


FIG. 8C

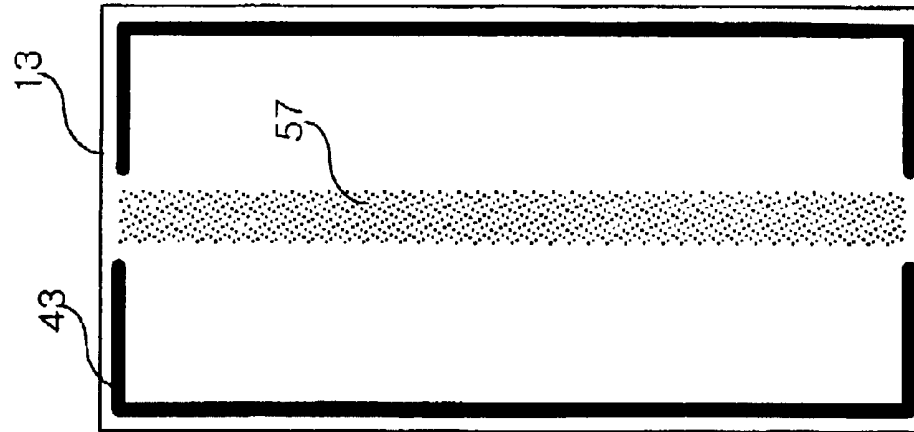


FIG. 8F

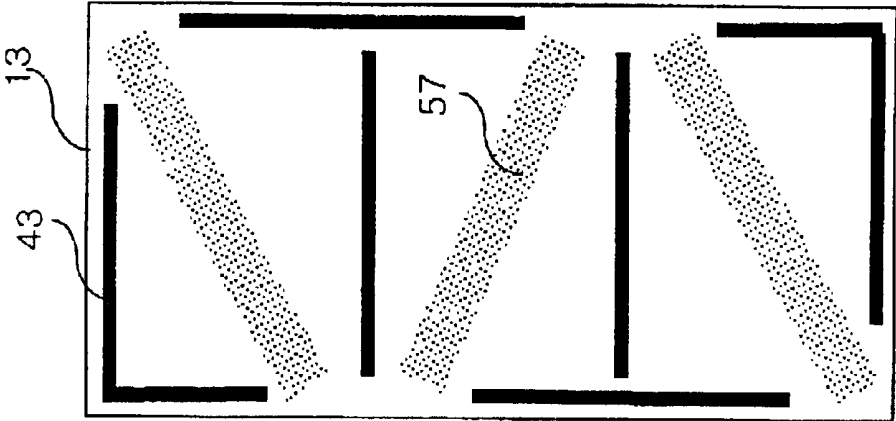


FIG. 8E

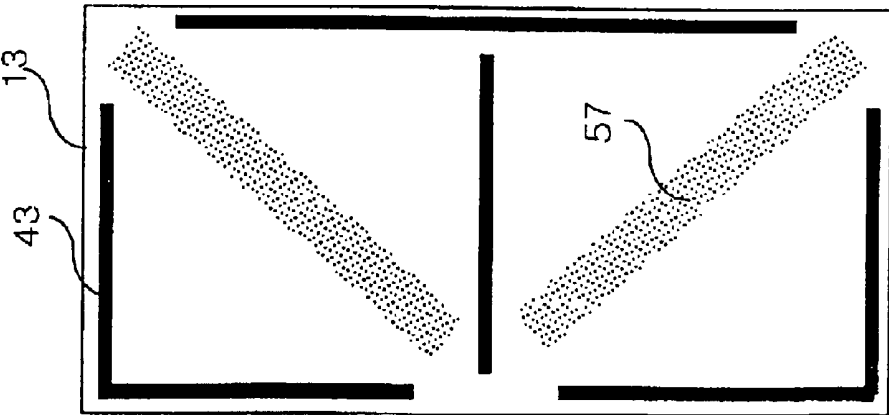


FIG. 8D

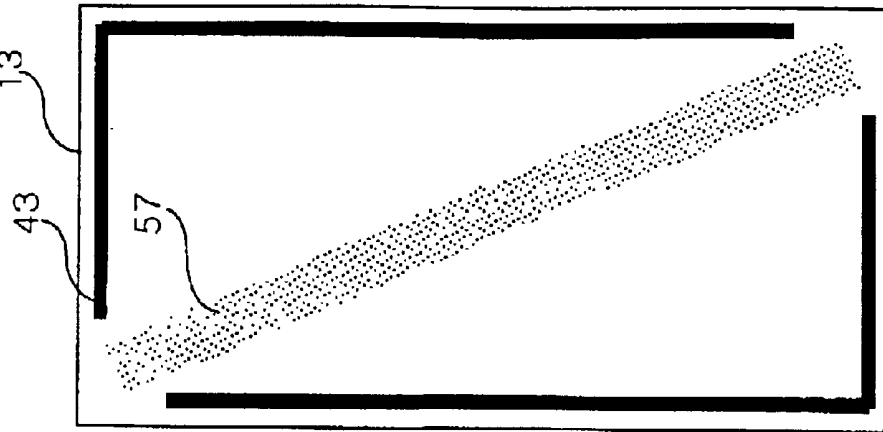


FIG. 8G

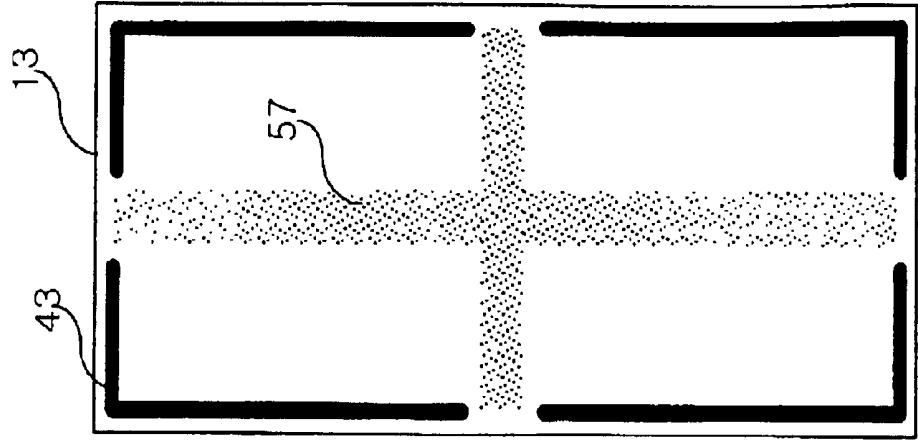


FIG. 8H

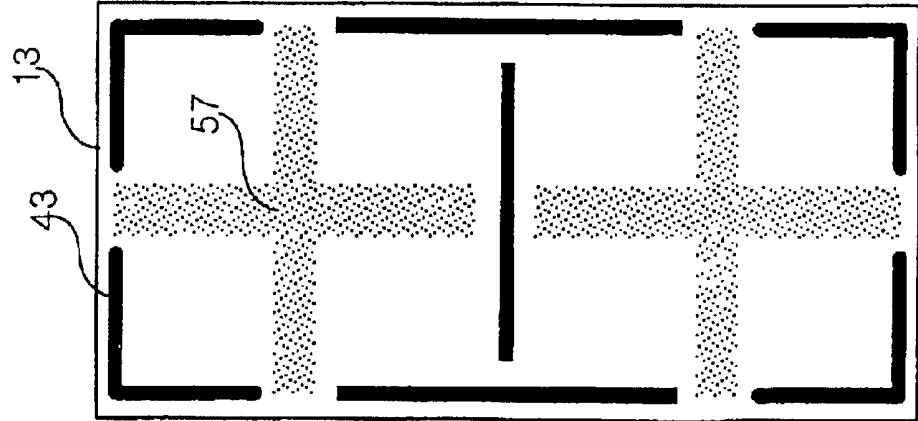


FIG. 8I

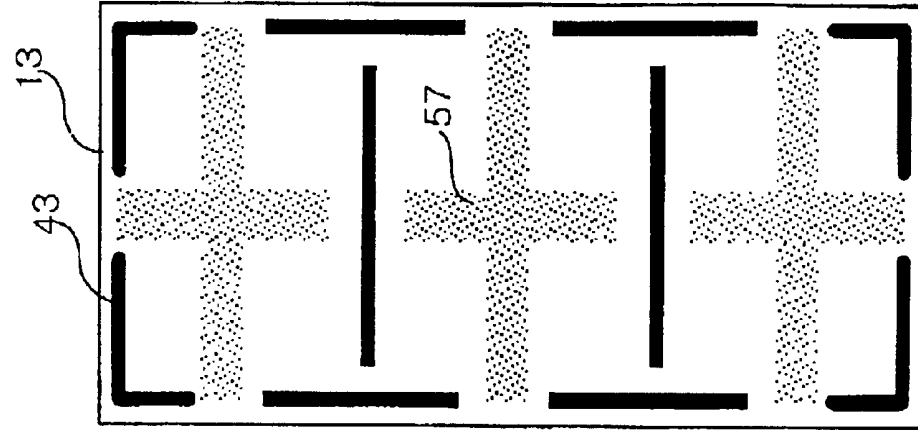


FIG. 8L

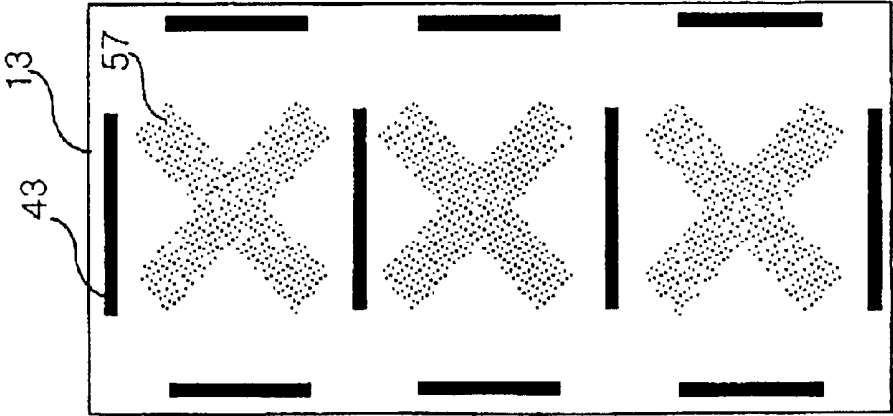


FIG. 8K

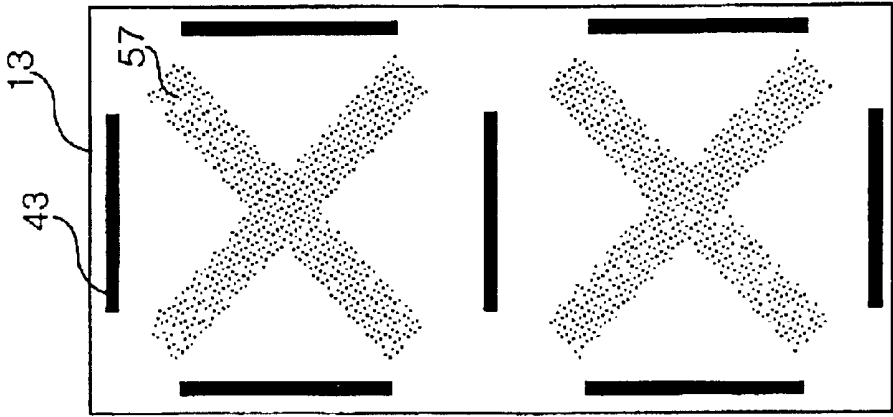
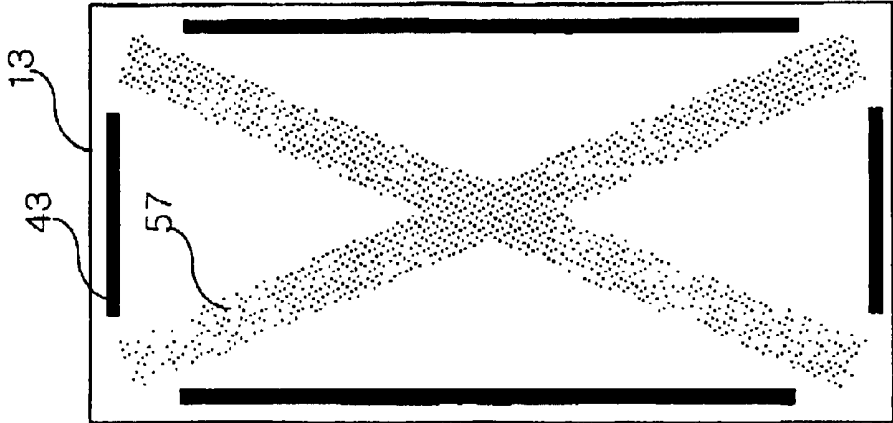


FIG. 8J



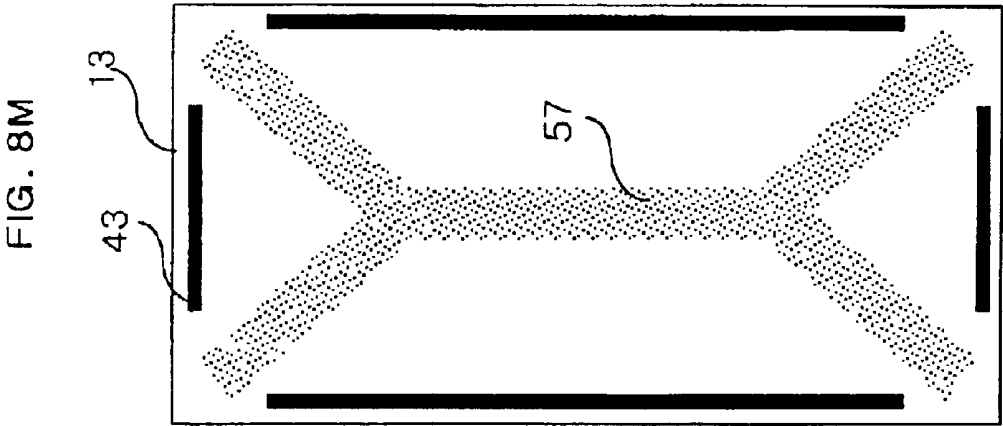


FIG. 9A

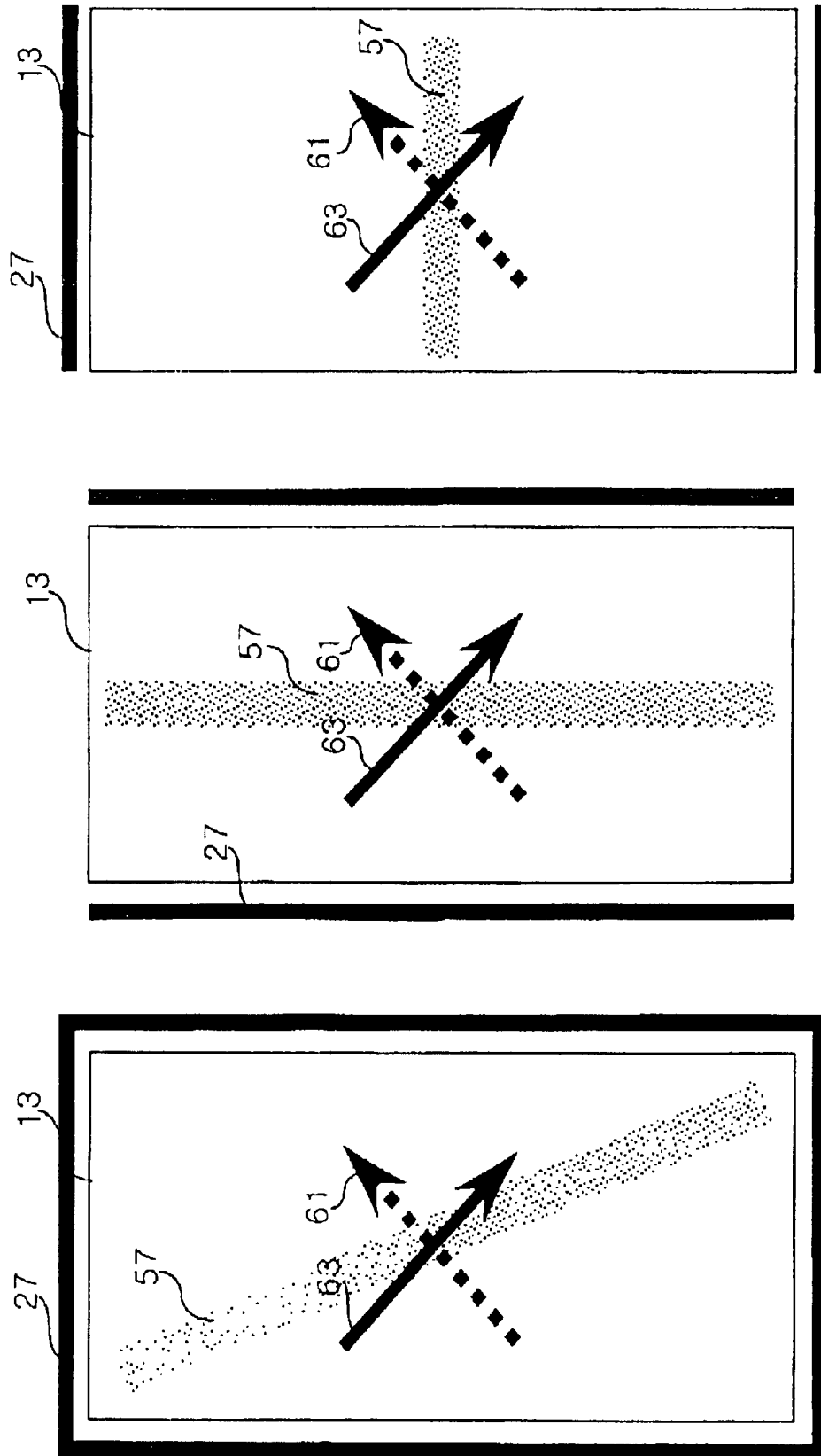


FIG. 9B

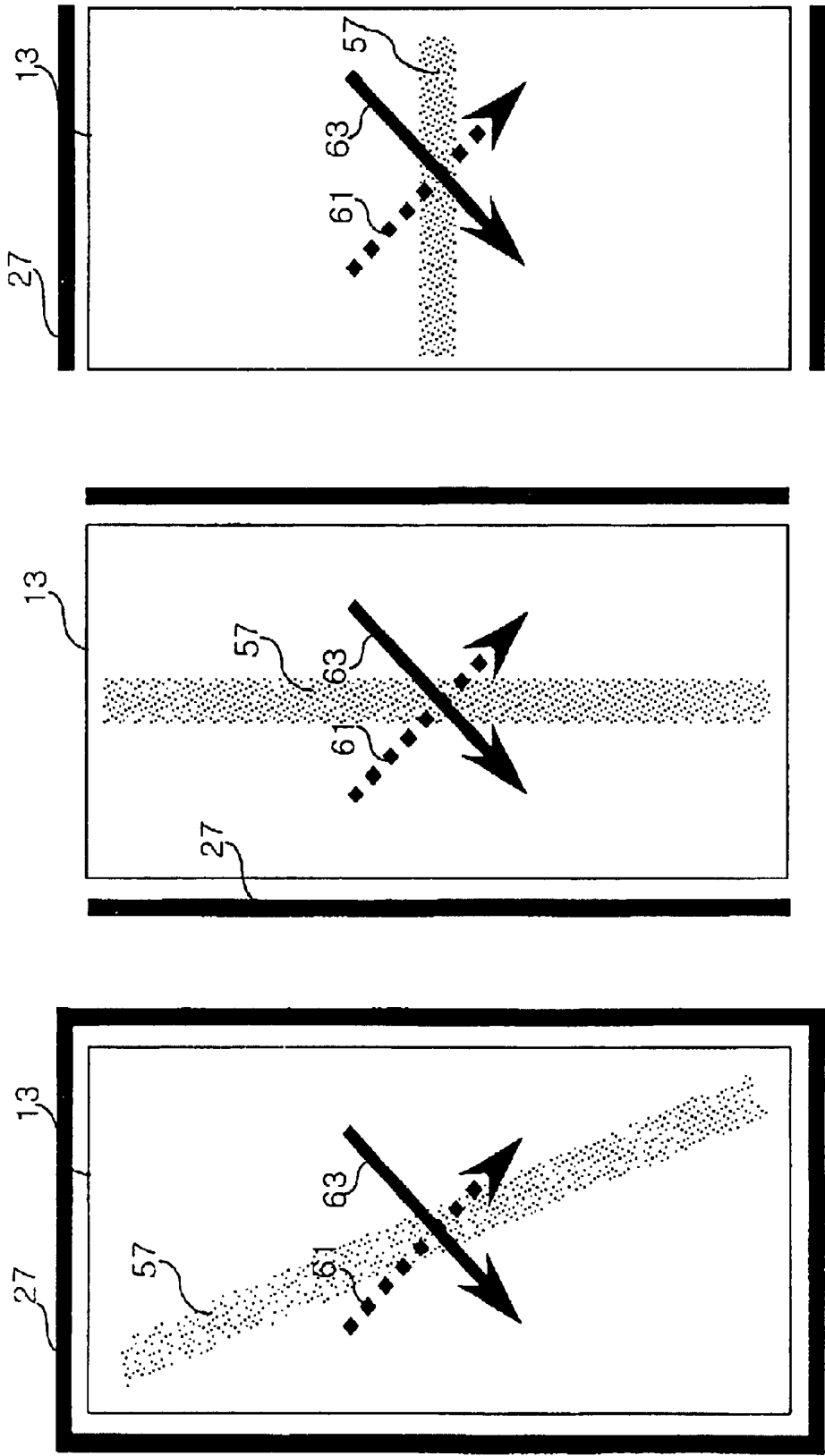


FIG. 9C

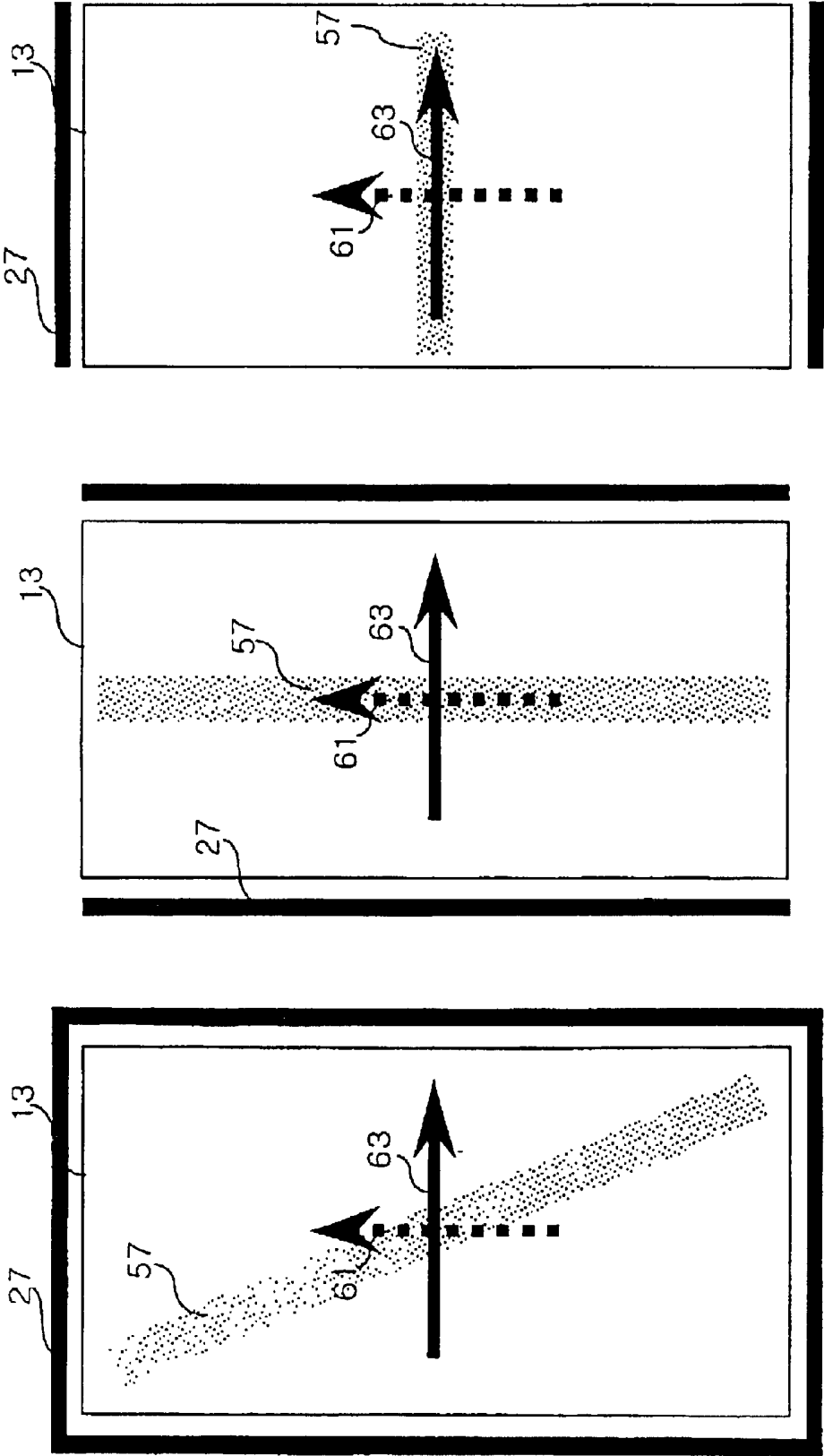


FIG. 9D

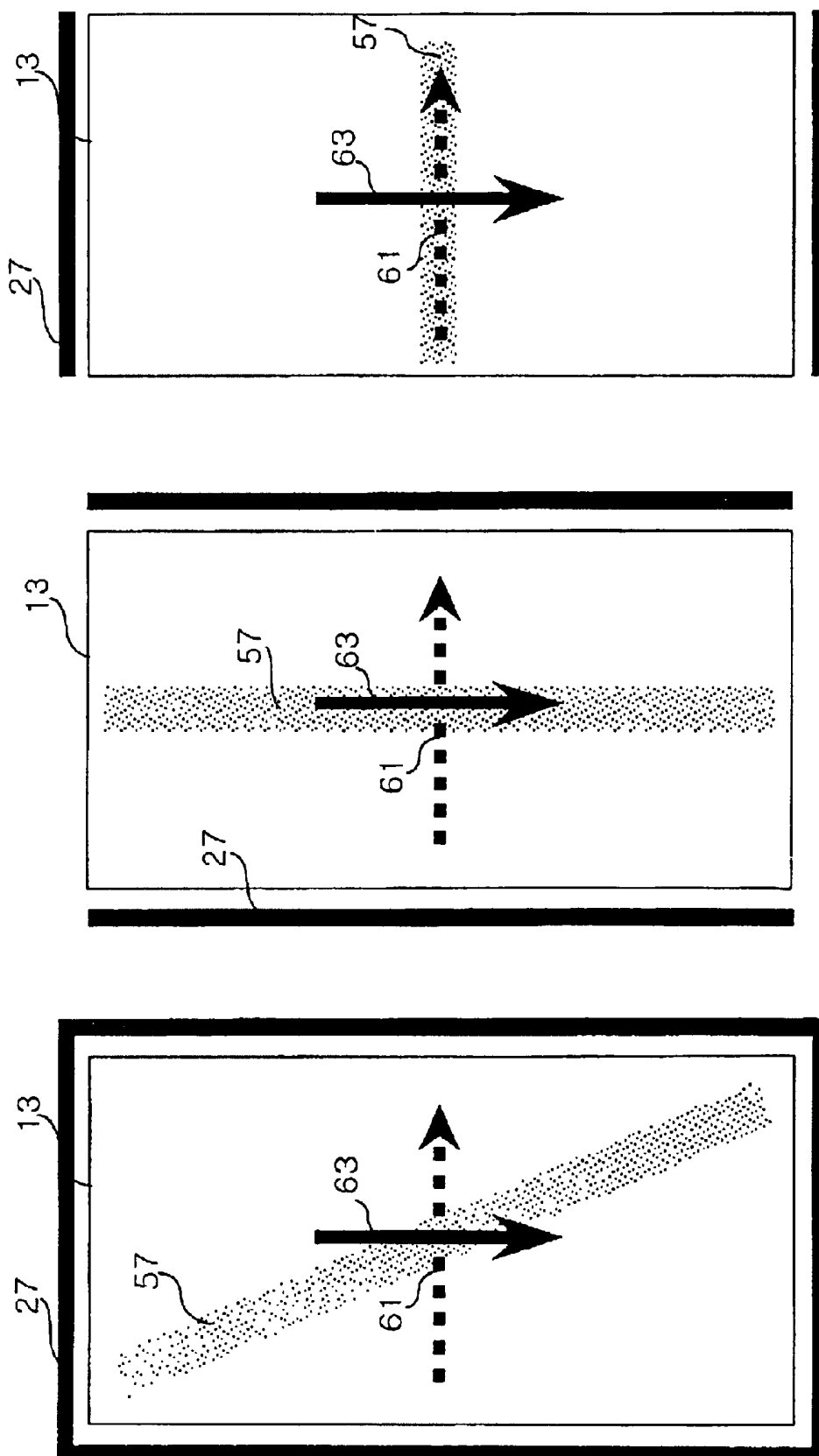


FIG. 10A

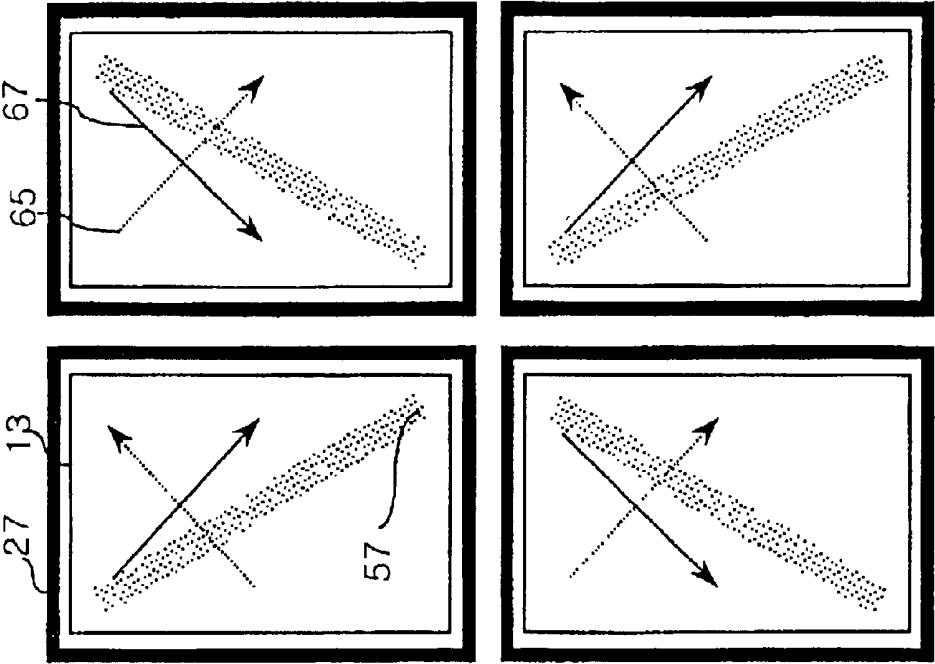


FIG. 10C

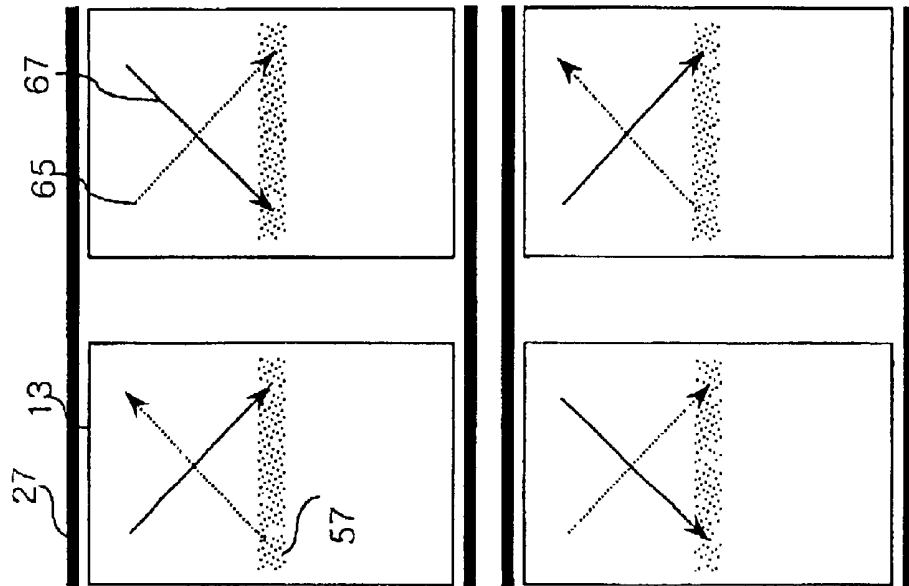


FIG. 10B

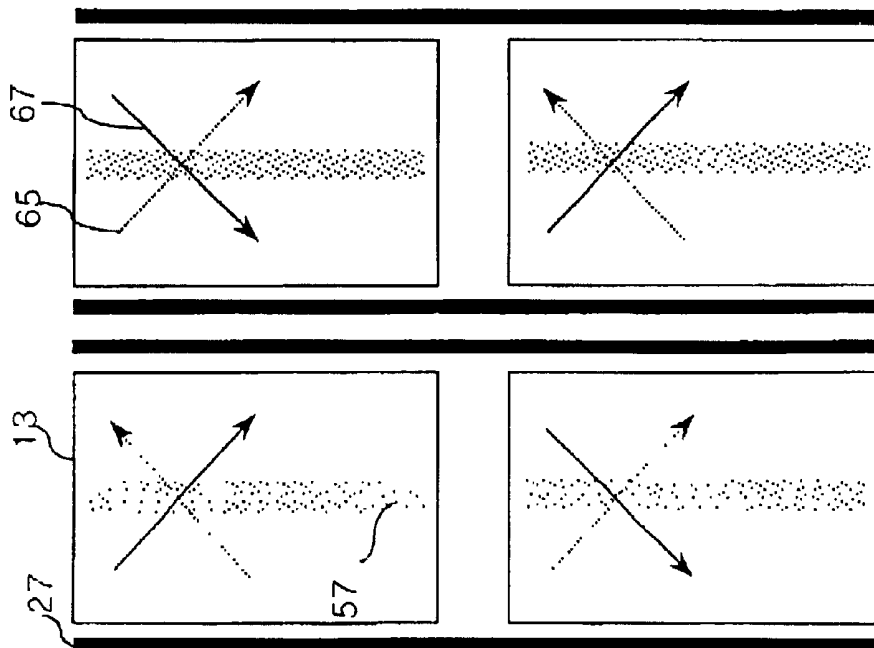


FIG. 11A

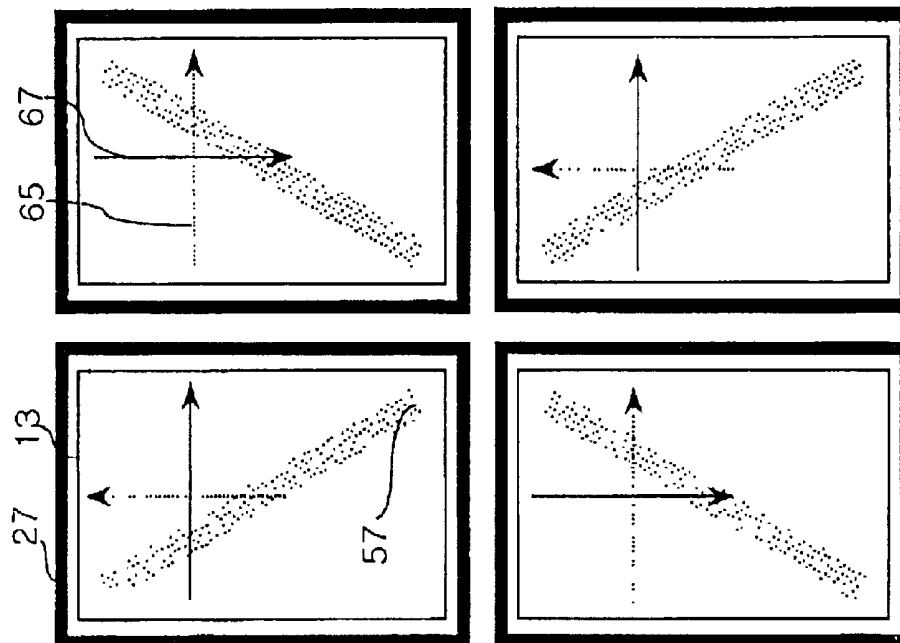


FIG. 11C

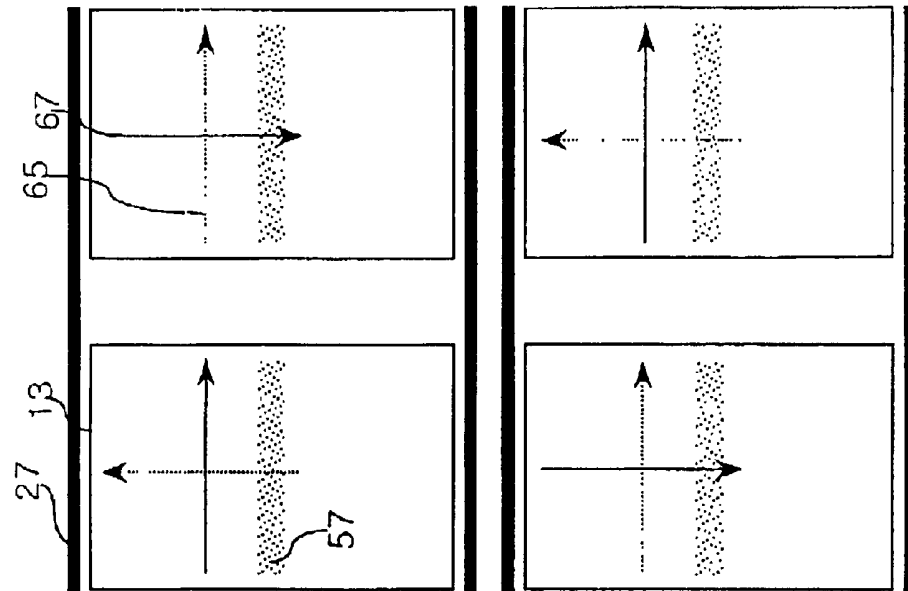
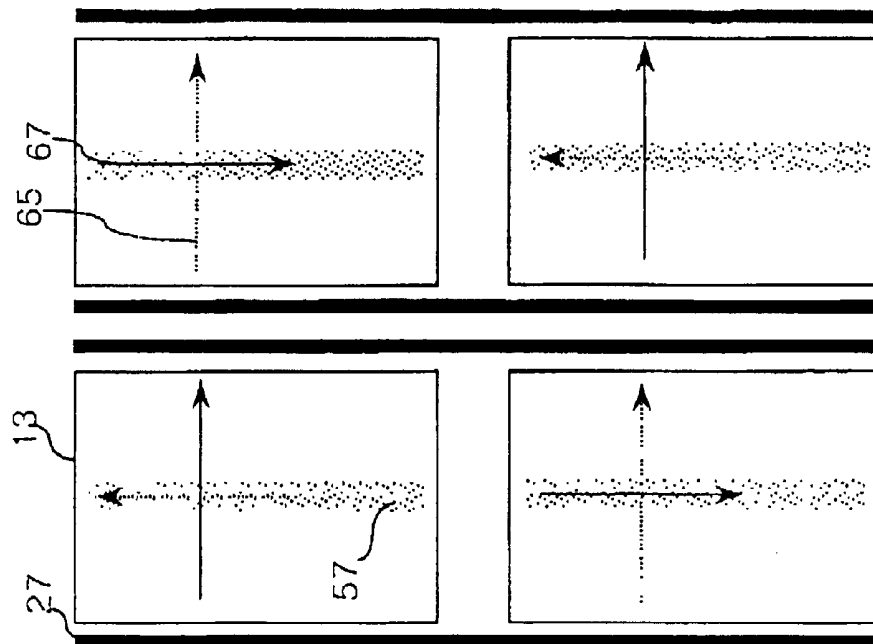


FIG. 11B



# MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE WITH DIELECTRIC FRAME

"This Application is a Divisional of Application Ser. No. 09/955,157, filed on Sep. 19, 2001, now U. S. Pat. No. 6,473,142, which is a continuation of Application Ser. No. 09/326,415, filed Jun. 4, 1999, now U.S. Pat. No. 6,356,335."

## BACKGROUND OF THE INVENTION

### 1. Field of the Invention

The present invention relates to a liquid crystal display device (LCD), and more particularly, to a liquid crystal display device having dielectric frames of which the dielectric constant is different from that of liquid crystal.

### 2. Description of the Related Art

Recently, an LCD has been proposed where the liquid crystal is not aligned, and the liquid crystal is driven by a common electrode **17** having open areas **19**. FIG. **1** is a sectional view of a pixel unit of the conventional LCD.

Regarding conventional LCDs, a plurality of gate bus lines arranged in a first direction on a first substrate and a plurality of data bus lines arranged in a second direction on the first substrate divide the first substrate into a plurality of pixel regions.

A thin film transistor (TFT) applies an image signal delivered from the data bus line to a pixel electrode **13** on a passivation layer. The TFT is formed on each pixel region and comprises a gate electrode, a gate insulator, a semiconductor layer, an ohmic contact layer, a source electrode, and a drain electrode, etc.

Alternatively, a passivation layer is formed over the whole first substrate, and pixel electrode **13** is connected to the drain electrode thereon.

On a second substrate, a light shielding layer **25** is formed to shield any light leakage from gate and data bus lines, and the TFT. A color filter layer **23** is formed on the light shielding layer, an over coat layer **45** is formed on the color filter layer, a common electrode **17** is formed to have an open area **19** on the over coat layer, spacers **51** are dispersed to maintain the gap between the first and second substrates thereon, and a liquid crystal layer is formed between the first and second substrates.

Pixel electrode **13** and open area (slit) **19** of the common electrode **17** distort the electric field applied to the liquid crystal layer. Then, liquid crystal molecules are driven variously in a unit pixel. This means that when voltage is applied to the LCD, dielectric energy due to the distorted electric field arranges the liquid crystal directors in needed or desired positions.

In the LCDs, however, open area **19** in common electrode **17** or pixel electrode **13** is necessary, and the liquid crystal (LC) molecules could be driven stably when the open area is wider. If the electrodes do not have an open area or the width of the open area is narrow, the electric field distortion needed to divide the pixel region becomes weak.

Disclination occurs from the area where the liquid crystal directors are parallel with a transmittance axis of the polarizer, which results in a decrease in brightness. Further, according to the surface state of LCDs, the liquid crystal texture has an irregular structure.

Moreover, because spacer **51** is not fixed and is mobile, there is problem that the distribution of the spacers is not uniform in the LC cell.

## SUMMARY OF THE INVENTION

Accordingly, the present invention is directed to an LCD that substantially obviates one or more problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide a multi-domain LCD having a wide viewing angle and high brightness by the stable arrangement of liquid crystal molecules.

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve the objects and in accordance with the purpose of the invention, as embodied and broadly described herein, a multi-domain liquid crystal display device comprises first and second substrates facing each other; a liquid crystal layer between the first and second substrates; a plurality of gate bus lines arranged in a first direction on the first substrate and a plurality of data bus lines arranged in a second direction on the first substrate to define a pixel region; a pixel electrode electrically charged through the data bus line in the pixel region; a color filter layer on the second substrate; a common electrode on the color filter layer; dielectric frames in the pixel region; and an alignment layer on at least one substrate between the first and second substrates.

The dielectric frame is patterned, and the dielectric constant of the dielectric frame is lower than the dielectric constant of the liquid crystal layer.

The dielectric frame includes photosensitive materials, preferably a material selected from the group consisting of photoacrylate and [BenzoCycloButene (BCB)].

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

## BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of the specification, illustrates embodiments of the invention and together with description serve to explain the principles of the invention.

In the drawings:

FIG. **1** is a sectional view of the liquid crystal display device in the related art;

FIGS. **2A** and **2B** are plan and sectional views multi-domain liquid crystal display device according to the first embodiment of the present invention;

FIGS. **3A** and **3B**, **3C** are plan and sectional views of the multi-domain liquid crystal display devices according to the second embodiment of the present invention;

FIGS. **4A** and **4B**, **4C** are plan and sectional views of the multi-domain liquid crystal display devices according to the third embodiment of the present invention;

FIGS. **5A** and **5B**, **5C**, **5D**, **5E**, **5F**, **5G** are plan and sectional views of the multi-domain liquid crystal display devices according to fourth embodiment of the present invention;

FIGS. **6A**, **6B**, **6C**, **6D**, **6E**, **6F**, and **6G** are plan views of the multi-domain liquid crystal display devices according to embodiments of the present invention;

FIGS. 7A, 7B, 7C, 7D, 7E, 7F, and 7G are plan views of the multi-domain liquid crystal display devices according to embodiments of the present invention;

FIGS. 8A, 8B, 8C, 8D, 8E, 8F, 8G, 8H, 8I, 8J, 8K, 8L, and 8M are plan views of the multi-domain liquid crystal display devices according to embodiments of the present invention;

FIGS. 9A, 9B, 9C, and 9D are plan views of the multi-domain liquid crystal display devices according to embodiments of the present invention;

FIGS. 10A, 10B, and 10C are plan views of the multi-domain liquid crystal display devices according to embodiments of the present invention; and

FIGS. 11A, 11B, and 11C are plan views of the multi-domain liquid crystal display devices according to embodiments of the present invention.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinafter, the multi-domain liquid crystal display device of the present invention is explained in detail by accompanying the drawings.

FIGS. 2A and 2B are plan and sectional view of the multi-domain liquid crystal display device according to the first embodiment of the present invention, FIGS. 3A, and 3B, 3C are plan and sectional views of the multi-domain liquid crystal display devices according to the second embodiment of the present invention, FIGS. 4A and 4B, 4C are plan and sectional views of the multi-domain liquid crystal display devices according to the third embodiment of the present invention, and FIGS. 5A and 5B, 5C, 5D, 5E, 5F, 5G are plan and sectional views of the multi-domain liquid crystal display devices according to fourth embodiment of the present invention.

As shown in the figures, the present invention comprises first substrate 31 and second substrate 33, a plurality of gate bus lines arranged in a first direction on the first substrate and a plurality of data bus lines 3 arranged in a second direction on the first substrate 31, a TFT 11, a passivation layer 37 on the whole first substrate 31, a pixel electrode 13, and a first alignment layer 53 on the whole first substrate 31.

On a second substrate 33, a light shielding layer 25 is formed to shield any light leakage from gate and data bus lines, and the TFT, a color filter layer 23 is formed on the light shielding layer, a common electrode 17 is formed on the color filter layer, a dielectric frame 57 to distort electric field on the common electrode 17, a second alignment layer 55 on the whole second substrate 33, and a liquid crystal layer is formed between the first and second substrates.

Data bus lines and gate bus lines divide the first substrate 31 into a plurality of pixel regions. The TFT is formed on each pixel region and comprises a gate electrode 11, a gate insulator 35, a semiconductor layer 5, an ohmic contact layer, source electrode 7 and a drain electrode 9. Passivation layer 37 is formed on the whole first substrate and pixel electrode 13 is coupled to drain electrode 9.

To manufacture the multi-domain LCD of the present invention, in each pixel region on the first substrate 31, a TFT is formed comprising gate electrode 11, gate insulator 35, semiconductor layer 5, ohmic contact layer and source/drain electrodes 7, 9. At this time, a plurality of gate bus lines and a plurality of data bus lines are formed to divide the first substrate 31 into a plurality of pixel regions.

Gate electrode 11 and gate bus line are formed by sputtering and patterning a metal such as aluminum (Al), molybdenum (Mo), chromium (Cr), tantalum (Ta), Al alloy, etc.

The gate insulator 35 is formed by depositing silicon nitride ( $\text{SiN}_x$ ) or silicon oxide ( $\text{SiO}_x$ ) using Plasma Enhanced Chemical Vapor Deposition (PECVD) thereon. Semiconductor layer 5 and the ohmic contact layer are formed by depositing with PECVD and patterning amorphous silicon (a-Si) and doped amorphous silicon ( $n^+$ a-Si), respectively. Also, the gate insulator 35, semiconductor layer 5, and the ohmic contact layer are formed by PECVD and patterned. Data bus line and source/drain electrodes 7, 9 are formed by sputtering and patterning a metal such as aluminum (Al), molybdenum (Mo), chromium (Cr), tantalum (Ta), Al alloy, etc.

A storage electrode (not shown in the figures) is formed to overlap gate bus line and to connect to the pixel electrode 13 at the same time, the storage electrode makes a storage capacitor with the gate bus line.

Subsequently, passivation layer 37 is formed with BenzoCycloButene (BCB), acrylic resin, polyimide based material, silicon nitride ( $\text{SiN}_x$ ) or silicon oxide ( $\text{SiO}_x$ ) on the whole first substrate 31. Pixel electrode 13 is formed by sputtering and patterning a metal such as indium tin oxide (ITO). A contact hole 39 is formed to connect the pixel electrode 13 to the drain and storage electrodes by opening and patterning a part of the passivation layer 37 on drain electrode 9.

On the second substrate 33, a light shielding layer 25 is formed to shield any light leakage from gate and data bus lines, and the TFT. A color filter layer 23 is formed red, green, blue (R, G, B) elements to alternate on the light shielding layer. A common electrode 17 is formed with ITO on the color filter layer. A dielectric frame 57 is formed by depositing photosensitive material on the common electrode 17 or pixel electrode 13 and patterning in various shapes using photolithography. A liquid crystal layer is formed by injecting liquid crystal between the first and second substrates.

The dielectric frame 57 includes material of which dielectric constant is same or smaller than that of the liquid crystal, and the dielectric constant thereof is preferably below 3, for example, photoacrylate or BenzoCycloButene (BCB).

Furthermore, the dielectric frame 57 is also used as a spacer (refer to FIGS. 2B, 3C, 4C, 5C, 5E, and 5G). Dielectric frame 57 is formed on at least one substrate between the first and second substrates. In these embodiments, a spacer dispersing process could be omitted and the gap uniformity of liquid crystal cell is enhanced, therefore, the yield is improved.

An electric field inducing window 43 is formed on at least one substrate between the first and second substrates (refer to FIGS. 4B and 5F, 5G). At this time, the dielectric frame and electric field inducing window are formed on same substrate together. The electric field inducing window 43 is formed in various shapes by patterning a hole or a slit in the common electrode 17 or pixel electrode 13.

As an embodiment in the multi-domain LCD of the present invention, an auxiliary electrode 27 is additionally formed in an area except the pixel region. (refer to FIGS. 3A and 5A) The auxiliary electrode 27 is formed on a layer whereon the pixel electrode 17 or gate electrode 11 is formed, and electrically connected to the common electrode 17. (refer to FIGS. 3B, 3C and 5D, 5E)

The auxiliary electrode 27 is formed by sputtering and patterning a metal such as indium tin oxide (ITO), aluminum (Al), molybdenum (Mo), chromium (Cr), tantalum (Ta), titanium (Ti) or Al alloy. At this time, it is possible to form the auxiliary and pixel electrodes 27, 13 by patterning the same metal once or by patterning different metals twice.

As shown in FIGS. 7, 9, 10, and 11, the auxiliary electrode 27 can be formed as surrounding the pixel electrode 13, on the side of data bus line and/or on the side of gate bus line.

FIG. 5 shows that the light shielding layer 25 is formed on the first substrate 31, FIGS. 5D and 5E show that the auxiliary electrode 27 is formed on a layer whereon the pixel electrode 17 is formed. In these embodiments, the light shielding layer is formed to adjust exactly the pixel region, hence, the lamination margin is reduced and the aperture ratio is enhanced than the light shielding layer is formed on the second substrate.

On at least one substrate, a compensation film 29 is formed with polymer. The compensation film is a negative uniaxial film, which has one optical axis, and compensates the phase difference of the direction according to viewing-angle. Hence, it is possible to compensate effectively the right-left viewing-angle by widening the area without gray inversion, increasing contrast ratio in an inclined direction, and forming one pixel to multi-domain.

In the present multi-domain liquid crystal display device, it is possible to form a negative biaxial film as the compensation film 29, which has two optical axes and has wider viewing-angle characteristics as compared with the negative uniaxial film. The compensation film could be formed on both substrates or on one of them.

After forming the compensation film 29, a polarizer is formed on at least one substrate. At this time, the compensation film and polarizer are preferably composed as one.

In the FIGS. 6A to 6G, the dielectric frame 57 is patterned in various shapes, which obtains a multi-domain effect.

In the FIGS. 7A to 7G, the auxiliary electrode 27 is formed surrounding pixel electrode 13, and the dielectric frame 57 is patterned in various shapes, which obtains a multi-domain effect.

In the FIGS. 8A to 8M, the electric field inducing window 43 is formed, and the dielectric frame 57 is patterned in various shapes, which obtains a multi-domain effect. The electric field inducing window 43 may be a slit or a hole.

In the LCD in FIGS. 6 to 8, the liquid crystal layer includes liquid crystal molecules having negative dielectric anisotropy, which applies a homeotropic alignment where liquid crystal molecules in the liquid crystal layer are aligned homeotropically to surfaces of the first and second substrates.

In the FIGS. 9A, 9B, 9C, and 9D, the auxiliary electrode 27 is formed, and the dielectric frame 57 is patterned in various shapes, which obtains a multi-domain effect. Although not shown in the figures, there are embodiments that do not form the auxiliary electrode 27.

The solid lined-arrow 63 represents the rubbing direction of the second substrate 33 and the dotted lined-arrow 61 represents the rubbing direction of the first substrate 31.

In the FIGS. 1A, 10B, and 10C, the auxiliary electrode 27 is formed, and the dielectric frame 57 is patterned in various shapes. Furthermore, two neighboring pixels and two alignment directions are associated, which obtains a multi-domain effect. Although not shown in the figures, there are embodiments that do not form the auxiliary electrode 27.

The solid lined-arrow 67 represents the alignment direction of the second substrate 33 and the dotted lined-arrow 65 represents the alignment direction of the first substrate 31.

In the FIGS. 11A, 11B, and 11C, the auxiliary electrode 27 is formed, and the dielectric frame 57 is patterned in various shapes. Furthermore, two neighboring pixels and two alignment directions are associated being different from that in

the FIG. 10, which obtains a multi-domain effect. Although not shown in the figures, there are embodiments that do not form the auxiliary electrode 27.

In the LCD in FIGS. 9 to 11, the liquid crystal layer includes liquid crystal molecules having positive dielectric anisotropy, which applies a homogeneous alignment where liquid crystal molecules in the liquid crystal layer are aligned homogeneously to surfaces of the first and second substrates.

From forming the electric field inducing window or dielectric frame, the multi-domain is obtained by dividing each pixel into four domains such as in a "+", "x", or "double Y" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate.

Furthermore, in multi-domain LCD of the present invention, the first and second alignment layers 53, 55 are formed over the whole first and/or second substrates. The alignment layer includes a material such as polyamide or polyimide based materials, polyvinylalcohol (PVA), polyamic acid or SiO<sub>2</sub>. When rubbing is used to determine an alignment direction, it should be possible to apply any material suitable for the rubbing treatment.

Moreover, it is possible to form the alignment layer with a photosensitive material such as polyvinylcinnamate (PVCN), polysiloxanecinnamate (PSCN), and cellulosecinnamate (CelCN) based materials. Any material suitable for the photo-aligning treatment may be used. Irradiating light once on the alignment layer determines the alignment or pretilt direction and the pretilt angle. The light used in the photo-alignment is preferably a light in a range of ultraviolet light, and any of unpolarized light, linearly polarized light, and partially polarized light can be used.

In the rubbing or photo-alignment treatment, it is possible to apply one or both of the first and second substrates, and to apply different aligning-treatment on each substrate.

From the aligning-treatment, a multi-domain LCD is formed with at least two domains, and LC molecules of the LC layer are aligned differently from one another on each domain. That is, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate.

It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Consequently, the multi-domain LCD of the present invention forms dielectric frames of which the dielectric constant is different from that of the liquid crystal, and the auxiliary electrode or electric field inducing window to distort electric field, thereby a wide viewing angle is obtained.

Also, the dielectric frame is patterned as a spacer, which can leave out the spacer process in the conventional LCD processes.

Furthermore, in the case of conducting an alignment-treatment, a high response time and a stable LC structure can be obtained by a pretilt angle and an anchoring energy.

It will be apparent to those skilled in the art that various modifications can be made in the liquid crystal display device of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention covers the modifications and variations of

7

this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A multi-domain liquid crystal display device comprising: first and second substrates;  
a plurality of gate bus lines and data bus lines on the first substrate to define a pixel region;  
a common electrode on the second substrate;  
a pixel electrode on the first substrate in the pixel region;  
an auxiliary electrode on the first substrate, the auxiliary electrode distorting electric field between the common electrode and the pixel electrode;  
a dielectric frame on the second substrate; and  
a liquid crystal layer between the first and second substrates.
2. The device of claim 1, wherein the dielectric frame includes a photosensitive material.
3. The device of claim 1, wherein the liquid crystal layer includes liquid crystal molecules having positive dielectric anisotropy.
4. The device of claim 1, wherein the liquid crystal layer includes liquid crystal molecules having negative dielectric anisotropy.
5. The device of claim 1, further comprising an alignment layer on at least one of the first and second substrates.
6. The device of claim 5, wherein the alignment layer is divided into at least two portions, the liquid crystal layer in each portion being aligned differently from each other.
7. The device of claim 5, wherein at least one portion of the alignment layer is rubbing-treated.
8. The device of claim 5, wherein at least one portion of the alignment layer is photo-alignment-treated.
9. The device of claim 8, wherein the liquid crystal layer includes liquid crystal molecules having positive dielectric anisotropy.

8

10. The device of claim 8, wherein the liquid crystal layer includes liquid crystal molecules having negative dielectric anisotropy.

11. A multi-domain liquid crystal display device comprising:

- first and second substrates;
- a plurality of gate bus lines and data bus lines on the first substrate to define a pixel region;
- thin film transistors adjacent crossings of the gate bus lines and the data bus lines;
- a light shielding layer over at least one of the thin film transistors in an area except the pixel region on the first substrate;
- an auxiliary electrode on the first substrate, the auxiliary electrode distorting electric field between the common electrode and the pixel electrode;
- a pixel electrode in the pixel region;
- a dielectric frame on the second substrate; and
- a liquid crystal layer between the first and second substrates.

12. The device of claim 11, wherein the dielectric frame includes a photosensitive material.

13. A multi-domain liquid crystal display device, comprising:

- first and second substrates;
  - a plurality of gate bus lines and data bus lines on the first substrate to define a pixel region;
  - an auxiliary electrode on the first substrate;
  - a pixel electrode in the pixel region;
  - a dielectric frame on the second substrate; and
  - a liquid crystal layer between the first and second substrates,
- wherein the dielectric frame is a spacer.

\* \* \* \* \*

|                |                                                                |         |            |
|----------------|----------------------------------------------------------------|---------|------------|
| 专利名称(译)        | 具有介电框架的多畴液晶显示装置                                                |         |            |
| 公开(公告)号        | <a href="#">US6825906</a>                                      | 公开(公告)日 | 2004-11-30 |
| 申请号            | US10/231167                                                    | 申请日     | 2002-08-30 |
| [标]申请(专利权)人(译) | KIM KYEONG金<br>YOO张真<br>BAE盛骏                                  |         |            |
| 申请(专利权)人(译)    | KIM KYEONG金<br>YOO张真<br>BAE盛骏                                  |         |            |
| 当前申请(专利权)人(译)  | LG.PHILIPS LCD CO. , INC.                                      |         |            |
| [标]发明人         | KIM KYEONG JIN<br>YOO JANG JIN<br>BAE SUNG JOON                |         |            |
| 发明人            | KIM, KYEONG JIN<br>YOO, JANG JIN<br>BAE, SUNG JOON             |         |            |
| IPC分类号         | G02F1/13 G02F1/1333 G02F1/1337 G02F1/1335 G02F1/139            |         |            |
| CPC分类号         | G02F1/133345 G02F1/133707 G02F1/133512 G02F1/1393 G02F1/133753 |         |            |
| 审查员(译)         | KIM , 罗伯特H.                                                    |         |            |
| 优先权            | 1019980048226 1998-11-11 KR                                    |         |            |
| 其他公开文献         | US20030020858A1<br>US20040027523A9                             |         |            |
| 外部链接           | <a href="#">Espacenet</a> <a href="#">USPTO</a>                |         |            |

#### 摘要(译)

一种多畴液晶显示装置，包括彼此面对的第一和第二基板以及在第一和第二基板之间的液晶层。多条栅极总线沿第一方向布置在第一基板上，多条数据总线沿第二方向布置在第一基板上以限定像素区域。像素电极通过像素区域中的数据总线充电，在第二基板上形成滤色器层，并且在滤色器层上形成公共电极。介电框架形成在像素区域中，并且在第一和第二基板之间的至少一个基板上形成配向层。

