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(54) **LIQUID CRYSTAL DISPLAY APPARATUS AND METHOD OF FORMING THE SAME**

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(57) **ABSTRACT**

A liquid crystal display device including a first substrate, a common electrode formed over the first substrate, and a second substrate disposed opposite the first substrate. A common voltage-applying member applies a common voltage to the common electrode and maintains a cell gap between the first substrate and the second substrate. The common voltage-applying member includes an insulator and a conductor formed over the insulator.

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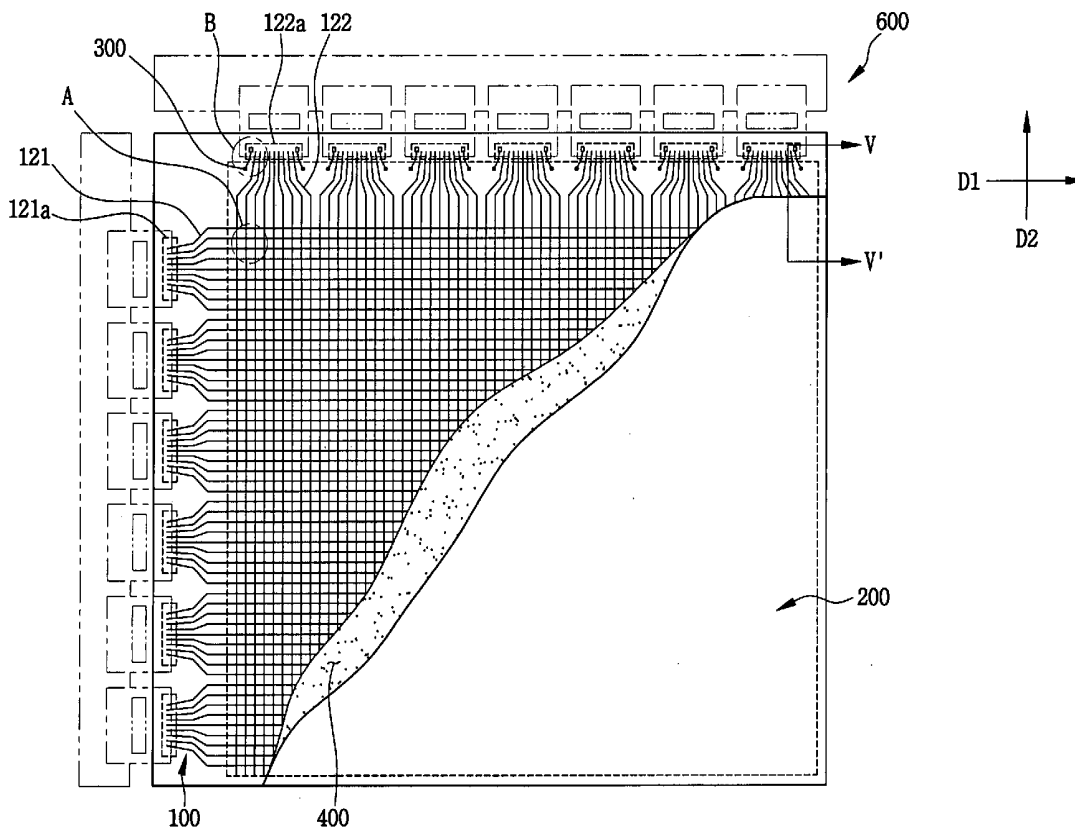


FIG. 1

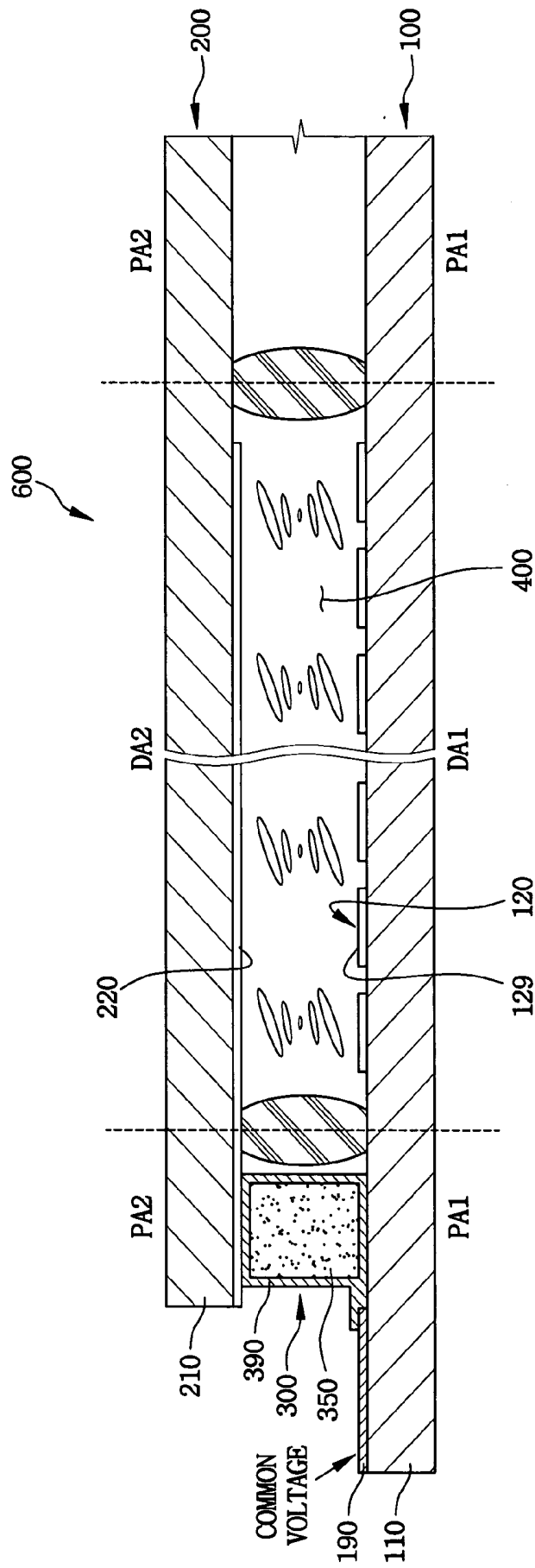


FIG. 2

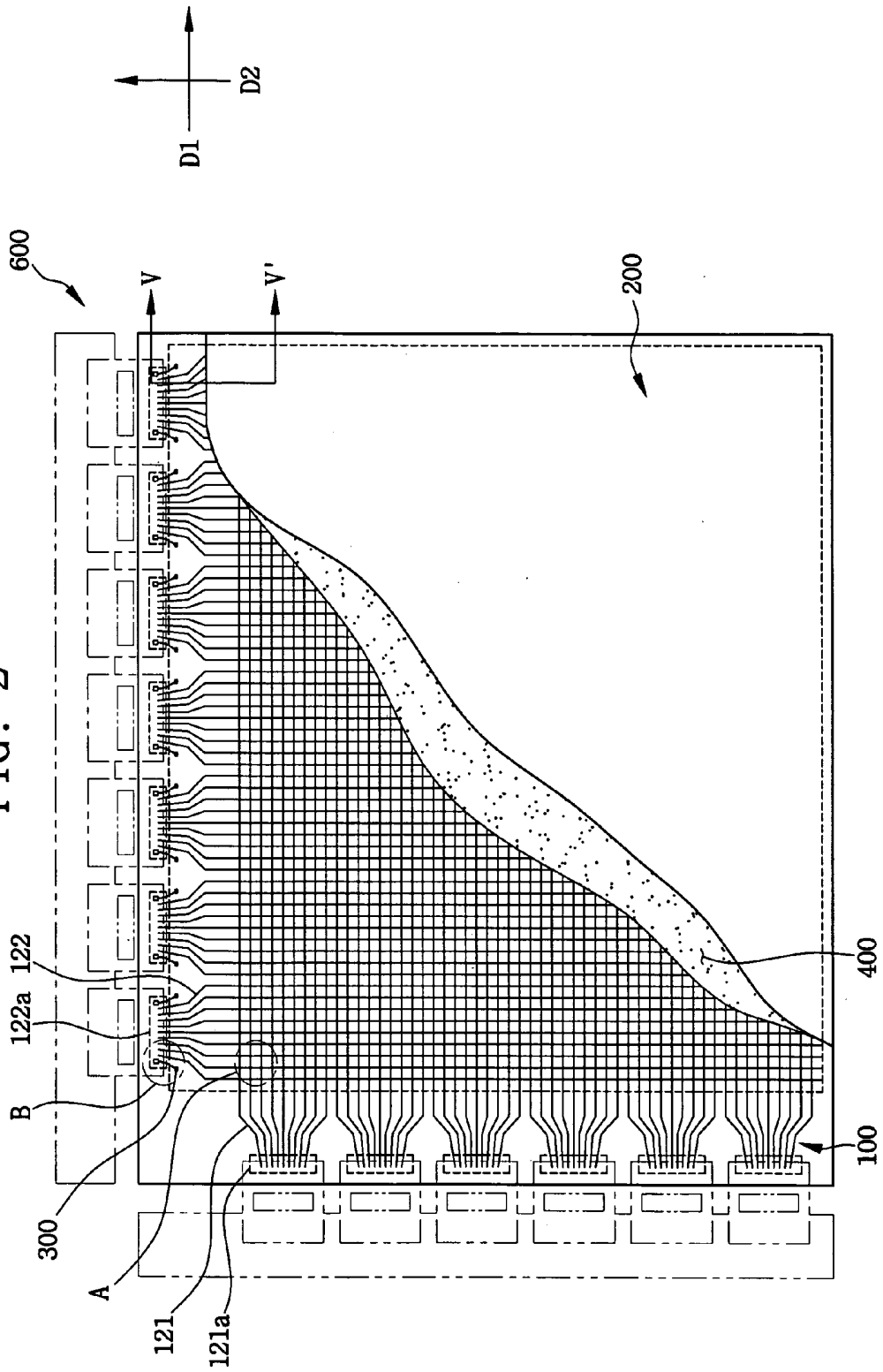


FIG. 3

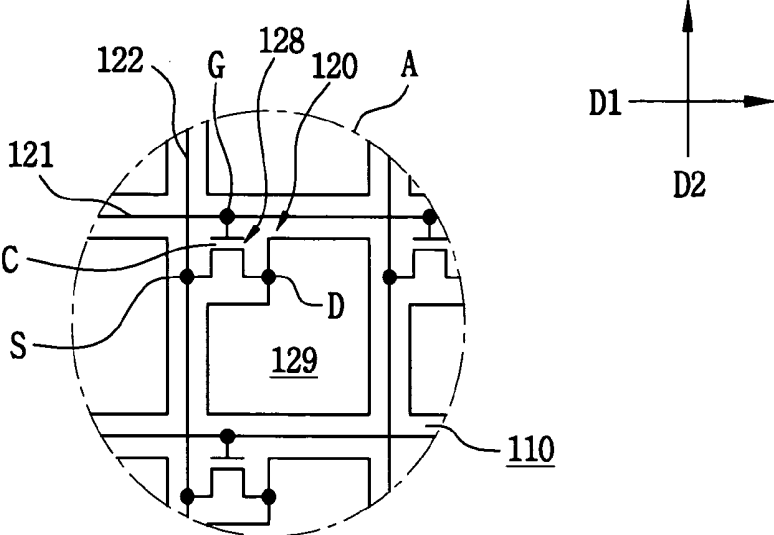


FIG. 4

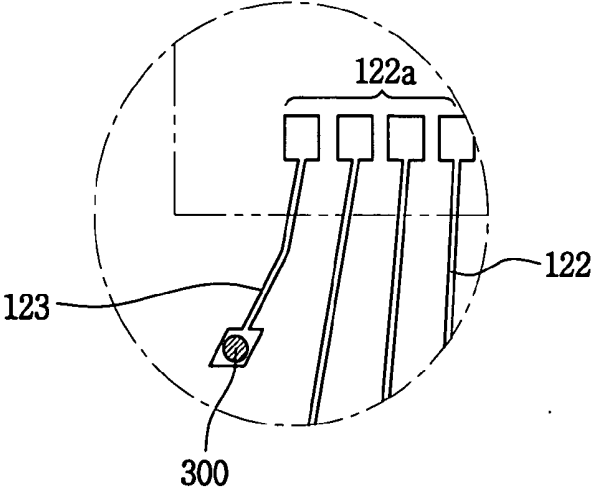


FIG. 5

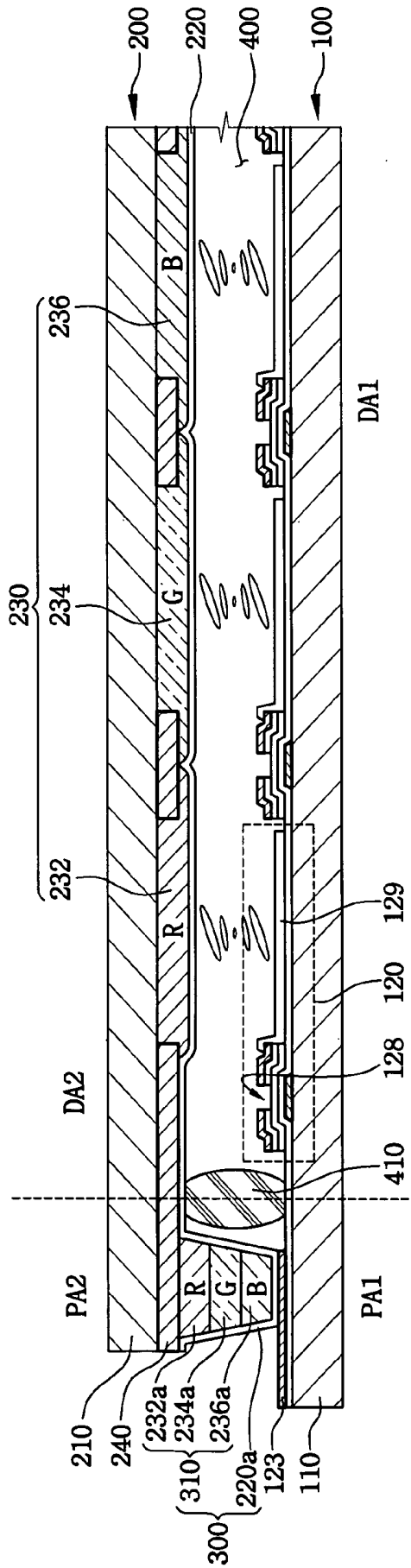


FIG. 6A

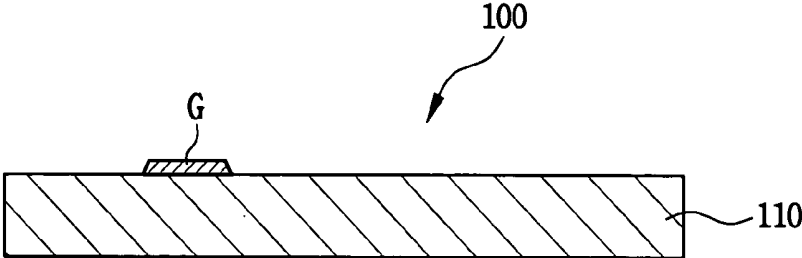


FIG. 6B

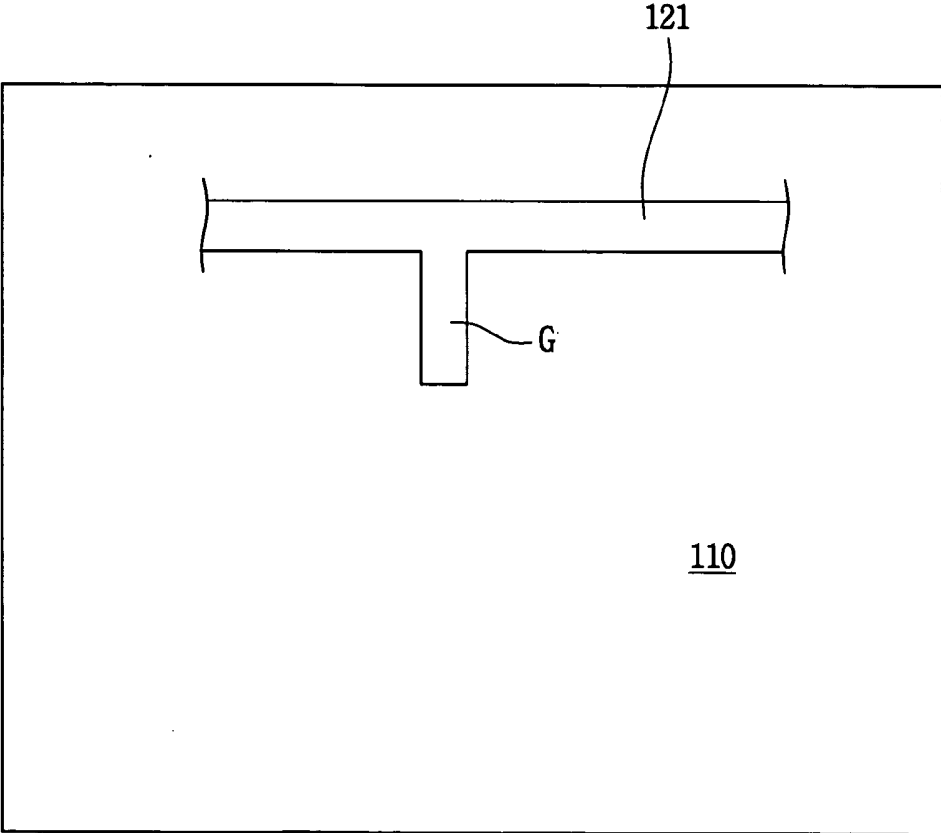


FIG. 7A

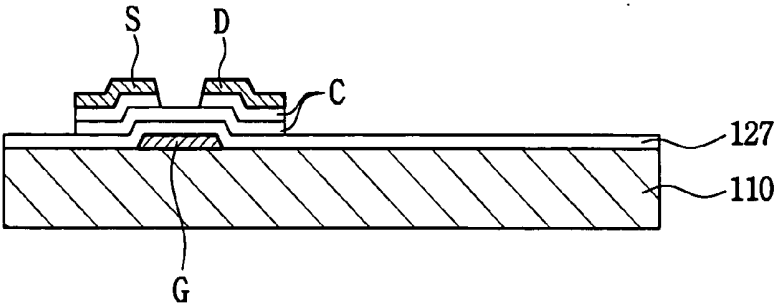


FIG. 7B

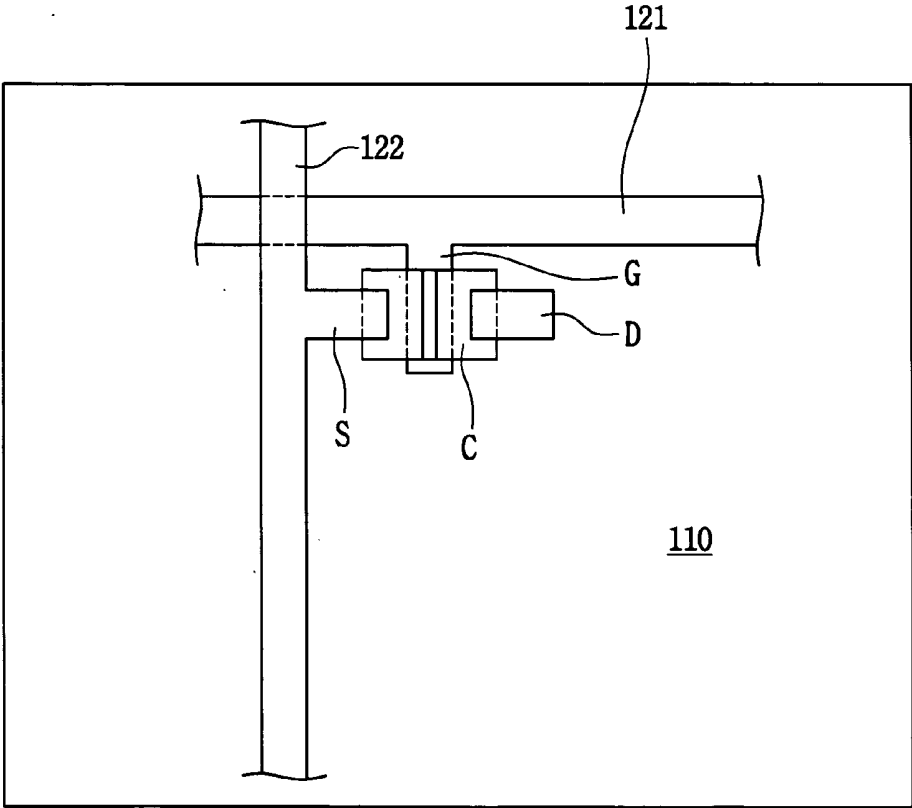


FIG. 8A

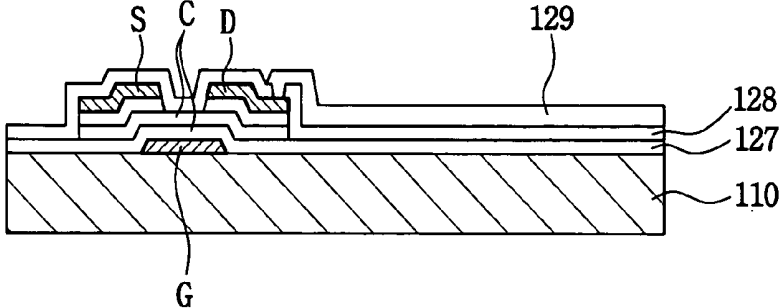


FIG. 8B

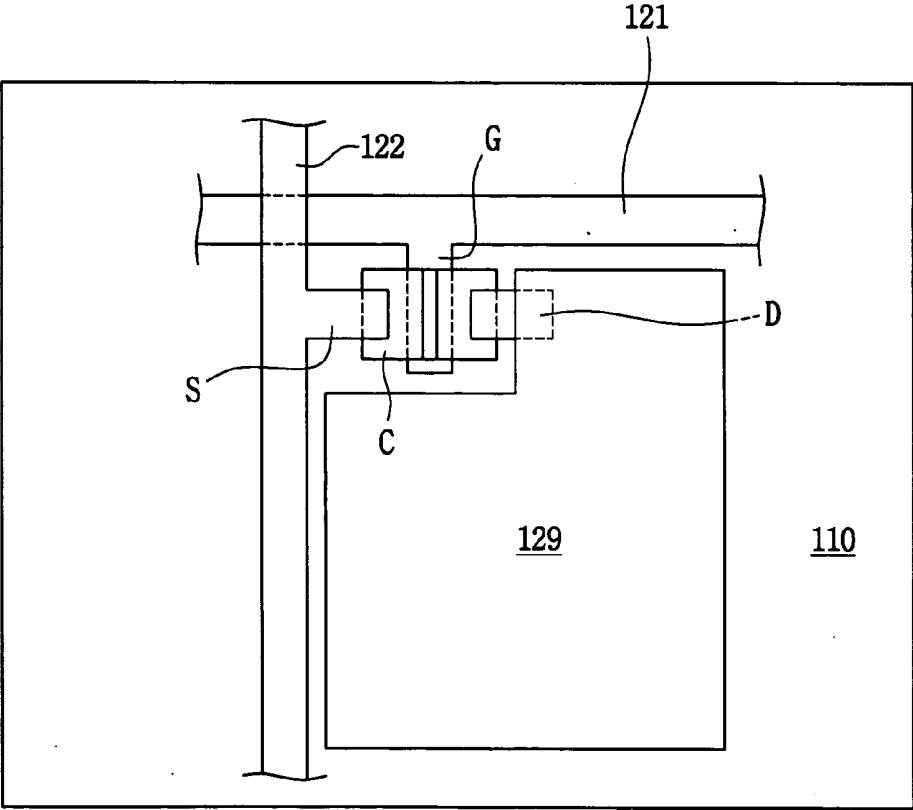


FIG. 9A

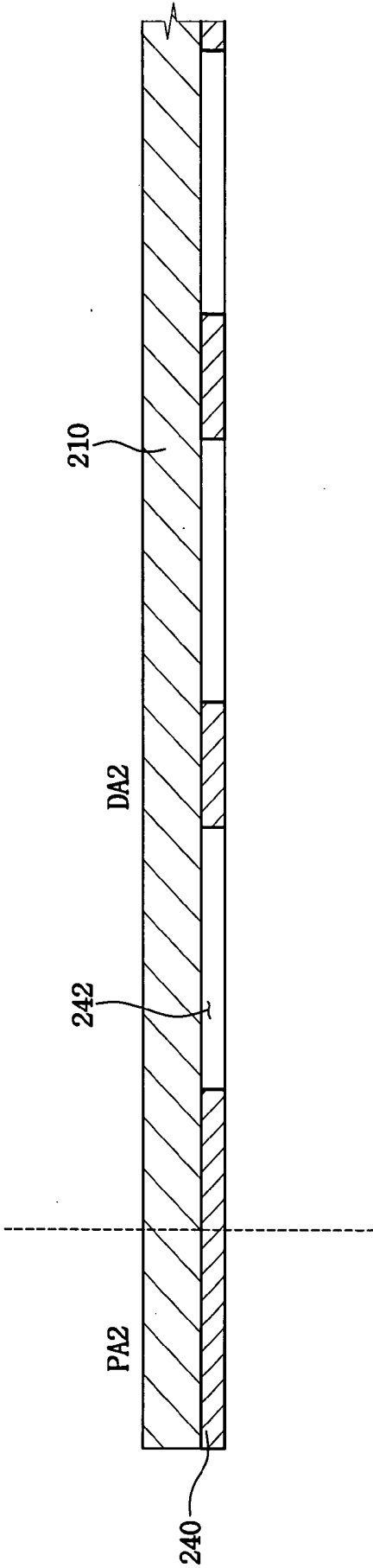


FIG. 9B

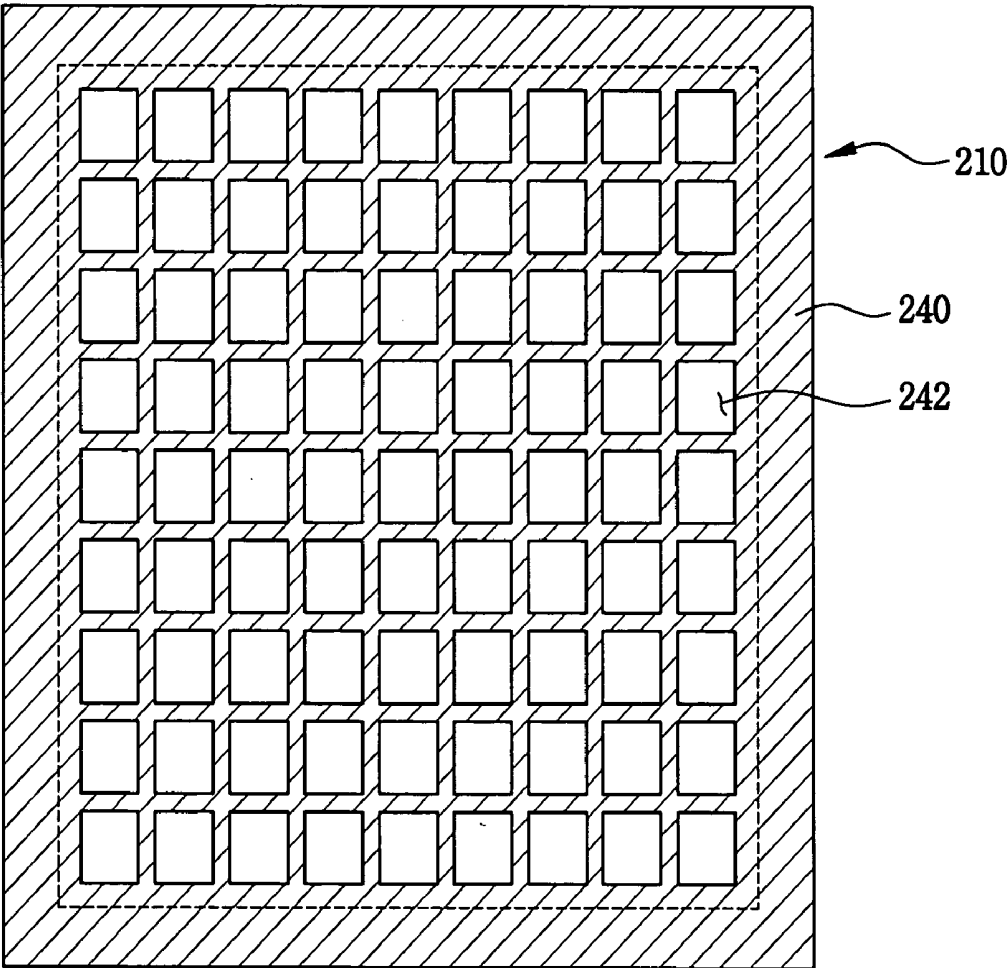


FIG. 10A

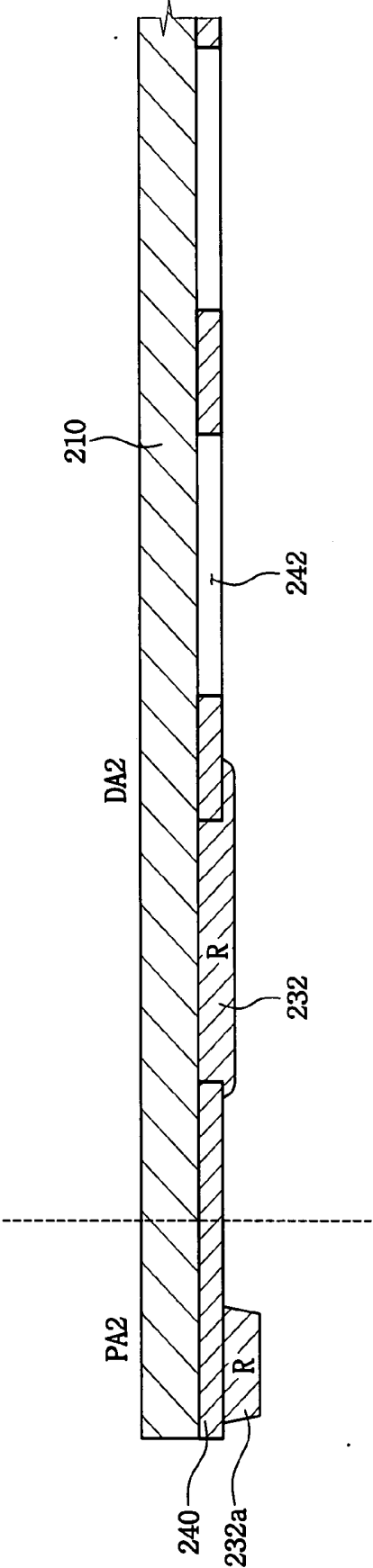


FIG. 10B

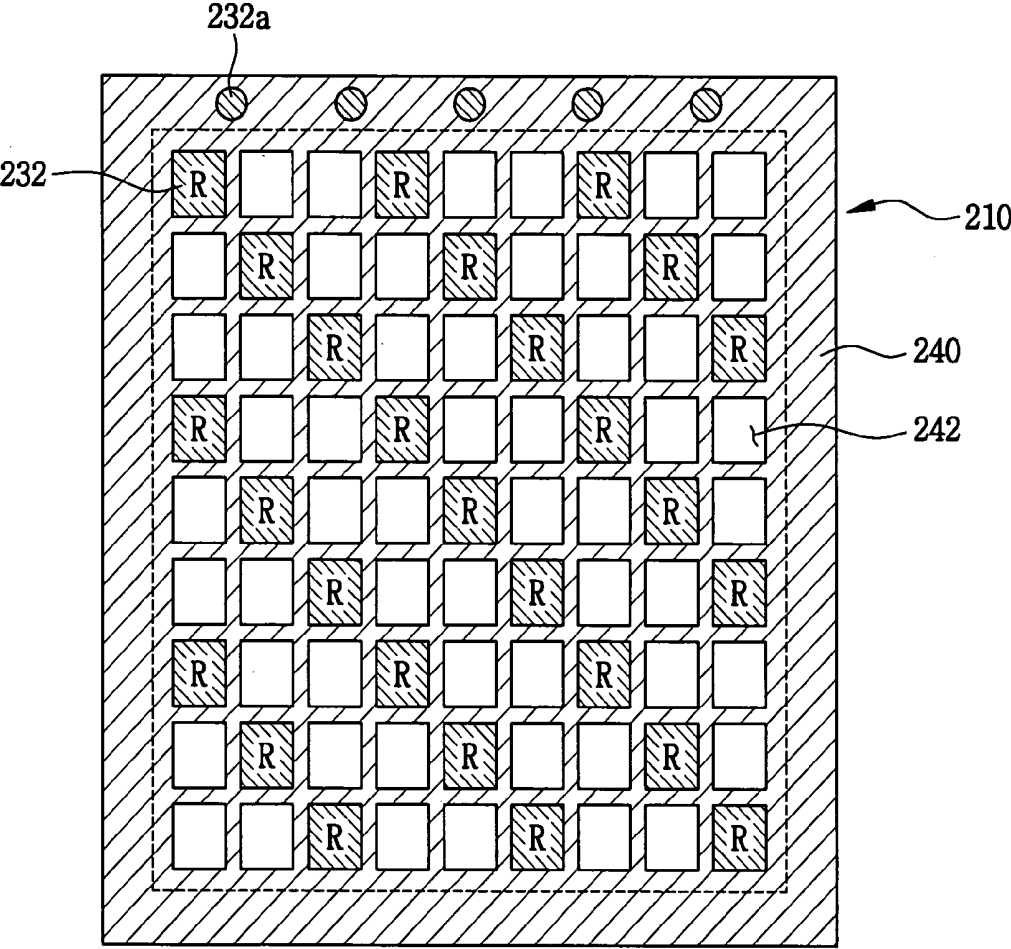


FIG. 11A

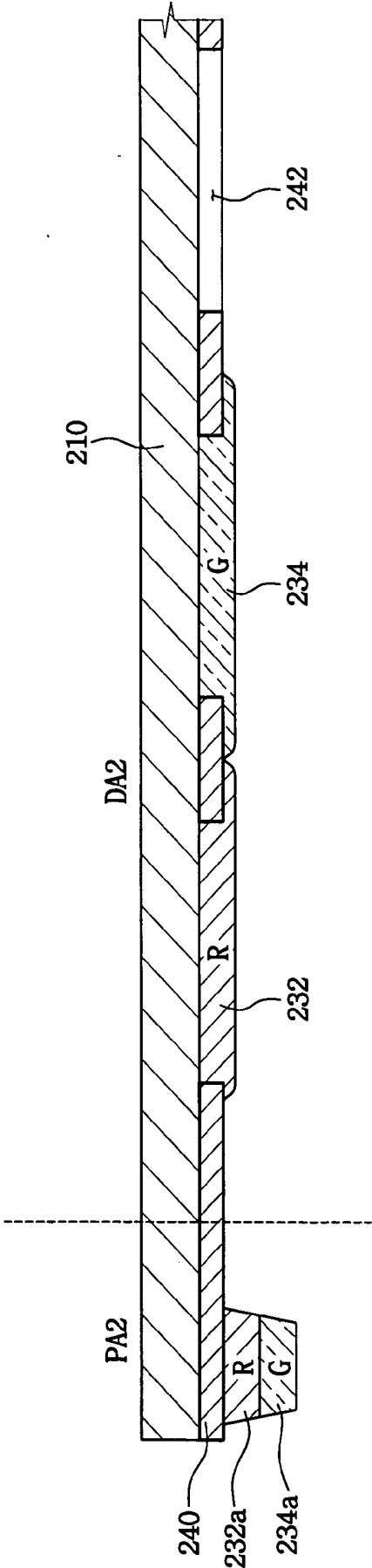


FIG. 11B

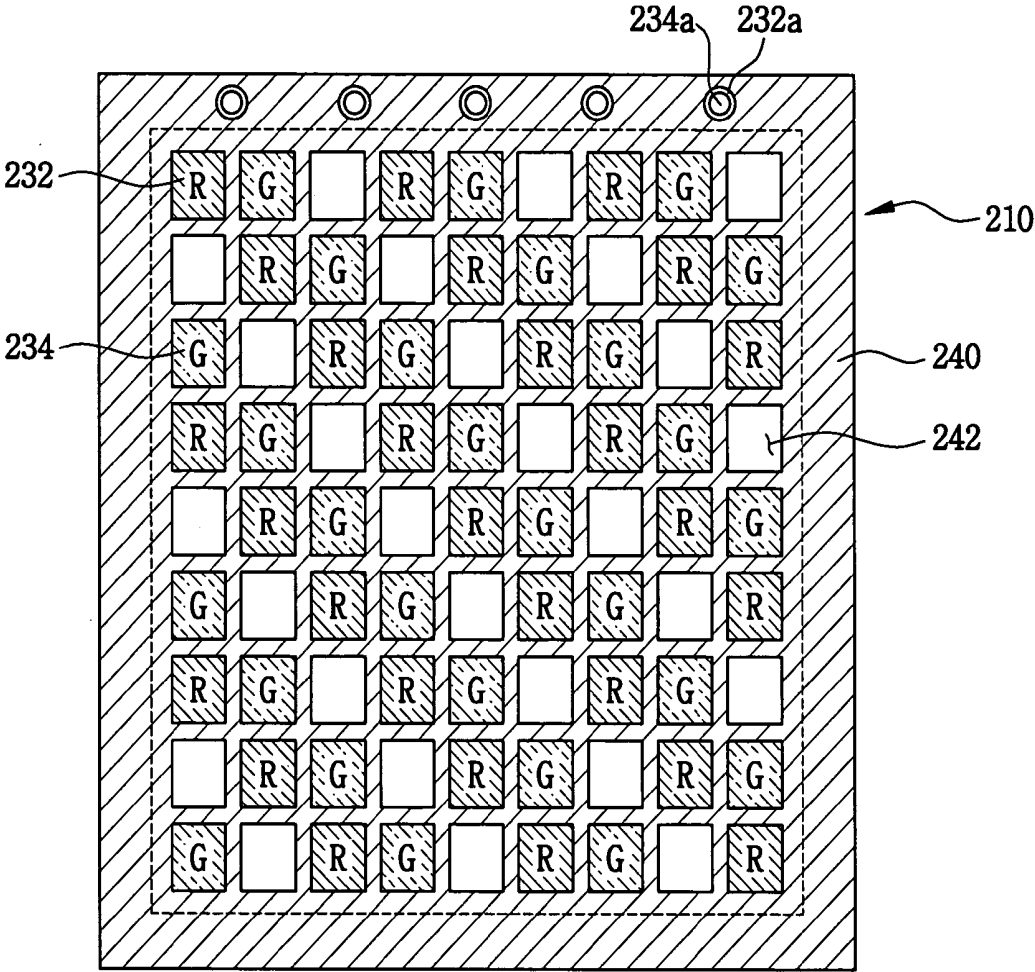


FIG. 12A

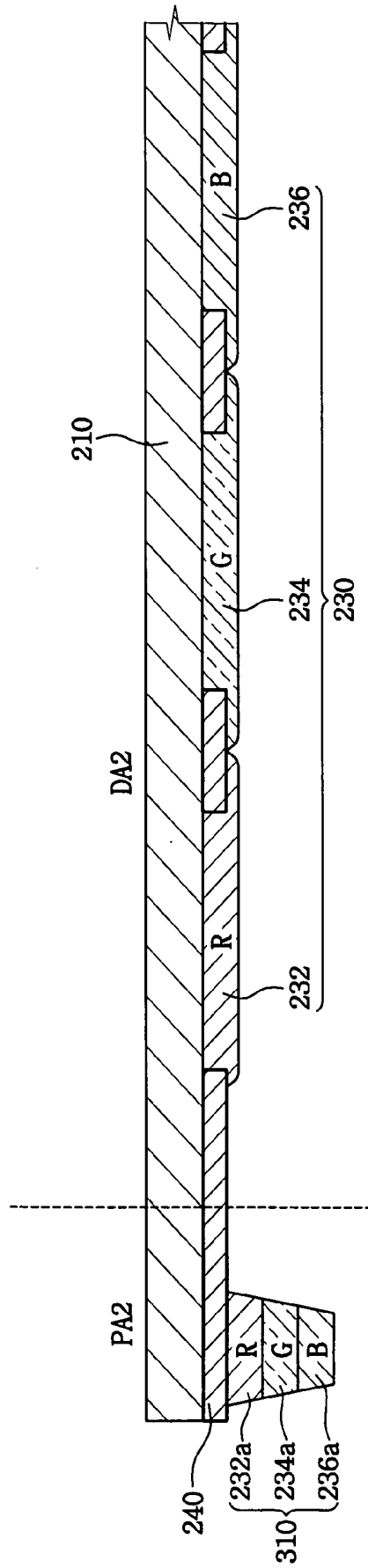


FIG. 12B

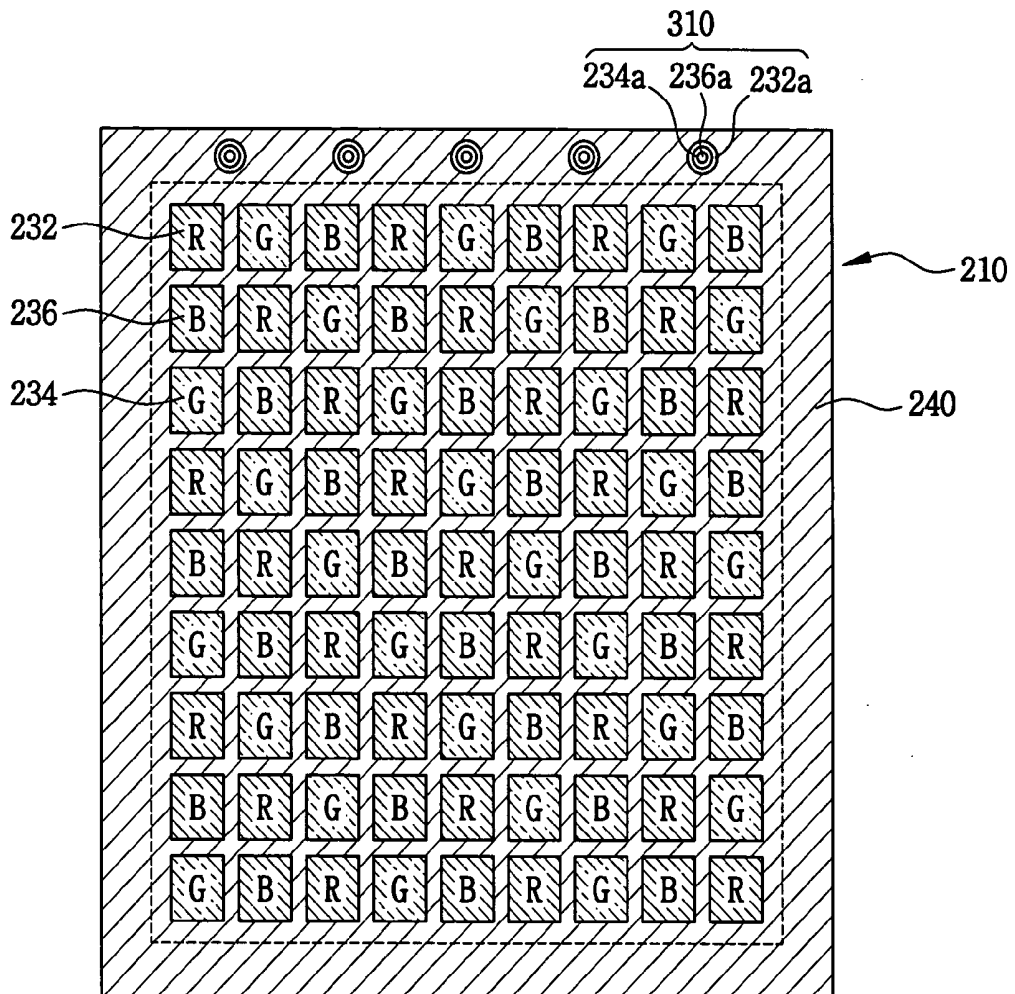




FIG. 14

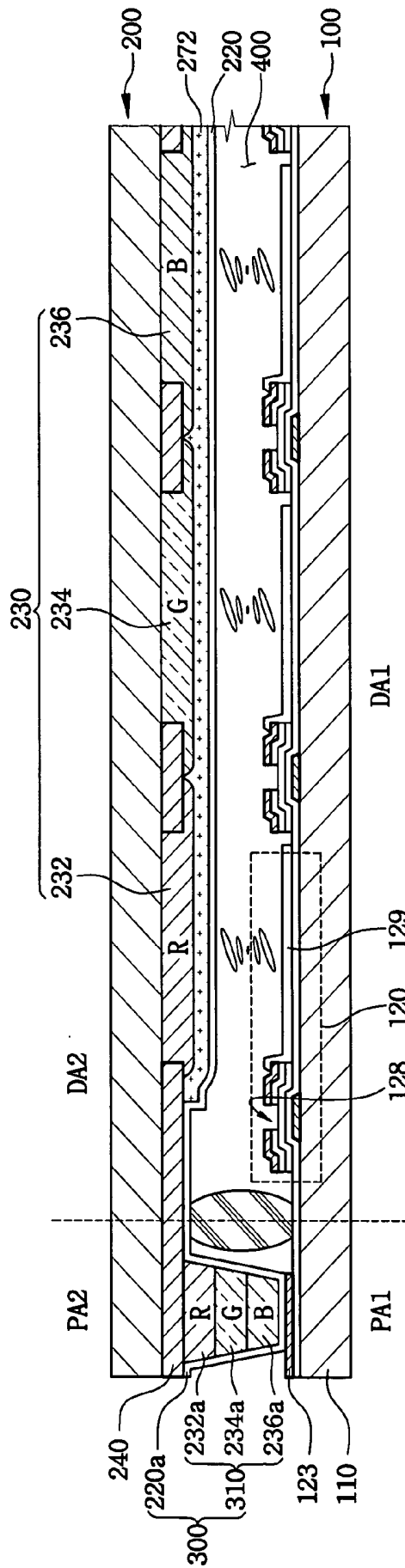


FIG. 15

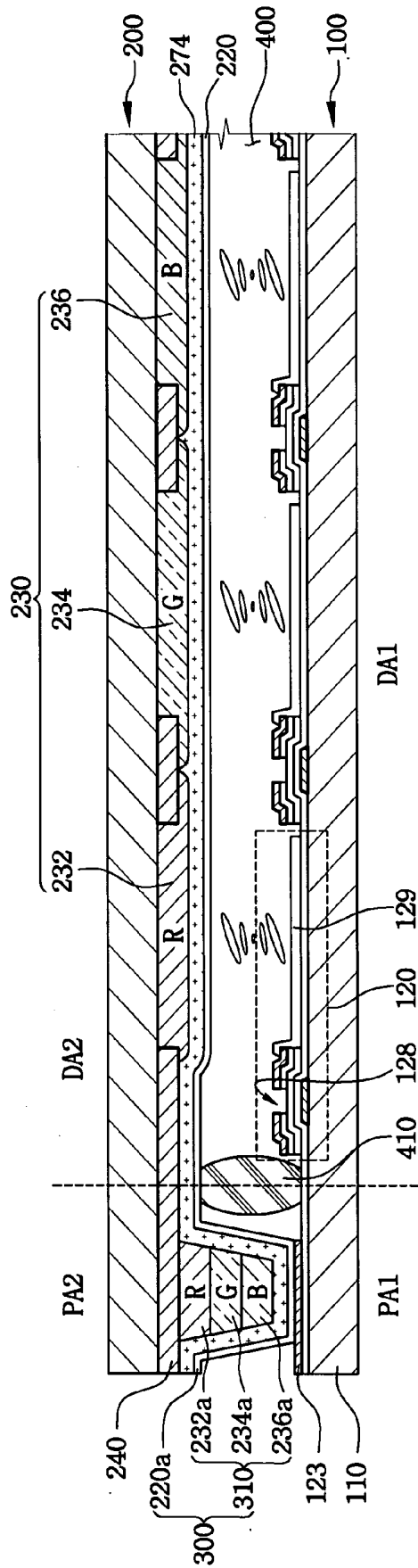


FIG. 16

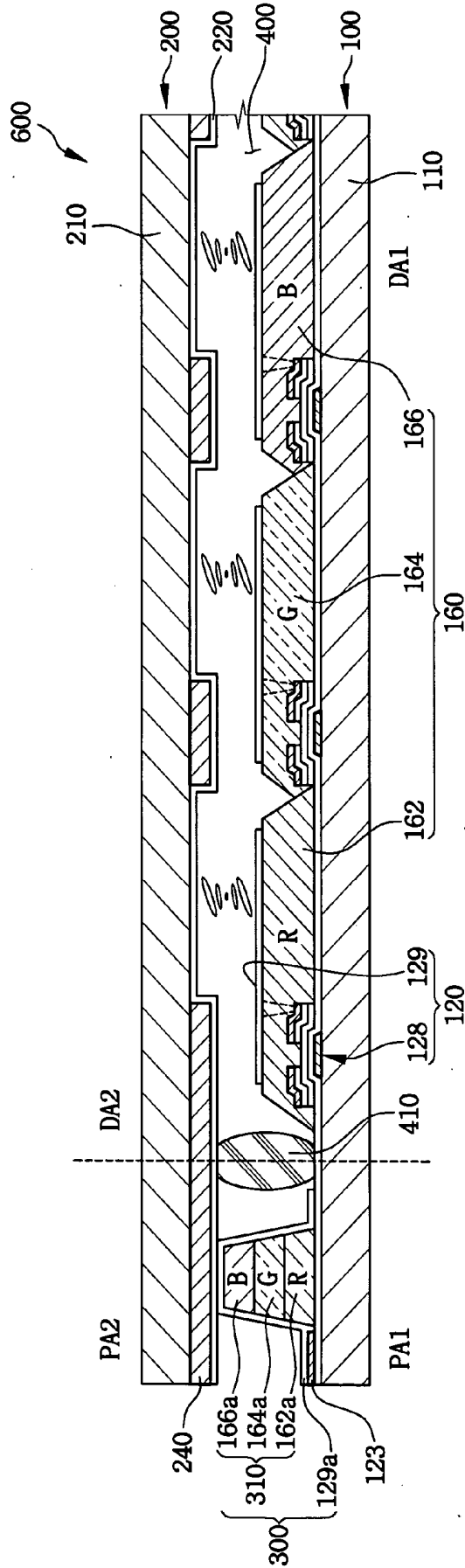


FIG. 17

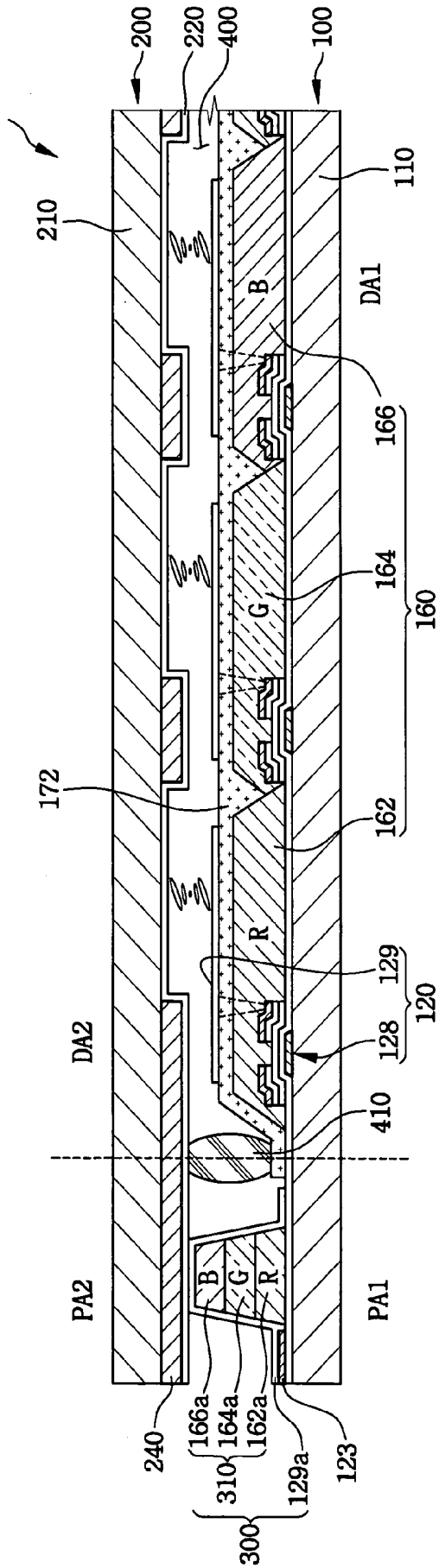




FIG. 19

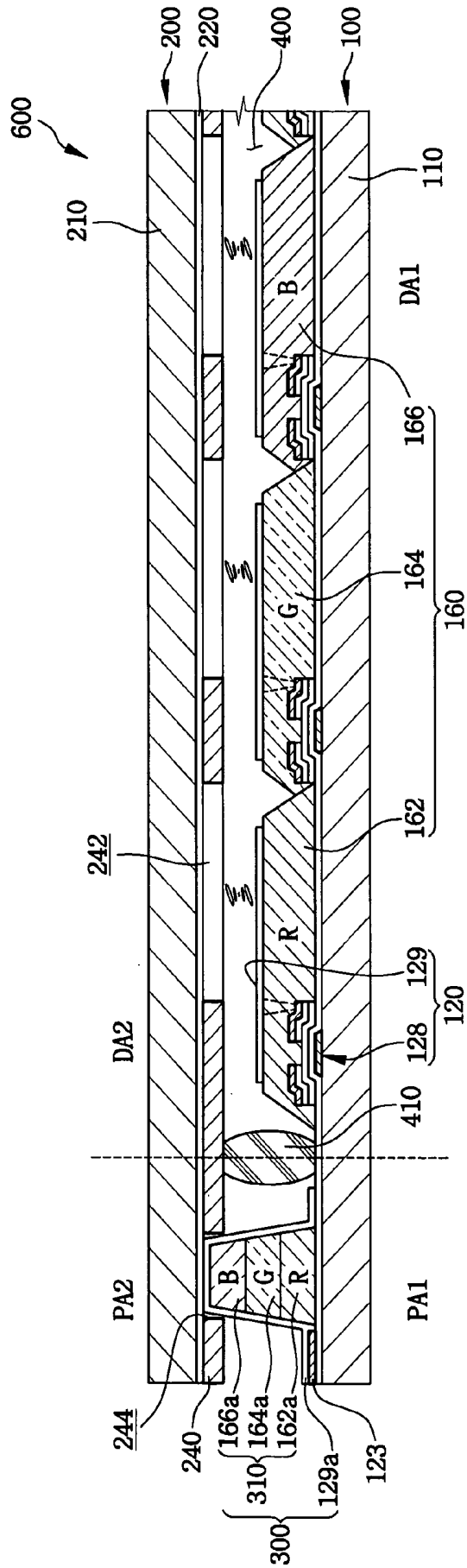


FIG. 20

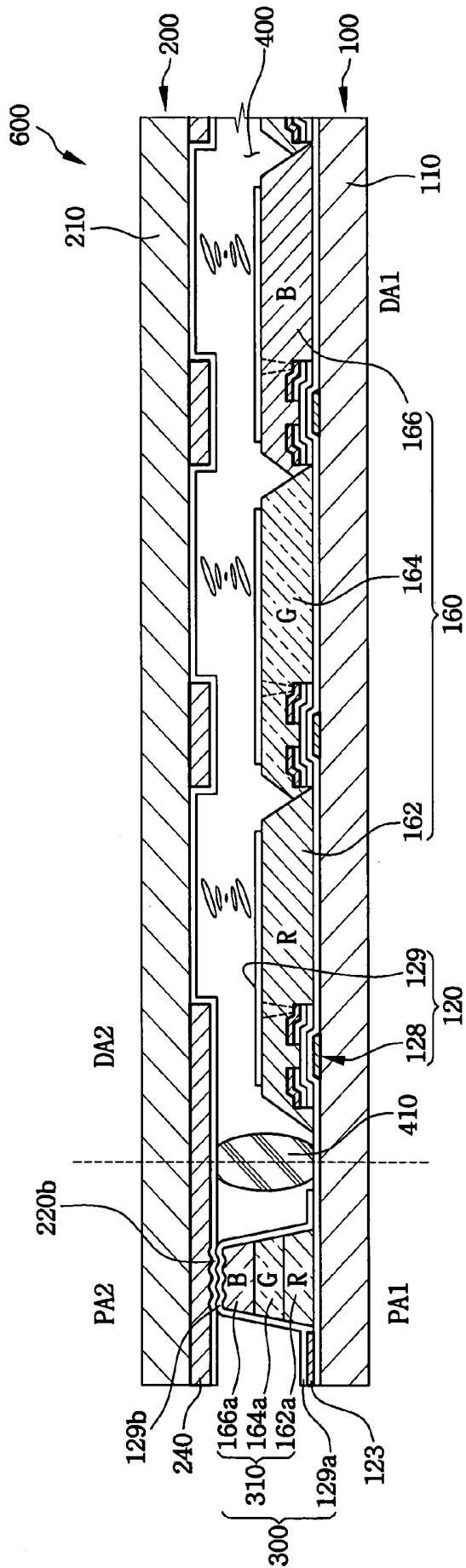
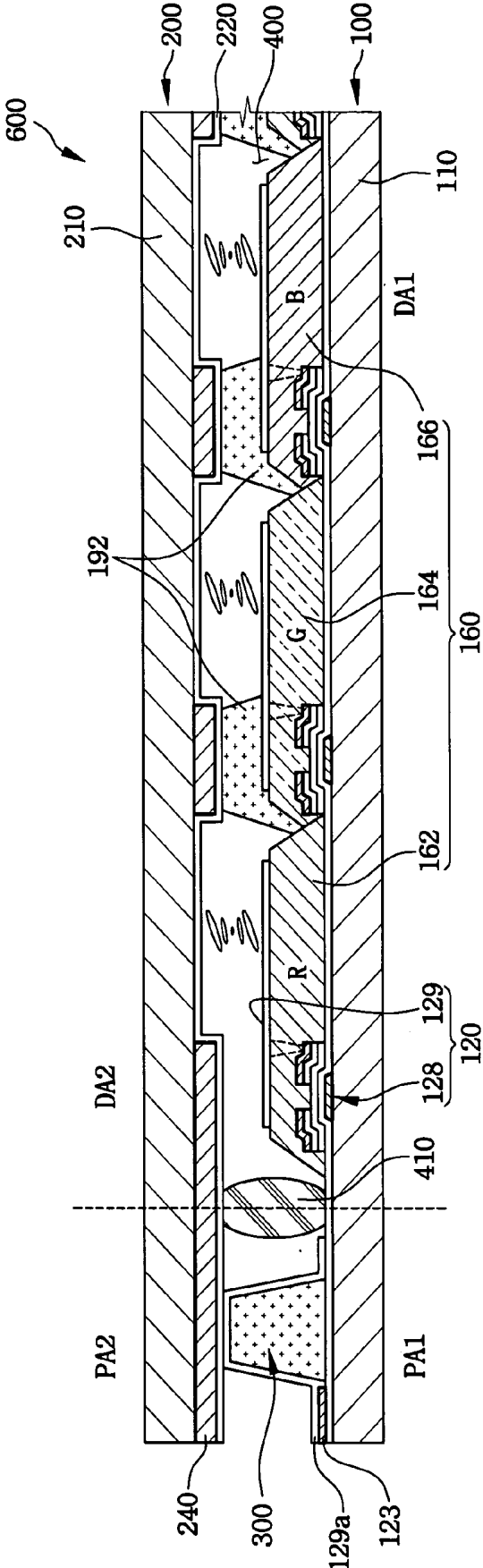


FIG. 21



## LIQUID CRYSTAL DISPLAY APPARATUS AND METHOD OF FORMING THE SAME

### CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claims priority based on Korean Patent Application No. 2003-30790 filed on May 15, 2003.

### BACKGROUND

[0002] 1. Technical Field

[0003] The present disclosure relates to a liquid crystal display (LCD) apparatus, and more particularly to an LCD apparatus manufactured by a simplified process and at low cost.

[0004] 2. Description of the Related Art

[0005] A conventional LCD apparatus displays an image using liquid crystal. In order to display the image, the conventional LCD apparatus includes an LCD panel and a backlight assembly for supplying light to the LCD panel. The LCD panel includes a first substrate, a second substrate and liquid crystal interposed between the first and second substrates.

[0006] The first substrate includes a plurality of first electrodes and the second substrate includes a second electrode facing the first electrodes. Each of the first electrodes receives a pixel voltage different from each other and the second electrode receives a common voltage having a uniform voltage level. The arrangement of the liquid crystal is varied according to an electric field applied between the first and second electrodes.

[0007] The pixel voltage and common voltage are applied to the first substrate. The pixel voltage is applied to the first electrodes and the common voltage applied to the first substrate is provided to the second substrate through a conductive spacer that is electrically connected between the first and second substrates. The conventional conductive spacer is typically formed of gold (Au) and is formed at many locations of the first substrate.

[0008] In a manufacturing processes for an LCD apparatus in which a common voltage from a first substrate is applied to a second substrate through a conductive spacer, much time is required to form the conductive spacer. For example, in a conventional manufacturing process, a common electrode and color filters are formed over a substrate of an LCD panel, and separate manufacturing steps are required to form the conductive spacer over the color filters and the common electrode. These additional manufacturing steps increase manufacturing time of the conventional LCD apparatus and require more raw materials. Also, because the conventional conductive spacer is made of gold (Au), manufacturing cost for the LCD apparatus may be high.

### SUMMARY OF THE INVENTION

[0009] According to various exemplary embodiments of the present invention, a common voltage applied to a first substrate of an LCD apparatus is provided to a common electrode formed over a second substrate through a conductor formed over an insulator. Thus, the manufacturing processes for the LCD apparatus may be simplified and the manufacturing cost for the LCD apparatus may be reduced.

[0010] A liquid crystal display device according to an embodiment of the invention includes a first substrate, a common electrode formed over the first substrate, and a second substrate disposed opposite the first substrate. A common voltage-applying member applies a common voltage to the common electrode and maintains a cell gap between the first substrate and the second substrate. The common voltage-applying member includes an insulator and a conductor formed over the insulator.

[0011] In at least one embodiment of the invention, a color filter is formed over one of the first and second substrates. The color filter and the insulator are formed simultaneously and of the same material.

[0012] In at least one embodiment of the invention, the common electrode and the conductor are formed simultaneously and of the same material.

[0013] In at least one embodiment of the invention, a plurality of pixel electrodes are formed over the second substrate, and the plurality of pixel electrodes and the conductor are formed simultaneously and of the same material.

[0014] A method of forming a liquid crystal display device according to an embodiment of the invention includes forming a common electrode over a first substrate, disposing a second substrate opposite the first substrate, forming an insulator over one of the first substrate and the second substrate, and forming a conductor over the insulator. The insulator and the conductor form a common voltage-applying member that applies a common voltage to the common electrode and that maintains a cell gap between the first substrate and the second substrate.

### BRIEF DESCRIPTION OF THE DRAWINGS

[0015] The present invention will become readily apparent by describing preferred embodiments thereof in detail with reference to the accompanying drawings wherein:

[0016] FIG. 1 is a schematic view of an LCD apparatus according to an exemplary embodiment of the present invention;

[0017] FIG. 2 is a schematic plan view of an LCD apparatus according to another exemplary embodiment of the present invention;

[0018] FIG. 3 is a partially enlarged view showing a portion "A" of FIG. 2;

[0019] FIG. 4 is a partially enlarged view showing a portion "B" of FIG. 2;

[0020] FIG. 5 is a cross-sectional view taken along the line V-V' of FIG. 2;

[0021] FIGS. 6A and 6B are schematic views showing a gate bus line and a gate electrode formed on the first transparent substrate according to an exemplary embodiment of the present invention;

[0022] FIGS. 7A and 7B are schematic views showing a channel layer formed on a first transparent substrate according to an exemplary embodiment of the present invention;

[0023] FIGS. 8A and 8B are schematic views showing a pixel electrode formed on a first transparent substrate according to an exemplary embodiment of the present invention;

[0024] FIGS. 9A and 9B are schematic views showing a black matrix formed on a second transparent substrate according to an exemplary embodiment of the present invention;

[0025] FIGS. 10A and 10B are schematic views showing a red color filter and a color filter layer formed on a second transparent substrate according to an exemplary embodiment of the present invention;

[0026] FIGS. 11A and 11B are schematic views showing a green color filter and a green color filter layer formed on a second transparent substrate according to an exemplary embodiment of the present invention;

[0027] FIGS. 12A and 12B are schematic views showing a blue color filter and a blue color filter layer formed on a second transparent substrate according to an exemplary embodiment of the present invention;

[0028] FIG. 13 is a cross-sectional view showing a common electrode and a conductor formed on a second transparent substrate according to an exemplary embodiment of the present invention;

[0029] FIG. 14 is a cross-sectional view of an LCD according to another exemplary embodiment of the present invention;

[0030] FIG. 15 is a cross-sectional view of an LCD according to another exemplary embodiment of the present invention;

[0031] FIG. 16 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention;

[0032] FIG. 17 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention;

[0033] FIG. 18 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention;

[0034] FIG. 19 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention;

[0035] FIG. 20 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention; and

[0036] FIG. 21 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention.

#### DESCRIPTION OF PREFERRED EMBODIMENTS

[0037] FIG. 1 is a schematic view showing an LCD apparatus according to an exemplary embodiment of the present invention.

[0038] Referring to FIG. 1, an LCD apparatus 600 according to the present embodiment of the invention includes a first substrate 100, a second substrate 200, a common voltage-applying member 300 and liquid crystal 400. The first substrate 100 faces the second substrate 200, and the

common voltage-applying member 300 and the liquid crystal 400 are disposed between the first and second substrates 100 and 200.

[0039] The first substrate 100 includes a first transparent substrate 110 and a plurality of pixels 120 on the first transparent substrate 110. The pixels 120 are formed on a first display area DA1 of the first substrate 110 in a matrix configuration, and the first display area DA1 is surrounded by a first peripheral area PA1. Each of the pixels 120 includes a pixel electrode 129 to which a pixel voltage is applied. The first transparent substrate 110 includes a pixel voltage applying line for applying a pixel voltage from the first peripheral area PA1 to the first display area DA1 and a common voltage applying line 190 for applying a common voltage to the first peripheral area PA1.

[0040] The second substrate 200 includes a second transparent substrate 210 and a common electrode 220 formed on the second transparent substrate. The second substrate 200 includes a second display area DA2 facing the first display area DA1 of the first substrate 100 and a second peripheral area PA2 surrounding the second display area DA2. The common electrode 220 is formed at the second display area DA2 and second peripheral area PA2 so as to partially cover the second display area DA2 and second peripheral area PA2. The common electrode 220 receives the common voltage applied to the common voltage applying line 190 of the first substrate 100.

[0041] The first and second substrates 100 and 200 are disposed such that the pixel electrodes 120 face the common electrode 220.

[0042] The common voltage-applying member 300 is disposed between the first and second substrates 100 and 200 and applies the common voltage applied to the first peripheral area PA1 of the first substrate 100 to the common electrode 220 of the second substrate 200. The common voltage-applying member 300 is disposed between the first peripheral area PA1 of the first substrate 100 and the second peripheral area PA2 of the second substrate 200. In various exemplary embodiments of the invention, the common voltage-applying member 300 may be formed at the first peripheral area PA1 or the second peripheral area PA2. The common voltage-applying member 300 includes an insulator 350 and a conductor 390 wrapping the insulator 350. The insulator 350 includes a photosensitive material and is formed during fabrication of the first and second substrates 100 and 200. The conductor 390 is formed on a surface of the insulator 350 during formation of the common electrode 220 or the pixel electrode 129.

[0043] The liquid crystal 400 is disposed between the pixels 120 of the first substrate 100 and the common electrode 220 of the second substrate 200. The liquid crystal 400 has a varying arrangement due to an electric field difference between the pixel voltage applied to the pixel electrode 129 of the first substrate 100 and the common voltage applied to the common electrode 220 through the common voltage applying member 300.

[0044] The LCD apparatus 600 according to the present embodiment of the invention may be manufactured to have a structure that can apply the common voltage applied to the first substrate 100 to the second substrate 200 without any additional process steps. Thus, the manufacturing processes

for the LCD apparatus **600** may be simplified. Also, the LCD apparatus **600** may be manufactured at a lower cost because it does not use a spacer made of gold (Au).

[0045] FIG. 2 is a schematic plan view showing an LCD apparatus according to another exemplary embodiment of the present invention. FIG. 3 is a partially enlarged view showing a portion "A" of FIG. 2. FIG. 4 is a partially enlarged view showing a portion "B" of FIG. 2. FIG. 5 is a cross-sectional view taken along the line V-V of FIG. 2.

[0046] Referring to FIG. 2, an LCD apparatus **600** includes a first substrate **100**, a second substrate **200**, a power supply member **300** and liquid crystal **400**.

[0047] Referring to FIG. 5, the first substrate **100** includes a first transparent substrate **110** having a first display area **DA1** and a first peripheral area **PA1** surrounding the first display area **DA1** and a plurality of pixels **120** disposed at the first display area **DA1**.

[0048] Referring to FIG. 3, the pixels **120** are arranged at the first display area **DA1** in a matrix configuration. For example, when the LCD apparatus **600** has a resolution of 1024×768, 1024×768×3 units of the pixels **120** units are formed at the first transparent substrate **110**.

[0049] Each of the pixels **120** includes a gate bus line **121**, a data bus line **122**, a thin film transistor (TFT) **128** and a pixel electrode **129**.

[0050] Referring to FIGS. 2 and 3, the gate bus line **121** is extended in a first direction **D1** from the first peripheral area **PA1** through the first display area **DA1**. If the LCD apparatus **600** has a resolution of 1024×768, 768 gate bus lines **121** are formed at the first transparent substrate **110**. The 768 gate bus lines **121** are divided into three groups and each group is disposed at the first peripheral area **PA1** as a gate bus line channel **121a**. Thus, three gate bus line channels **121a** are formed at the first peripheral area **PA1**. Each of the gate bus line channels **121a** is connected to a gate tape carrier package (TCP) to which a driving signal is applied.

[0051] The data bus line **122** is insulated from the gate bus line **121** and extended in a second direction **D2** substantially perpendicular to the first direction **D1**. The data bus line **122** is extended from the first peripheral area **PA1** to the first display area **DA1**. If the LCD apparatus **600** has a resolution of 1024×768, 1024×3 units of data bus lines **122** are formed at the first transparent substrate **110**. The 1024×3 units of data bus lines **122** are divided into twelve groups and each group is disposed at the first peripheral area **PA1** as a data bus line channel **122a**. Thus, twelve data bus line channels **122a** are formed at the first peripheral area **PA1**. Each of the data bus line channels **122a** is connected to a data TCP to which a driving signal is applied.

[0052] Referring to FIG. 3, the TFT **128** includes a gate electrode **G**, a source electrode **S**, a drain electrode **D** and a channel layer **C**. The gate and source electrodes **G** and **S** are extended from the gate and data bus lines **121** and **122**, respectively. The channel layer **C** is insulated from the gate electrode **G** and disposed on the gate electrode **G**. When a voltage is applied to the gate electrode **G**, the channel layer **C** operates as a conductor. When the voltage is not applied to the gate electrode **G**, the channel layer **C** operates as an insulator. The source and drain electrodes **S** and **D** are

electrically insulated from the channel layer **C** and connected to the channel layer **C**.

[0053] The pixel electrode **129** is connected to the drain electrode **D** of the TFT **128**. The pixel electrode **129** comprises a transparent conductive material, such as, for example, indium tin oxide, indium zinc oxide, etc.

[0054] Referring to FIG. 4, a common voltage applying line **123** is formed at a position adjacent to the data bus line channel **122a** of the first peripheral area **PA1** of the first substrate **100**. The common voltage applying line **123** receives the common voltage externally provided through the first peripheral area **PA1** of the first substrate **100**. A plurality of common voltage applying lines is formed at the first peripheral area **PA1** so as to apply the common voltage having a uniform level to the common electrode **220** shown in FIG. 5.

[0055] Referring to FIGS. 2 and 5, the second substrate **200** includes a second transparent substrate **210**, a color filter **230**, a common electrode **220** and a power-applying member **300**. The second substrate **200** may further include a black matrix **240**.

[0056] The second transparent substrate **210** faces the first transparent substrate **110** and includes a second display area **DA2** and a second peripheral area **PA2** surrounding the second display area **DA2**. The second display area **DA2** and second peripheral area **PA2** face the first display area **DA1** and first peripheral area **PA1** of the first substrate **100**, respectively.

[0057] The color filter **230** is disposed at the second display area **DA2** of the second transparent substrate **210** in a matrix configuration, thereby facing the pixel electrode **129**. The color filter **230** includes a red color filter **232**, a green color filter **234** and a blue color filter **236**. Two color filters adjacent to each other among the red, green and blue color filters **232**, **234** and **236** may be overlapped at edges thereof. Thus, the red, green and blue color filters **232**, **234** and **236** may prevent light from leaking between the red, green and blue color filters **232**, **234** and **236**.

[0058] In the present exemplary embodiment of the invention, the black matrix **240** is formed between the first transparent substrate **210** and common electrode **220**. The black matrix **240** has a lattice-shape so as to block a space between the pixel electrode **129** and an adjacent pixel electrode. The black matrix **240** preferably includes a chromium layer, a double-layer having a chromium layer and a chromium oxide layer, or a black organic layer having a light transmittance substantially equal to that of chromium **Cr**.

[0059] The common electrode **220** includes a transparent conductive material, such as, for example, ITO, IZO or the like. The common electrode **220** is formed over the second display area **DA2** and on a portion of the second peripheral area **PA2** so as to cover the color filter **230**.

[0060] Referring to FIG. 5, the power-applying member **300** includes an insulator **310** and a conductor **220a**.

[0061] The insulator **310** is formed at the second peripheral area **PA2** of the second substrate **200**. The insulator **310** faces the common voltage applying line **123** formed at the first peripheral area **PA1** of the first substrate **100**. In the present exemplary embodiment of the invention, the insulator **310** is formed using portions of color filter layers used

for the color filter **230**. The color filter layers include a red color filter layer **232a**, a green color filter layer **234a** and a blue color filter layer **236a**. The red, green and blue color filter layers **232a**, **234a** and **236a** formed at the second peripheral area **PA2** are formed at the same time the color filter **230** is formed at the second display area **DA2**. Particularly, the red color filter layer **232a** is formed with the red color filter **232** formed at the second display area **DA2**, the green color filter layer **234a** is formed on the red color filter layer **232a** with the green color filter **234** formed at the second display area **DA2**, and the blue color filter layer **236a** is formed on the green color filter layer **234a** with the blue color filter **236** formed at the second display area **DA2**.

[0062] The insulator **310** having the red, green and blue color filter layers **232a**, **234a** and **236a** has a thickness substantially equal to a distance between the first and second substrates **100** and **200**.

[0063] The conductor **220a** covers the insulator **310** formed at the second peripheral area **PA2**. The conductor **220a** is partially connected to the common electrode **220** and common voltage applying line **123**. In the present exemplary embodiment of the invention, the conductor **220a** includes a transparent conductive material, such as, for example, ITO or IZO. The conductor **220a** may be formed using the same material as the common electrode **220**, so that the conductor **220a** may be simultaneously formed with the common electrode **220**.

[0064] A sealing member **410** having a band shape is formed at a boundary between the first peripheral area **PA1** and first display area **DA1** of the first substrate **100** or at a boundary between the second peripheral area **PA2** and second display area **DA2** of the second substrate **200**. The liquid crystal **400** is dropped into the first display area **DA1** or second display area **DA2** surrounded by the sealing member **410**, and then the first substrate **100** is assembled with the second substrate **200**. Alternatively, the liquid crystal **400** may be injected between the first and second substrates **100** and **200** after the first and second substrates **100** and **200** are assembled with each other using the sealing member **410**.

[0065] FIGS. 6A-13 are cross sectional views showing steps of a method for forming a liquid crystal display apparatus according to an embodiment of the invention. Referring to FIGS. 6A and 6B, a gate metal, such as, for example, aluminum, aluminum alloy, etc., is formed over the first transparent substrate **110** of the first substrate **100** using chemical vapor deposition or sputtering.

[0066] A photoresist layer is formed on the first transparent substrate **110** and the photoresist layer is partially exposed to light using a first mask. As a result, the gate bus line **121** is formed on the first transparent substrate **110** in the first direction **D1** and the gate electrode **G** extending from the gate bus line **121** in the second direction **D2** is formed as shown in FIGS. 2 and 6B. When the LCD apparatus **600** has a resolution of 1024×768, 768 gate bus lines **121** are formed on the first transparent substrate **110** and 1024×3 units of gate electrodes **G** extend from one gate bus line **121**.

[0067] After the gate bus line **121** and gate electrode **G** are formed on the first transparent substrate **110**, a gate-insulating layer **127** is formed over the first transparent substrate **110**.

[0068] Referring to FIGS. 7A and 7B, an amorphous silicon layer, an n<sup>+</sup> amorphous silicon layer and a source/drain metal layer are successively formed on the gate-insulating layer **127**. A photoresist layer is formed on the source/drain metal layer and patterned using a second mask, thereby forming a photoresist pattern at a position corresponding to the channel layer **C**. The source/drain metal layer, n<sup>+</sup> amorphous silicon layer and amorphous silicon layer are successively etched using the photoresist pattern as a mask. As a result, the data bus line **122** extended in the second direction **D2**, source electrode **S** extended from the data bus line **122** in the first direction **D1** and drain electrode **D** extended from the data bus line **122** in the first direction **D1** are simultaneously formed. When the photoresist pattern is etched back, the n<sup>+</sup> amorphous silicon layer is divided to form the channel layer **C**.

[0069] Referring to FIGS. 8A and 8B, a transparent protection layer **128** is formed over the first transparent substrate **110** and a photoresist layer is formed on the protection layer **128**. A contact hole is formed in the protection layer **128** to partially expose the drain electrode **D** by etching the protection layer **128** using a third mask. A transparent conductive material, such as, for example, ITO or IZO, is formed over the protection layer **128**. The pixel electrode **129** is formed by patterning the transparent conductive material using a fourth mask.

[0070] Referring to FIGS. 9A and 9B, a black layer, such as, for example, a chromium layer, a double-layer having a chromium layer and a chromium oxide layer or a black organic layer, is formed over the second transparent substrate **210**. A photoresist layer is coated on the black layer and patterned, and the patterned photoresist layer is used to form an opening **242** in the black layer corresponding to the pixel electrode **129** of the first substrate **100**. As a result, the black matrix **240** is formed at the second display area **DA2** of the second transparent substrate **210** as shown in FIG. 9B.

[0071] Referring to FIGS. 10A and 10B, a red color filter material is formed over the second transparent substrate **210**. The red color filter material is patterned through a photolithography process to form the red color filter layer **232a** at the second peripheral area **PA2** and to form the red color filter **232** at the opening **242** of the black matrix **240**.

[0072] Referring to FIGS. 11A and 11B, a green color filter material is formed over the second transparent substrate **210**. The green color filter material is patterned through a photolithography process to form the green color filter layer **234a** on the red color filter layer **232a** formed at the second peripheral area **PA2** and to form the green color filter **234** at the opening **242** of the black matrix **240**.

[0073] Referring to FIGS. 12A and 12B, a blue color filter material is formed over the second transparent substrate **210**. The blue color filter material is patterned through a photolithography process to form the blue color filter layer **236a** on the green color filter layer **234a** formed at the second peripheral area **PA2** and to form the blue color filter **236** at the opening **242** of the black matrix **240**.

[0074] The red, green and blue color filter layers **232a**, **234a** and **236a** formed at the second peripheral area **PA2** of the second transparent substrate **210** operate as the insulator **310**. As shown in FIGS. 12a and 12b, the insulator **310** has a nub-like shape having a base portion formed of the red

color filter layer **232a** and a top portion formed of the blue color filter layer **236a**. The red color filter layer **232a** has larger cross sectional area than that of the blue color filter **236a**, so that the insulator **310** is tapered from the base portion to the top portion.

[0075] Referring to FIG. 13, a transparent conductive layer, such as, for example, ITO or IZO, is formed over the second transparent substrate **210** to cover the red, green and blue color filters **232**, **234** and **236**. The transparent conductive layer may be formed only at the second display area DA2 of the second transparent substrate **210**, or formed to cover the insulator **310** formed at the second peripheral area PA2 and the color filter **230** formed at the second display area DA2 of the second transparent substrate **210**.

[0076] In present exemplary embodiment of the invention, the transparent conductive layer covers the insulator **310** formed at the second peripheral area PA2 and the color filter **230** formed at the second display area DA2 of the second transparent substrate **210**. A portion of the transparent conductive layer, which covers the color filter **230** of the second display area DA2, operates as the common electrode **220**, and a portion of the transparent conductive layer, which covers the insulator **310** of the second peripheral area PA2 operates as the conductor **220a**.

[0077] The first substrate **100** is assembled with the second substrate **200** as shown in FIG. 5. The conductor **220a** of the second substrate **200**, which covers the insulator **310**, is aligned to be connected to the common voltage applying line **123** of the first substrate **100**. The color filter **230** of the second substrate **200** is aligned with the pixel electrode **129** of the first substrate **100**. To assemble the first and second substrates **100** and **200** with each other, the sealing member **410** is formed at the boundary between the first peripheral area PA1 and first display area DA1 of the first substrate **100** or at a boundary between the second peripheral area PA2 and second display area DA2 of the second substrate **200**. Then, the liquid crystal **400** is injected between the first and second substrates **100** and **200** after or before assembling the first and second substrates **100** and **200** with each other.

[0078] As shown in FIG. 2, a driving module is assembled with the gate and data bus lines **121** and **122** after injecting the liquid crystal **400** between the first and second substrates **100** and **200**, thereby forming the LCD apparatus **600**.

[0079] According to this exemplary embodiment of the invention, the insulator **310** and conductor **220a** are formed at the second peripheral area PA2 of the second substrate **200** at the same time the color filter **230** is formed at the second display area PA2 of the second substrate **200** to apply the common voltage applied to the first substrate to the common electrode **220** through the conductor **220a**.

[0080] FIG. 14 is a cross-sectional view of an LCD according to another exemplary embodiment of the present invention. In FIG. 14, the same reference numerals denote the same elements in FIGS. 2 to 13, and thus the detailed descriptions of the same elements will be omitted.

[0081] Referring to FIG. 14, a transparent planarizing layer **272** is formed over a second transparent substrate **210** of the second substrate **200** to improve step coverage of a color filter **230** formed at a second substrate **200**. A portion of the planarizing layer **272** that covers an insulator **310** formed at a second peripheral area PA2 is removed by a photolithography process.

[0082] According to the present exemplary embodiment of the invention, the insulator **310** and conductor **220a** are formed at the second peripheral area PA2 of the second substrate **200** and the color filter **230** is formed at the second display area PA2 of the second substrate **200** to provide the common voltage applied to the first substrate to the common electrode **220** through the conductor **220a**. Thus, the manufacturing processes for the LCD apparatus **600** is simplified and the LCD apparatus **600** may be manufactured at a lower cost since the LCD apparatus does not use a gold (Au) spacer. Also, the step coverage of the color filter **230** and the common electrode **220** of the LCD apparatus **600** is improved.

[0083] FIG. 15 is a cross-sectional view of an LCD according to another exemplary embodiment of the present invention. In FIG. 15, the same reference numerals denote the same elements in FIGS. 2 to 13, and thus the detailed descriptions of the same elements will be omitted.

[0084] Referring to FIG. 15, a transparent planarizing layer **272** is formed over a second transparent substrate **210** of the second substrate **200** to improve step coverage of a color filter **230** formed at a second substrate **200**. When the combined thickness of a conductor **220a** and an insulator **310** is smaller than a cell gap between the first and second substrates **100** and **200**, the planarizing layer **272** may be formed over the color filter **230** and insulator **310**. In this exemplary embodiment of the invention, the planarizing layer **272** is formed between the conductor **220a** and insulator **310**, so that the conductor **220a** may electrically connect a common voltage applying line **123** of the first substrate **100** to the common electrode **220** of the second substrate **200**.

[0085] According to this exemplary embodiment of the invention, the insulator **310** and conductor **220a** are formed at the second peripheral area PA2 of the second substrate **200** and the color filter **230** is formed at the second display area PA2 of the second substrate **200** to provide the common voltage applied to the first substrate to the common electrode **220** through the conductor **220a**. Thus, the manufacturing processes for the LCD apparatus **600** is simplified and the LCD apparatus **600** may be manufactured at a lower cost since the LCD apparatus does not use a gold (Au) spacer. Also, the step coverage of the color filter **230** and common electrode **220** of the LCD apparatus **600** is improved. Furthermore, the planarizing layer **272** is formed between the conductor **220a** and insulator **310**, thereby electrically connecting the common voltage applying line **123** of the first substrate **100** to the common electrode **220** of the second substrate **200**.

[0086] FIG. 16 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention.

[0087] Referring to FIG. 16, an LCD apparatus **600** includes a first substrate **100**, a second substrate **200**, a power-applying member **300** and liquid crystal **400**.

[0088] The first substrate **100** includes a first transparent substrate **110** having a first display area DA1 and a first peripheral area PA1 surrounding the first display area DA1, a plurality of pixels **120**, a color filter **160** and the power-applying member **300**.

[0089] The pixels **120** and color filter **160** are placed in the first display area **DA1**, and the power-applying member **300** is placed in the first peripheral area **PA1**.

[0090] Referring to **FIGS. 2, 3** and **16**, the pixels **120** are arranged in the first display area **DA1** in a matrix configuration. For example, when the LCD apparatus **600** has a resolution of  $1024 \times 768$ ,  $1024 \times 768 \times 3$  units of the pixels **120** are formed on the first transparent substrate **110**.

[0091] Each of the pixels **120** includes a gate bus line **121**, a data bus line **122**, a thin film transistor (TFT) **128** and a pixel electrode **129**.

[0092] The gate bus line **121** is extended in a first direction **D1** from the first peripheral area **PA1** through the first display area **DA1**. If the LCD apparatus **600** has a resolution of  $1024 \times 768$ , 768 gate bus lines **121** are formed on the first transparent substrate **110**. The 768 gate bus lines **121** are divided into three groups and disposed in the first peripheral area **PA1**, and each of the three groups forms a gate bus line channel **121a**. Thus, three gate bus line channels **121a** are formed in the first peripheral area **PA1**. Each of the gate bus line channels **121a** is connected to a gate TCP to which a driving signal is applied.

[0093] The data bus line **122** is insulated from the gate bus line **121** and extended in a second direction **D2** substantially perpendicular to the first direction **D1**. The data bus line **122** is extended from the first peripheral area **PA1** to the first display area **DA1**. If the LCD apparatus **600** has a resolution of  $1024 \times 768$ ,  $1024 \times 3$  units of data bus lines **122** are formed on the first transparent substrate **110**. The  $1024 \times 3$  units of data bus lines **122** are divided into twelve groups and disposed at the first peripheral area **PA1**, and each of the twelve groups form a data bus line channel **122a**. Thus, twelve data bus line channels **122a** are formed in the first peripheral area **PA1**. Each of the data bus line channels **122a** is connected to a data TCP to which a driving signal is applied.

[0094] As shown in **FIG. 3**, the TFT **128** includes a gate electrode **G**, a source electrode **S**, a drain electrode **D** and a channel layer **C**. The gate and source electrodes **G** and **S** are extended from the gate and data bus lines **121** and **122**, respectively. The channel layer **C** is insulated from the gate electrode **G** and disposed on the gate electrode **G**. When a voltage is applied to the gate electrode **G**, the channel layer **C** operates as a conductor. When the voltage is not applied to the gate electrode **G**, the channel layer **C** operates as an insulator. The source and drain electrodes **S** and **D** are electrically insulated from the channel layer and connected to the channel layer **C**.

[0095] A common voltage applying line **123** is formed at a position adjacent to the data bus line channel **122a** of the first peripheral area **PA1** of the first substrate **100**. The common voltage applying line **123** receives the common voltage externally provided through the first peripheral area **PA1** of the first substrate **100**. A plurality of common voltage applying lines is formed at the first peripheral area **PA1** to apply the common voltage having a uniform level to the common electrode **220** shown in **FIG. 5**.

[0096] The color filter **160** is disposed in the first display area **DA1** of the first transparent substrate **110** in a matrix configuration, thereby covering the TFT **128**. The color filter **160** includes a red color filter **162**, a green color filter **164**

and a blue color filter **166**. Two color filters adjacent to each other among the red, green and blue color filters **162**, **164** and **166** overlap at edges thereof. Also, each of the red, green and blue color filters **162**, **164** and **166** includes a contact hole so as to partially expose the drain electrode **D**.

[0097] The pixel electrode **129** is formed on the red, green and blue color filters **162**, **164** and **166**. The pixel electrode **129** is connected to the drain electrode **D** of the TFT **128** through the contact hole. The pixel electrode **129** is made of a transparent conductive material, such as, for example, ITO or IZO.

[0098] When the pixel electrode **129** is formed on the color filter **160**, the pixel electrode **129** may be placed at a position more distantly spaced from the gate and data bus lines **121** and **122**. Thus, the LCD apparatus **600** may prevent a parasitic capacitance between the pixel electrode **129** and the gate and data bus lines **121** and **122**, which in turn prevents deterioration of an image caused by distortion of the driving signal. Also, since the red, green and blue color filters **162**, **164** and **166** are partially overlapped, the LCD apparatus **600** does not need a black matrix, thereby increasing an area through which light is transmitted.

[0099] The power-applying member **300** includes an insulator **310** and a conductor **129a**.

[0100] The insulator **310** is formed in the first peripheral area **PA1** of the first substrate **100**. The insulator **310** is formed at a position adjacent to the common voltage applying line **123** formed in the first peripheral area **PA1** of the first substrate **100**. In this exemplary embodiment of the invention, the insulator **310** is formed using a portion of a color filter layer that also forms the color filter **160**. The color filter layer includes a red color filter layer **162a**, a green color filter layer **164a** and a blue color filter layer **166a**. The red, green and blue color filter layers **162a**, **164a** and **166a** formed in the first peripheral area **PA1** are formed at the same time the color filter **160** is formed in the first display area **DA1**.

[0101] Particularly, the red color filter layer **162a** is formed at the same time the red color filter **162** is formed in the first display area **DA1**. The green color filter layer **164a** is formed on the red color filter layer **162a** at the same time the green color filter **164** is formed in the first display area **DA1**. The blue color filter layer **166a** is formed on the green color filter layer **164a** at the same time the blue color filter **166** is formed in the first display area **DA1**.

[0102] The insulator **310** having the red, green and blue color filter layers **162a**, **164a** and **166a** has a thickness substantially equal to a distance between the first and second substrates **100** and **200**.

[0103] The conductor **129a** covers the insulator **310** formed in the first peripheral area **PA1** and is insulated from the pixel electrode **129** formed at the first display area **DA1**. The conductor **129a** comprises a transparent conductive material, such as, for example, ITO or IZO. The conductor **129a** may be simultaneously formed with the pixel electrode **129**.

[0104] Referring to **FIG. 16**, the second substrate **200** includes a second transparent substrate **210** and a common electrode **220**. The second transparent substrate **200** may further include a black matrix **240**.

[0105] The second transparent substrate **210** faces the first transparent substrate **110** and includes a second display area **DA2** and a second peripheral area **PA2** surrounding the second display area **DA2**. The second display area **DA2** and second peripheral area **PA2** face the first display area **DA1** and first peripheral area **PA1** of the first substrate **100**, respectively. The common electrode **220** is formed in the second display area **DA2** and partially extends into the second peripheral area **PA2**. The extended portion of the common electrode **220** is connected to the conductor **129a** of the power-applying member **300** placed in the first peripheral area **PA1** of the first substrate **100**.

[0106] The black matrix **240** having a lattice-shape is formed between the second transparent substrate **210** and common electrode **220** to block light from leaking between the pixel electrode **219** and an adjacent pixel electrode. The black matrix **240** is made of, for example, a chromium layer, a double-layer having a chromium layer and a chromium oxide layer, or a black organic layer having a light transmittance substantially equal to that of chromium Cr.

[0107] A sealing member **410** having a band shape is formed at a boundary between the first peripheral area **PA1** and first display area **DA1** of the first substrate **100** or at a boundary between the second peripheral area **PA2** and second display area **DA2** of the second substrate **200**. The liquid crystal **400** is dropped into the first display area **DA1** or second display area **DA2** surrounded by the sealing member **410**, and then the first substrate **100** is assembled with the second substrate **200**. Alternatively, the liquid crystal **400** may be injected between the first and second substrates **100** and **200** after the first and second substrates **100** and **200** are assembled with each other using the sealing member **410**. The conductor **129a** of the first substrate **100** makes contact with the common electrode **220** of the second substrate **200** after the first substrate **100** and the second substrate **200** are assembled together.

[0108] According to this exemplary embodiment of the invention, the insulator **310** and conductor **129a** are formed in the first peripheral area **PA1** of the first substrate **100** and the color filter **160** is formed in the first display area **DA1** of the first substrate **100** to provide the common voltage applied to the first substrate to the common electrode **220** through the conductor **129a**. Thus, the manufacturing processes for the LCD apparatus **600** may be simplified. Also, since the LCD apparatus **600** does not use a gold (Au) spacer, the LCD apparatus **600** may be manufactured at a lower cost.

[0109] FIG. 17 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention. In FIG. 17, the same reference numerals denote the same elements in FIG. 16, and thus the detailed descriptions of the same elements will be omitted.

[0110] Referring to FIG. 17, a transparent planarizing layer **172** is formed over a first transparent substrate **110** of the first substrate **100** to improve step coverage of a color filter **160** formed at a first substrate **100**. A portion of the planarizing layer **172** that covers an insulator **310** formed in a first peripheral area **PA1** is removed by a photolithography process. Thus, a distance between a pixel electrode **129** and a gate bus line **121** and between the pixel electrode **129** and a data bus line **122** of the LCD apparatus is increased, thereby improving display quality of an image.

[0111] According to this exemplary embodiment of the invention, the insulator **310** and the conductor **129a** are formed in the first peripheral area **PA1** of the first substrate **100** and the color filter **160** is formed in the first display area **DA1** of the first substrate **100** to provide the common voltage applied to the first substrate **100** to the common electrode **220** of the second substrate **200** through the conductor **129a**. Thus, the manufacturing processes for the LCD apparatus **600** is simplified and the LCD apparatus **600** may be manufactured at a lower cost since the LCD apparatus **600** does not use a gold (Au) spacer. Also, the color filter **160** and the common electrode **220** of the LCD apparatus **600** have improved step coverage.

[0112] FIG. 18 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention. In FIG. 18, the same reference numerals denote the same elements in FIG. 16, and thus the detailed descriptions of the same elements will be omitted.

[0113] Referring to FIG. 18, a transparent planarizing layer **174** is formed over a first transparent substrate **110** of the first substrate **100** to improve step coverage of a color filter **160** formed at a first substrate **100**. If the total thickness of a conductor **129a** and an insulator **310** is smaller than a cell gap between the first and second substrates **100** and **200**, the planarizing layer **174** may be formed over the color filter **160** and the insulator **310**. In this exemplary embodiment of the invention, the planarizing layer **174** is formed between the color filter **160** and the pixel electrode **129**, so that the conductor **129a** is electrically connected to a common electrode **220** of the second substrate **200**.

[0114] According to this exemplary embodiment of the invention, the insulator **310** and the conductor **129a** are formed in the first peripheral area **PA1** of the first substrate **100** and the color filter **160** is formed in the first display area **DA1** of the first substrate **100** to provide the common voltage applied to the first substrate **100** to the common electrode **220** of the second substrate **200** through the conductor **129a**. Thus, the manufacturing processes for the LCD apparatus **600** is simplified and the LCD apparatus **600** may be manufactured at a lower cost since the LCD apparatus does not use a gold (Au) spacer. Also, the color filter **160** and the common electrode **220** of the LCD apparatus **600** have improved step coverage. Furthermore, the planarizing layer **174** is formed between the color filter **160** and the pixel electrode **129** and between the insulator **310** and conductor **129a**, thereby electrically connecting the conductor **129a** formed in the first peripheral area **PA1** of the first substrate **100** to the common electrode **220** of the second substrate **200**.

[0115] FIG. 19 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention. In FIG. 19, the same reference numerals denote the same elements in FIG. 16, and thus the detailed descriptions of the same elements will be omitted.

[0116] Referring to FIG. 19, a common electrode **220** is formed between a second transparent substrate **210** and a black matrix **240**. This structure prevents deterioration of display quality of an image due to an electric field caused by bad step coverage of the common electrode **220** formed on the black matrix **240**. The black matrix **240** has an opening **244** formed at a position corresponding to a conductor **129a** wrapping an insulator **310**. The conductor **129a** makes

contact with the common electrode **220** through the opening **244** formed in the black matrix **240**.

[0117] According to this exemplary embodiment of the invention, the insulator **310** and the conductor **129a** are formed in the first peripheral area PA1 of the first substrate **100** and the color filter **160** is formed in the first display area DA1 of the first substrate **100** to provide the common voltage applied to the first substrate **100** to the common electrode **220** of the second substrate **200** through the conductor **129a**. Thus, the manufacturing processes for the LCD apparatus **600** is simplified and the LCD apparatus **600** may be manufactured at a lower cost since the LCD apparatus does not use a gold (Au) spacer. Also, the LCD apparatus **600** may prevent deterioration of the display quality of the image due to the electric field caused by the black matrix **240**.

[0118] FIG. 20 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention. In FIG. 20, the same reference numerals denote the same elements in FIG. 16, and thus the detailed descriptions of the same elements will be omitted.

[0119] Referring to FIG. 20, a conductor **129a** formed in a first peripheral area PA1 of a first substrate **100** includes a first concavo-convex portion **129b** that makes contact with a common electrode **220** formed in a second peripheral area PA2 of a second substrate **200**. The common electrode **220** includes a second concavo-convex portion **220b** that makes contact with the conductor **129a**. The first concavo-convex portion **129b** is formed by a slit-exposure method at the same time a color filter **160** is formed, and the second concavo-convex portion **220b** is formed by a slit-exposure method at the same time a black matrix **240** is patterned.

[0120] Although the total thickness of the conductor **129a** and an insulator **310** is different from a distance between the first and second substrates **100** and **200**, the conductor **129a** may make contact with the common electrode **220** by the first and second concavo-convex portions **129b** and **220b**, thereby applying a common voltage applied to the first substrate to the common electrode **220** of the second substrate **200**.

[0121] According to this exemplary embodiment of the invention, the insulator **310** and the conductor **129a** are formed in the first peripheral area PA1 of the first substrate **100** and the color filter **160** is formed in the first display area DA1 of the first substrate **100** to provide the common voltage applied to the first substrate **100** to the common electrode **220** of the second substrate **200** through the conductor **129a**. Thus, the manufacturing processes for the LCD apparatus **600** is simplified and the LCD apparatus **600** may be manufactured at a lower cost since the LCD apparatus does not use a gold (Au) spacer. Also, although the total thickness of the conductor **129a** and insulator **310** is different from the distance between the first and second substrates **100** and **200**, the conductor **129a** may make contact with the common electrode **220**.

[0122] FIG. 21 is a cross-sectional view of an LCD apparatus according to another exemplary embodiment of the present invention. In FIG. 21, the same reference numerals denote the same elements in FIG. 16, and thus the detailed descriptions of the same elements will be omitted.

[0123] Referring to FIG. 21, a spacer **192** having a pole shape is formed in a first display area DA1 of a first substrate

**100** or in a second display area DA2 of a second substrate **200**. In this exemplary embodiment of the invention, the spacer **192** is formed in the first display area DA1 of the first substrate **100**. The spacer **192** is formed between a pixel electrode **129** and an adjacent pixel electrode to maintain a cell gap between the first and second substrates **100** and **200**.

[0124] An insulator **300** is formed in a first peripheral area PA1 of the first substrate **100** and in a second peripheral area PA2 of the second substrate **200**. The insulator **300** is formed at the same time the spacer **192** is formed at the first and second display areas DA1 and DA2. A conductor **129a** is formed on the insulator **300** so as to apply a common voltage applied to the first substrate **100** to a common electrode **220** of the second substrate **200**.

[0125] According to this exemplary embodiment of the invention, the spacer **129** for maintaining the cell gap between the first and second substrates **100** and **200** is used as an insulator while the first and second substrates **100** and **200** are manufactured. The conductor **129a** formed on the insulator **300** applies the common voltage applied to the first substrate **100** to the common electrode **220** of the second substrate **200**. Thus, the manufacturing processes for the LCD apparatus **600** is simplified and the LCD apparatus **600** may be manufactured at a lower cost since the LCD apparatus does not use a gold (Au) spacer.

[0126] In various exemplary embodiments of the present invention, the common voltage applied to the first substrate is applied to the common electrode of the second substrate using an insulator formed by patterning the color filter layer and a conductor formed on the insulator. The common voltage applied to the first substrate may also be provided to the common electrode of the second substrate using a spacer formed by patterning the color filter layer.

[0127] While the present invention has been described in detail with reference to the preferred embodiments, it is to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the spirit and scope of the appended claims.

What is claimed is:

1. A liquid crystal display device, comprising:
  - a first substrate;
  - a common electrode formed over the first substrate;
  - a second substrate disposed opposite the first substrate; and
  - a common voltage applying member that applies a common voltage to the common electrode and that maintains a cell gap between the first substrate and the second substrate, the common voltage-applying member comprising an insulator and a conductor formed over the insulator.
2. The liquid crystal display device of claim 1, further comprising:
  - a liquid crystal layer formed between the first substrate and the second substrate.
3. The liquid crystal display device of claim 1, wherein the common electrode is formed of the same material as the conductor.

4. The liquid crystal display device of claim 1, further comprising a plurality of pixel electrodes formed over the second substrate, the plurality of pixel electrodes being formed of the same material as the conductor.

5. The liquid crystal display device of claim 3, further comprising:

a black matrix formed over the first substrate; and

a color filter formed in openings of the black matrix, wherein the common electrode is formed over the black matrix and the color filter.

6. The liquid crystal display device of claim 5, wherein the color filter is made of the same material as the insulator.

7. The liquid crystal display device of claim 6, wherein the color filter comprises a red color filter, a green color filter and a blue color filter, each of the red, green and blue color filters being formed in a respective opening of the black matrix.

8. The liquid crystal display device of claim 3, further comprising:

a plurality of gate bus lines formed over the second substrate;

a plurality of data bus lines extending perpendicular to the plurality of gate bus lines;

a plurality of gate electrodes extending from the plurality of gate bus lines; and

a plurality of source electrodes extending from the plurality of data bus lines, wherein the plurality of gate electrodes and the plurality of source electrodes form a plurality of thin film transistors.

9. The liquid crystal display device of claim 8, further comprising:

a plurality of pixel electrodes formed over the second substrate, each of the plurality of pixel electrodes being electrically connected to a respective one of the plurality of drain electrodes.

10. The liquid crystal display device of claim 3, further comprising:

a common voltage applying line formed over the second substrate, the common voltage applying line applying a common voltage to the common electrode through the common voltage-applying member.

11. The liquid crystal display device of claim 6, further comprising a planarizing layer formed over the first substrate.

12. The liquid crystal display device of claim 11, wherein the planarizing layer is formed between the insulator and the conductor of the common voltage-applying member.

13. The liquid crystal display device of claim 4, further comprising a color filter formed over the second substrate.

14. The liquid crystal display device of claim 13, wherein the color filter is formed of the same material as the insulator.

15. The liquid crystal display device of claim 14, wherein the plurality of pixel electrodes are formed over the color filter.

16. The liquid crystal display device of claim 14, further comprising:

a plurality of gate bus lines formed over the second substrate;

a plurality of data bus lines extending perpendicular to the plurality of gate bus lines;

a plurality of gate electrodes extending from the plurality of gate bus lines;

a plurality of source electrodes extending from the plurality of data bus lines, wherein the plurality of gate electrodes and the plurality of source electrodes form a plurality of thin film transistors, each of the plurality of pixel electrodes being electrically connected to a respective one of the plurality of drain electrodes.

17. The liquid crystal display device of claim 14, further comprising a common voltage applying line formed over the second substrate, the common voltage applying line applying a common voltage to the common electrode through the common voltage applying member.

18. The liquid crystal display device of claim 14, further comprising a planarizing layer formed over the second substrate.

19. The liquid crystal display device of claim 18, wherein the planarizing layer is formed between the conductor and the insulator of the common voltage-applying member.

20. The liquid crystal display device of claim 14, further comprising a black matrix formed over the first substrate.

21. The liquid crystal display device of claim 20, wherein the common electrode is formed over the black matrix.

22. The liquid crystal display device of claim 20, wherein the black matrix is formed over the common electrode, and the conductor contacts the common electrode through an opening in the black matrix.

23. The liquid crystal display device of claim 14, wherein a concavo-convex portion of the conductor is in contact with a corresponding concavo-convex portion of the common electrode.

24. The liquid crystal display device of claim 4, further comprising a plurality of spacers disposed between the first substrate and the second substrate, the plurality of spacers being formed of the same material as the insulator.

25. A method of forming a liquid crystal display device, comprising:

forming a common electrode over a first substrate;

disposing a second substrate opposite the first substrate;

forming an insulator over one of the first substrate and the second substrate; and

forming a conductor over the insulator, the insulator and the conductor forming a common voltage applying member that applies a common voltage to the common electrode and that maintains a cell gap between the first substrate and the second substrate.

26. The method of claim 25, further comprising forming a liquid crystal layer between the first substrate and the second substrate.

27. The method of claim 25, wherein the insulator is formed over the first substrate, and the step of forming the insulator comprises forming a plurality of color filter layers over one another.

28. The method of claim 27, further comprising:

forming a black matrix over the first substrate;

forming a color filter in openings of the black matrix, wherein the step of forming the color filter is done

simultaneously with the step of forming the plurality of color filter layers of the insulator.

**29.** The method of claim 28, wherein the step of forming a color filter and the step of forming a plurality of color filter layers of the insulator comprise:

simultaneously forming a first color filter in a first opening of the black matrix and a first color filter layer in a peripheral region of the first substrate;

simultaneously forming a second color filter in a second opening of the black matrix and a second color filter layer over the first color filter layer; and

simultaneously forming a third color filter in a third opening of the black matrix and a third color filter layer over the second color filter layer.

**30.** The method of claim 29, further comprising forming a common electrode over the color filter and the black matrix, wherein the step of forming the common electrode is done simultaneously with the step of forming the conductor over the insulator.

**31.** The method of claim 25, further comprising:

forming a plurality of gate bus lines over the second substrate;

forming a plurality of data bus lines extending perpendicular to the plurality of gate bus lines;

forming a plurality of gate electrodes extending from the plurality of gate bus lines; and

forming a plurality of source electrodes extending from the plurality of data bus lines, the plurality of gate electrodes and the plurality of source electrodes forming a plurality of thin film transistors.

**32.** The method of claim 31, further comprising:

forming a plurality of pixel electrodes over the second substrate, each of the plurality of pixel electrodes being electrically connected to a respective one of the plurality of drain electrodes.

**33.** The method of claim 25, further comprising:

forming a common voltage applying line over the second substrate, the common voltage applying line applying a common voltage to the common electrode through the common voltage-applying member.

**34.** The method of claim 25, wherein the insulator is formed over the second substrate, and the step of forming the insulator comprises forming a plurality of color filter layers over one another.

**35.** The method of claim 34, further comprising forming a color filter over the second substrate, wherein the step of forming the color filter is done simultaneously with the step of forming the plurality of color filter layers of the insulator.

**36.** The method of claim 35, wherein the step of forming the color filter and the step of forming the plurality of color filter layers of the insulator comprise:

simultaneously forming a first color filter over the second substrate and a first color filter layer in a peripheral region of the first substrate;

simultaneously forming a second color filter over the second substrate and a second color filter layer over the first color filter layer; and

simultaneously forming a third color filter over the second substrate and a third color filter layer over the second color filter layer.

**37.** The method of claim 36, further comprising forming a plurality of pixel electrodes over the color filter, wherein the step of forming the plurality of pixel electrodes is done simultaneously with the step of forming the conductor over the insulator.

\* \* \* \* \*

专利名称(译)	液晶显示装置及其形成方法		
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摘要(译)

一种液晶显示装置，包括第一基板，形成在第一基板上的公共电极，以及与第一基板相对设置的第二基板。公共电压施加构件将公共电压施加到公共电极并保持第一基板和第二基板之间的单元间隙。公共电压施加构件包括绝缘体和形成在绝缘体上的导体。

