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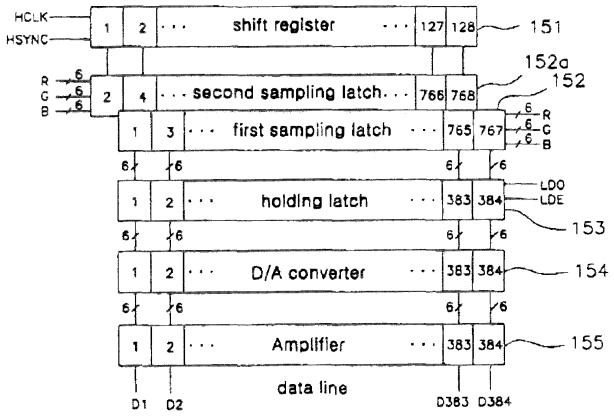
(54) Data driver for a liquid crystal display

(57) A source driver of the liquid crystal display comprising: an (n/3)-clock shift register for shifting a horizontal synchronous signal pulse, to output a latch clock; a first sampling latch for sampling and latching digital video signals corresponding to odd-numbered column lines among 2n column lines according to the latch clock sent from the shift register; a second sampling latch for sampling and latching digital video signals corresponding to even-numbered column lines among 2n column lines according to the latch clock outputted from the shift register; a holding latch for receiving and latching the data stored in the first sampling latch according to a first load signal, and for receiving and latching the data stored in the second sampling latch according to a second load signal; a D/A converter for converting the digital video signals corresponding to the odd-numbered column lines or the digital video signals corresponding to the even-numbered column lines, stored in the holding latch, into analog data signals; and an amplifier for amplifying the analog video signals corresponding to the odd-numbered column lines or the analog video signals corresponding to the even-numbered column lines, supplied from the D/A converter.

FIG.15A



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EUROPEAN SEARCH REPORT

Application Number
EP 00 11 2042

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
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A	EP 0 686 960 A (CANON) 13 December 1995 (1995-12-13) * column 17, line 15 - column 18, line 19 * ---	1,3	
A	US 5 648 792 A (SATO ET AL.) 15 July 1997 (1997-07-15) * column 2, line 10 - line 28 * --- -/--	1,3	

The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	26 February 2001	Lange, J

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
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D : document cited in the application
L : document cited for other reasons

& : member of the same patent family, corresponding document

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EUROPEAN SEARCH REPORT

Application Number
EP 00 11 2042

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
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THE HAGUE	26 February 2001	Lange, J

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ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO. EP 00 11 2042

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26-02-2001

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	用于液晶显示器的数据驱动器		
公开(公告)号	EP1058232A3	公开(公告)日	2001-04-18
申请号	EP2000112042	申请日	2000-06-02
[标]申请(专利权)人(译)	KWON OH KYONG		
申请(专利权)人(译)	KWON，OH-KYONG		
当前申请(专利权)人(译)	SAMSUNG ELECTRONICS CO.，LTD.		
[标]发明人	KWON OH KYONG		
发明人	KWON, OH-KYONG		
IPC分类号	G02F1/133 G09F9/30 G09G3/20 G09G3/36		
CPC分类号	G09G3/3677 G09G3/3614 G09G3/3648 G09G3/3659 G09G3/3688 G09G2300/0814 G09G2310/0251 G09G2310/027 G09G2310/0297 G09G2310/06		
优先权	1019990020721 1999-06-04 KR		
其他公开文献	EP1058232B1 EP1058232A2		
外部链接	Espacenet		

摘要(译)

一种液晶显示器的源极驱动器，包括：（n / 3）时移位寄存器，用于移位水平同步信号脉冲，输出锁存时钟;第一采样锁存器，用于根据从移位寄存器发送的锁存时钟对2n列线中的奇数列线对应的数字视频信号进行采样和锁存;第二采样锁存器，用于根据从移位寄存器输出的锁存时钟，对2n列线中的偶数列线对应的数字视频信号进行采样和锁存;保持锁存器，用于根据第一负载信号接收和锁存存储在第一采样锁存器中的数据，并根据第二负载信号接收和锁存存储在第二采样锁存器中的数据; D / A转换器，用于将对应于奇数列线的数字视频信号或对应于存储在保持锁存器中的偶数列线的数字视频信号转换成模拟数据信号;和放大器，用于放大与D / A转换器提供的对应于奇数列线或对应于偶数列线的模拟视频信号的模拟视频信号。

FIG.15A