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(71) Applicant: **Sharp Kabushiki Kaisha**
Osaka-shi, Osaka 545-8522 (JP)

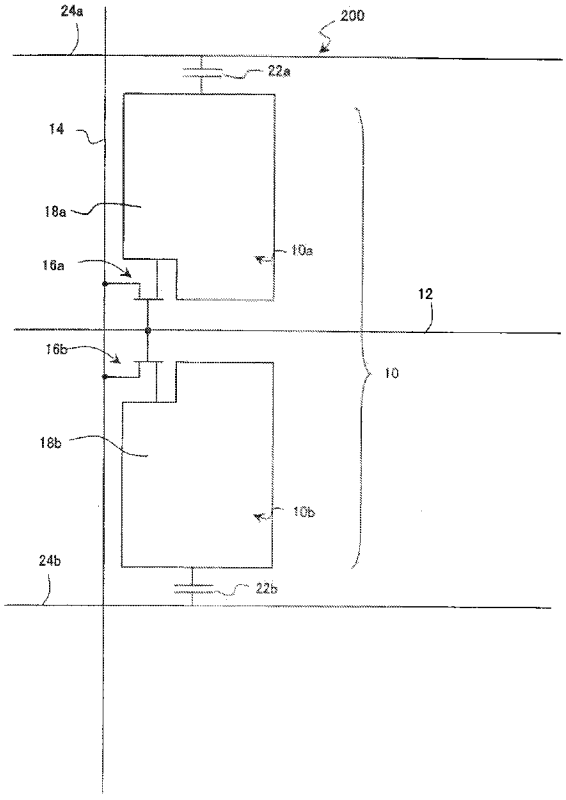
(72) Inventor: **Shimoshikiryoh, Fumikazu**
Matsusaka-shi Mie 515-0043 (JP)

(74) Representative: **Treeby, Philip David William et al**
R.G.C. Jenkins & Co
26 Caxton Street
London SW1H 0RJ (GB)

(54) Liquid crystal display

(57) A liquid crystal display of the invention includes a plurality of pixels each of which has a liquid crystal layer and a plurality of electrodes for applying a voltage to the liquid crystal layer and which are arranged in a matrix of rows and columns, wherein: each of the plurality of pixels has a first sub-pixel and a second sub-pixel which can apply mutually different voltages to the liquid crystal layer, where the first sub-pixel has a higher brightness than the second sub-pixel in certain gradations; the first sub-pixel and the second sub-pixel each has: a liquid crystal capacitor formed by a counter electrode and a sub-pixel electrode opposing the counter electrode via the liquid crystal layer, and a storage capacitor formed by a storage capacitor electrode connected electrically to the sub-pixel electrode, an insulating layer, and a storage capacitor counter electrode opposing the storage capacitor electrode via the insulating layer; the counter electrode is a single electrode shared by the first sub-pixel and the second sub-pixel, and the storage capacitor counter electrodes of the first sub-pixel and the second sub-pixel are electrically independent of each other; and the storage capacitor counter electrode of the first sub-pixel in any of the plurality of pixels and the storage capacitor counter electrode of the second sub-pixel of a pixel adjacent to any of the pixels in the column direction are electrically independent of each other.

FIG. 12



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EUROPEAN SEARCH REPORT

Application Number
EP 10 18 4908

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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A	US 2003/146893 A1 (SAWABE DAIICHI [JP]) 7 August 2003 (2003-08-07) * paragraphs [0313] - [0332]; figures 32-34 * * paragraphs [0334] - [0343]; figures 38,39 * -----	1-28	
A	JP 10 274783 A (SHARP KK) 13 October 1998 (1998-10-13) * paragraphs [0026] - [0028]; figure 1 * -----	1-28	
A	US 2002/047822 A1 (SENDA KOUJI [JP] ET AL) 25 April 2002 (2002-04-25) * abstract * * paragraphs [0153], [0154] * -----	1-28	TECHNICAL FIELDS SEARCHED (IPC) G09G
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The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
The Hague	12 April 2012	van Wesenbeeck, R

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
Y : particularly relevant if combined with another document of the same category
A : technological background
O : non-written disclosure
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& : member of the same patent family, corresponding document

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ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO. EP 10 18 4908

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12-04-2012

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	液晶显示器		
公开(公告)号	EP2267692A3	公开(公告)日	2012-05-23
申请号	EP2010184908	申请日	2004-12-03
[标]申请(专利权)人(译)	夏普株式会社		
申请(专利权)人(译)	夏普株式会社		
当前申请(专利权)人(译)	夏普株式会社		
[标]发明人	SHIMOSHIKIRYOH FUMIKAZU		
发明人	SHIMOSHIKIRYOH, FUMIKAZU		
IPC分类号	G09G3/36 G02F1/1362 G02F1/1368 G02F1/133 G02F1/1343 G09G3/20		
CPC分类号	G09G3/3655 G02F1/136213 G02F1/13624 G02F2001/134345 G09G3/2074 G09G3/3614 G09G3/3648 G09G2300/0443 G09G2300/0447 G09G2300/0876 G09G2320/0223 G09G2320/0247 G09G2320/0276 G09G2320/028		
优先权	2004250982 2004-08-30 JP 2003408046 2003-12-05 JP		
其他公开文献	EP2267692A2 EP2267692B1		
外部链接	Espacenet		

摘要(译)

本发明的液晶显示器包括多个像素，每个像素具有液晶层和多个电极，用于向液晶层施加电压并且以行和列的矩阵排列，其中：每个像素所述多个像素具有第一子像素和第二子像素，所述第一子像素和第二子像素可以向所述液晶层施加相互不同的电压，其中所述第一子像素在某些灰度中具有比所述第二子像素更高的亮度;第一子像素和第二子像素各自具有：由对电极形成的液晶电容器和经由液晶层与对电极相对的子像素电极，以及由连接的存储电容器电极形成的存储电容器通过绝缘层与子像素电极，绝缘层和与存储电容器电极相对的存储电容器对电极电连接;对电极是由第一子像素和第二子像素共用的单个电极，第一子像素和第二子像素的存储电容对电极彼此电独立;所述多个像素中的任意一个像素中的第一子像素的存储电容对置电极和与列方向上的任意像素相邻的像素的第二子像素的存储电容对置电极彼此电独立。