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(54) **PIXEL ARRANGEMENT STRUCTURE**

(57) Provided is a pixel arrangement structure, comprising multiple first pixel rows and multiple second pixel rows alternately arranged. Each of the first pixel rows comprise multiple first sub-pixels and multiple second sub-pixels alternately arranged at an interval. Each of the second pixel rows comprises multiple third sub-pixels arranged at an interval. Two first sub-pixels and two second sub-pixels adjacent to a third sub-pixel form a virtual square, and the third sub-pixel is disposed within the virtual square formed by the two first sub-pixels and the two second sub-pixels adjacent thereto. The first sub-pixels have the same area as the second sub-pixels, and the first sub-pixels and the second sub-pixels have an area greater than the area of the third sub-pixels. The pixel arrangement structure is applicable in OLED display panels and is capable of reducing manufacturing complexity of OLED display panels and increasing the service life thereof.

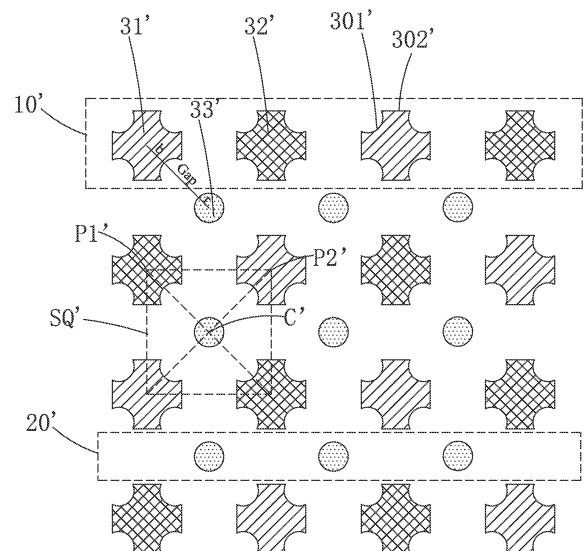


Fig. 2

Description

FIELD OF INVENTION

[0001] The present invention relates to the field of display technologies, and more particularly to a pixel arrangement structure.

BACKGROUND OF INVENTION

[0002] Organic light emitting diode (OLED) display devices have many advantages such as self-luminous, low driving voltage, high luminous efficiency, short response time, high clarity and contrast, nearly 180 degrees viewing angle, wide operating temperature range, flexible display and large area full-color display, and are recognized by the industry as the most promising display devices.

[0003] OLED display devices generally include a substrate, an anode disposed on the substrate, a hole injection layer disposed on the anode, a hole transport layer disposed on the hole injection layer, a light emitting layer disposed on the hole transport layer, an electron transport layer disposed on the light emitting layer, an electron injection layer disposed on the electron transport layer, and a cathode disposed on the electron injection layer. The principle of light emission of the OLED display devices is that a semiconductor material and an organic light emitting material are driven by an electric field to cause light emission through carrier injection and recombination. In details, the OLED display devices generally use an indium tin oxide (ITO) pixel electrode and a metal electrode as the anode and the cathode of the device, respectively. Driven by a certain voltage, electrons and holes are injected into the electron transport layer and hole transport layer from the cathode and the anode, respectively. The electrons and holes migrate to the light emitting layer through the electron transport layer and the hole transport layer, respectively, and meet in the light emitting layer to form excitons and excite light emitting molecules, which emit visible light after radiation relaxation.

[0004] With the development of display technologies, people have higher and higher requirements on resolution and brightness of display devices. To make a high-resolution OLED display requires a higher precision fine metal mask (FMM). In a pixel composed of ordinary stripe sub-pixels (RGB stripe), three sub-pixels are arranged within a pixel pitch in a direction perpendicular to a sub-pixel stripe direction. When a pixel density is greater than 300 ppi, it is very difficult to realize an FMM process at this stage. In addition, due to the improvement of resolution, a distance between a light emitting areas of the sub-pixels is required to be smaller and smaller, and color mixing of an evaporated screen will become more and more serious, especially conventional stripe arrangement of red (R), green (G), and blue (B) three sub-pixels. A length of an opening area of the FMM corresponding to each sub-pixel is long, it is difficult to control the line-

arity, and color mixing is likely to occur. If the conventional RGB stripe pixel structure is directly applied to the OLED panel, there is an issue that the resolution cannot be improved and the production is difficult.

SUMMARY OF INVENTION

[0005] An object of the present invention is to provide a pixel arrangement structure, which is applicable in an organic light emitting diode (OLED) display panel, which can reduce difficulty of manufacturing the OLED display panel and extend lifetime of the OLED display panel.

[0006] To achieve the above objective, the present invention provides a pixel arrangement structure comprising a plurality of first pixel rows and a plurality of second pixel rows arranged alternately.

[0007] Each first pixel row comprises a plurality of first sub-pixels and a plurality of second sub-pixels arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels arranged at intervals.

[0008] Two first sub-pixels and two second sub-pixels adjacent to the third sub-pixel form a virtual box, and the third sub-pixel is arranged in the virtual box formed by the two first sub-pixels and the two second sub-pixels adjacent to the third sub-pixel, an area of the first sub-pixel is same as an area of the second sub-pixel, and the area of the first sub-pixel and the area of the second sub-pixel are greater than an area of the third sub-pixel.

[0009] Optionally, shapes of the first sub-pixel, the second sub-pixel, and the third sub-pixel are all square.

[0010] Optionally, a shape of the third sub-pixel is a circle.

[0011] Optionally, the first sub-pixel and the second sub-pixel both comprise four first sides and four second sides, and the first sides and the second sides are alternately connected to form a closed figure.

[0012] Optionally, the first side is an arc recessed toward an inside of the closed figure, and the second side is a straight line.

[0013] Optionally, the first side is an arc recessed toward an inside of the closed figure, and the second side is an arc protruding toward an outside of the closed figure. The four first sides have different radii of curvature, and the four second sides have different radii of curvature.

[0014] Optionally, diagonal lines of each first sub-pixel and each second sub-pixel in each first pixel row are on a same straight line, and a diagonal line of the third sub-pixel is parallel to a diagonal line of the first sub-pixel.

[0015] Optionally, a center of the first sub-pixel coincides with a first vertex of the virtual box, a center of the second sub-pixel coincides with a second vertex adjacent to the first vertex of the virtual box, and a center of the third sub-pixel coincides with a center of the virtual box.

[0016] Optionally, the first sub-pixel, the second sub-pixel, and the third sub-pixel have different colors and are one of a red sub-pixel, a blue sub-pixel, and a green sub-pixel.

[0017] Optionally, the four first sides of each first sub-pixel are respectively opposite to four third sub-pixels adjacent to the first sub-pixel, and the four first sides of each second sub-pixel are respectively opposite to four third sub-pixels adjacent to the second sub-pixel.

[0018] Optionally, a distance from the first side of the first sub-pixel to an edge of the third sub-pixel opposite to the first side thereof and a distance from the first side of the second sub-pixel to an edge of the third sub-pixel opposite to the first side thereof are both a first length, a distance from a center of the first sub-pixel to the first side thereof and a distance from a center of the second sub-pixel to the first side thereof are both a second length, and a radius of the third sub-pixel is a third length.

[0019] A pixel density of the pixel arrangement structure ranges from 200 ppi to 600 ppi, the first length ranges from 10 μm to 30 μm , the second length ranges from 10 μm to 50 μm , and the third length ranges from 4 μm to 40 μm .

[0020] The present invention further provides a pixel arrangement structure comprising a plurality of first pixel rows and a plurality of second pixel rows arranged alternately.

[0021] Each first pixel row comprises a plurality of first sub-pixels and a plurality of second sub-pixels arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels arranged at intervals.

[0022] Two first sub-pixels and two second sub-pixels adjacent to the third sub-pixel form a virtual box, and the third sub-pixel is arranged in the virtual box formed by the two first sub-pixels and the two second sub-pixels adjacent to the third sub-pixel, an area of the first sub-pixel is same as an area of the second sub-pixel, and the area of the first sub-pixel and the area of the second sub-pixel are greater than an area of the third sub-pixel.

[0023] A shape of the third sub-pixel is a circle.

[0024] The first sub-pixel and the second sub-pixel both comprise four first sides and four second sides, and the first sides and the second sides are alternately connected to form a closed figure.

[0025] The first side is an arc recessed toward an inside of the closed figure, and the second side is a straight line.

[0026] A center of the first sub-pixel coincides with a first vertex of the virtual box, a center of the second sub-pixel coincides with a second vertex adjacent to the first vertex of the virtual box, and a center of the third sub-pixel coincides with a center of the virtual box.

[0027] Beneficial effects of the present invention: the present invention provides a pixel arrangement structure comprising a plurality of first pixel rows and a plurality of second pixel rows arranged alternately. Each first pixel row comprises a plurality of first sub-pixels and a plurality of second sub-pixels arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels arranged at intervals. Two first sub-pixels and two second sub-pixels adjacent to the third sub-pixel form a virtual box, and the third sub-pixel is arranged in

the virtual box formed by the two first sub-pixels and the two second sub-pixels adjacent to the third sub-pixel, an area of the first sub-pixel is same as an area of the second sub-pixel, and the area of the first sub-pixel and the area of the second sub-pixel are greater than an area of the third sub-pixel. The pixel arrangement structure is applicable in an OLED display panel, which can reduce difficulty of manufacturing the OLED display panel and extend lifetime of the OLED display panel.

DESCRIPTION OF DRAWINGS

[0028] In order to further understand features and technical content of the present invention, refer to the following detailed description and drawings of the present invention. However, the drawings are provided for reference and explanation only, and are not intended to limit the present invention.

[0029] In the drawings:

FIG. 1 is a schematic diagram of a pixel arrangement structure according to a first embodiment the present invention.

FIG. 2 is a schematic diagram of a pixel arrangement structure according to a second embodiment of the present invention.

FIG. 3 is a schematic diagram of a first arrangement of a pixel arrangement structure according to a third embodiment of the present invention.

FIG. 4 is a schematic diagram of a second arrangement of the pixel arrangement structure according to the third embodiment of the present invention.

FIG. 5 is a schematic diagram comparing the pixel arrangement structure according to the first embodiment of the present invention with an existing embodiment.

FIG. 6 is a schematic diagram comparing the pixel arrangement structure according to the first embodiment of the present invention with another existing embodiment.

FIG. 7 is a schematic diagram comparing the pixel arrangement structure according to the second embodiment of the present invention with the first embodiment.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0030] In order to further elaborate on the technical means and effects adopted by the present invention, the following will describe in detail with reference to the preferred embodiments and the accompanying drawings of

the present invention.

[0031] The present invention provides a pixel arrangement structure, which is mainly applicable in an organic light emitting diode (OLED) display panel to reduce difficulty of manufacturing the OLED display panel and extend lifetime of the OLED display panel.

[0032] Referring to FIG. 1, in a pixel arrangement structure according to a first embodiment of the present invention, the pixel arrangement structure comprises a plurality of first pixel rows 10 and a plurality of second pixel rows 20 arranged alternately. Each first pixel row 10 comprises a plurality of first sub-pixels 31 and a plurality of second sub-pixels 32 arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels 33 arranged at intervals. Two first sub-pixels 31 and two second sub-pixels 32 adjacent to the third sub-pixel 33 form a virtual box SQ, and the third sub-pixel 33 is arranged in the virtual box SQ formed by the two first sub-pixels 31 and the two second sub-pixels 32 adjacent thereto, an area of the first sub-pixel 31 is same as an area of the second sub-pixel 32, and the area of the first sub-pixel 31 and the area of the second sub-pixel 32 are greater than an area of the third sub-pixel 33.

[0033] In details, as illustrated in FIG. 1, in the first embodiment, a center of the first sub-pixel 31 coincides with a first vertex P1 of the virtual box SQ, a center of the second sub-pixel 32 coincides with a second vertex P2 adjacent to the first vertex P1 of the virtual box SQ, and a center of the third sub-pixel 33 coincides with a center C of the virtual box SQ.

[0034] Further, shapes of the first sub-pixel 31, the second sub-pixel 32, and the third sub-pixel 33 are all square, and diagonal lines of each first sub-pixel 31 and each second sub-pixel 32 in each first pixel row 10 are on a same straight line, and a diagonal line of the third sub-pixel 33 is parallel to a diagonal line of the first sub-pixel 31. Therefore, four first sides of each first sub-pixel 31 are respectively opposite to four third sub-pixels 33 adjacent thereto, four first sides of each second sub-pixel 32 are respectively opposite to four third sub-pixels 33 adjacent thereto, and four first sides of each third sub-pixel 33 are respectively opposite to two first sub-pixels 31 and two second sub-pixels 32 adjacent thereto.

[0035] Preferably, the first sub-pixel 31, the second sub-pixel 32, and the third sub-pixel 33 have different colors and are one of a red sub-pixel, a blue sub-pixel, and a green sub-pixel. In the first embodiment, the first sub-pixel 31, the second sub-pixel 32, and the third sub-pixel 33 emit red light, blue light, and green light respectively. Corresponding to the OLED display panel, that is, the first sub-pixel 31, the second sub-pixel 32, and the third sub-pixel 33 comprise organic light emitting diodes that emit red light, blue light, and green light, respectively. It is understood that, in other embodiments of the present invention, the first sub-pixel 31, the second sub-pixel 32, and the third sub-pixel 33 may also emit light of other colors.

[0036] It is noted that square sub-pixels in this embod-

iment have advantages over octagonal sub-pixels. As illustrated in FIG. 5, compared to shapes of a first sub-pixel 31a and a second sub-pixel 32a being octagonal, and a shape of a third sub-pixel being a square, a distance between the first sub-pixel 31a and the third sub-pixel and a distance between the second sub-pixel 32a and the third sub-pixel are both a first length Gap. As illustrated in FIG. 5, under the same first length Gap, an embodiment of the present invention increases an area of the first sub-pixel 31 and an area of the second sub-pixel 32 by setting both the first sub-pixel 31 and the second sub-pixel 32 to be square. Corresponding to the OLED display panel, an area of an organic light emitting diode is proportional to its lifetime, so that compared with the embodiment using the octagonal first sub-pixel 31a and the octagonal second sub-pixel 32a, the pixel arrangement of the first embodiment of the present invention has a longer lifetime without changing the first length.

[0037] Similarly, referring to FIG. 6, compared with the embodiment where shapes of the first sub-pixel and the second sub-pixel are both square, and the shape of the third sub-pixel 33b is octagonal, in an embodiment of the present invention same as the previous embodiment, by setting the shape of the third sub-pixel 33 to be square, the area of the third sub-pixel is increased and lifetime of the OLED display panel is increased without changing the first length GAP.

[0038] Referring to FIG. 2, in a pixel arrangement structure according to a second embodiment of the present invention, the pixel arrangement structure comprises a plurality of first pixel rows 10' and a plurality of second pixel rows 20' arranged alternately. Each first pixel row 10' comprises a plurality of first sub-pixels 31' and a plurality of second sub-pixels 32' arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels 33' arranged at intervals. Two first sub-pixels 31' and two second sub-pixels 32' adjacent to the third sub-pixel 33' form a virtual box SQ', and the third sub-pixel 33' is arranged in the virtual box SQ' formed by the two first sub-pixels 31' and the two second sub-pixels 32' adjacent thereto, an area of the first sub-pixel 31' is same as an area of the second sub-pixel 32', and the area of the first sub-pixel 31' and the area of the second sub-pixel 32' are greater than an area of the third sub-pixel 33'.

[0039] In details, as illustrated in FIG. 2, in the second embodiment, a center of the first sub-pixel 31' coincides with a first vertex P1' of the virtual box SQ', a center of the second sub-pixel 32' coincides with a second vertex P2' adjacent to the first vertex P1' of the virtual box SQ', and a center of the third sub-pixel 33' coincides with a center C' of the virtual box SQ'. A shape of the third sub-pixel 33' is a circle. The first sub-pixel 31' and the second sub-pixel 32' both comprise four first sides 301' and four second sides 302', and the first sides 301' and the second sides 302' are alternately connected to form a closed figure. The first side 301' is an arc recessed toward an inside of the closed figure, and the second side 302' is a

straight line. Therefore, four first sides 301' of each first sub-pixel 31' are respectively opposite to four third sub-pixels 33' adjacent to the first sub-pixel 31', four first sides 301' of each second sub-pixel 32' are respectively opposite to four third sub-pixels 33' adjacent to the second sub-pixel 32'.

[0040] It is worth mentioning that, in an ideal state of the second embodiment of the pixel arrangement structure, shapes of the first sub-pixel 31' and the second sub-pixel 32' should be exactly the same, and radii of curvature of four first sides 301' of each first sub-pixel 31' and each second sub-pixel 32' are also the same. In an actual manufacturing process of the second embodiment of the pixel arrangement structure, due to existence of manufacturing errors (for example, manufacturing errors caused by different states of a vapor deposition machine), shapes of the first sub-pixel 31' and the second sub-pixel 32' may be slightly different, and radii of curvature of four first sides 301' of each first sub-pixel 31' and each second sub-pixel 32' may also be slightly deviated, which will not affect the implementation of the present invention.

[0041] It should be noted that, in an actual production of FMM, it is difficult to produce square or pointed graphics regardless of etching or laser. In an embodiment of the present invention, the third sub-pixel 33' is set to a circle, and arc-shaped first sides 301' are set in the first sub-pixel 31' and the second sub-pixel 32', which is more in line with the principle of diffusion and can effectively reduce difficulty of making the FFM. In addition, the circular third sub-pixel 33' is the shape with the smallest side length and the highest efficiency in the same area. Due to halo effect of human eyes, a tiny non-circular luminous body will also be regarded as an approximately circular shape. Setting the third sub-pixel 33' as a circular shape can maximize efficiency of the third sub-pixel 33' and meet the halo effect of human eyes. By setting the four first sides 301' of each first sub-pixel 31' to be respectively opposite to four third sub-pixels 33' adjacent to the first sub-pixel 31' and by setting the four first sides 301' of each second sub-pixel 32' to be respectively opposite to the four third sub-pixels 33' adjacent to the second sub-pixel 32', spacing between sub-pixels is maximized.

[0042] Preferably, the first sub-pixel 31', the second sub-pixel 32', and the third sub-pixel 33' have different colors and are one of a red sub-pixel, a blue sub-pixel, and a green sub-pixel. In the second embodiment, the first sub-pixel 31', the second sub-pixel 32', and the third sub-pixel 33' emit red light, blue light, and green light respectively. Corresponding to the OLED display panel, that is, the first sub-pixel 31', the second sub-pixel 32', and the third sub-pixel 33' comprise organic light emitting diodes that emit red light, blue light, and green light, respectively. It is understood that, in other embodiments of the present invention, the first sub-pixel 31', the second sub-pixel 32', and the third sub-pixel 33' may also emit light of other colors.

[0043] Further, as illustrated in FIG. 7, compared with the first embodiment, in the second embodiment, when a distance between the first sub-pixel and the third sub-pixel and a distance between the second sub-pixel and the third sub-pixel, i.e., the first length Gap, are constant, areas of the first sub-pixel 31' and the second sub-pixel 32' are larger, but the area of the third sub-pixel 33' is smaller. In the OLED display panel, lifetime of an organic light emitting diode emitting blue light is the worst, while lifetime of an organic light emitting diode emitting green light is better. The overall lifetime of the OLED display panel depends on the organic light emitting diode with the worst lifetime. In the second embodiment of the present invention, although the area of the third sub-pixel 33' becomes smaller, the third sub-pixel 33' corresponds to an organic light emitting diode that emits green light, and the second sub-pixel 32' corresponds to an organic light emitting diode that emits blue light. In an actual implementation, actual lifetime of the third sub-pixel 33' after the area thereof becomes smaller is still greater than actual lifetime of the second sub-pixel 32' after the area thereof becomes larger. Therefore, the overall lifetime of the OLED display panel still depends on the actual lifetime of the second sub-pixel 32'. The area of the second sub-pixel 32' is larger than that of the first embodiment, so that the overall lifetime of the OLED display panel is larger than that of the first embodiment.

[0044] In details, in the second embodiment of the present invention, a distance from the first side of the first sub-pixel 31' to an edge of the third sub-pixel 33' opposite to the first side 301' thereof and a distance from the first side 301' of the second sub-pixel 32' to an edge of the third sub-pixel 33' opposite to the first side 301' thereof are both a first length Gap, a distance from a center of the first sub-pixel 31' to the first side 301' thereof and a distance from a center of the second sub-pixel 32' to the first side 301' thereof are both a second length b, and a radius of the third sub-pixel 33' is a third length r.

[0045] During specific implementation, in order to make the actual lifetime of the third sub-pixel 33' after the area thereof becomes smaller still greater than the actual lifetime of the second sub-pixel 32' after the area thereof becomes larger, thereby extending the overall lifetime of the OLED display panel, a pixel density of the pixel arrangement structure preferably ranges from 200 ppi to 600 ppi, the first length Gap preferably ranges from 10 μm to 30 μm , the second length b preferably ranges from 10 μm to 50 μm , and the third length r preferably ranges from 4 μm to 40 μm .

[0046] Optionally, in a specific implementation of the second embodiment of the present invention, the first length Gap may be 20 μm , the pixel density of the pixel arrangement may be 200 ppi, a sum of the second length b and the third length r may be 40 μm , and the second length b may range from 10 μm to 30 μm .

[0047] Optionally, in another specific implementation of the second embodiment of the present invention, the first length Gap may be 20 μm , the pixel density of the

pixel arrangement structure may be 250 ppi, a sum of the second length b and the third length r may be 28 μm , and the second length b may range from 10 μm to 21 μm .

[0048] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 20 μm , the pixel density of the pixel arrangement structure may be 300 ppi, a sum of the second length b and the third length r may be 20 μm , and the second length b may range from 10 μm to 15 μm .

[0049] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 20 μm , the pixel density of the pixel arrangement structure may be 350 ppi, a sum of the second length b and the third length r may be 14 μm , and the second length b may be 10 μm .

[0050] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 15 μm , the pixel density of the pixel arrangement structure may be 200 ppi, a sum of the second length b and the third length r may be 45 μm , and the second length b may range from 10 μm to 33 μm .

[0051] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 15 μm , the pixel density of the pixel arrangement structure may be 250 ppi, a sum of the second length b and the third length r may be 33 μm , and the second length b may range from 10 μm to 24 μm .

[0052] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 15 μm , the pixel density of the pixel arrangement structure may be 300 ppi, a sum of the second length and the third length may be 25 μm , and the second length b may range from 10 μm to 18 μm .

[0053] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 15 μm , the pixel density of the pixel arrangement structure may be 350 ppi, a sum of the second length b and the third length r may be 19 μm , and the second length b may range from 10 μm to 14 μm .

[0054] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 15 μm , the pixel density of the pixel arrangement structure may be 400 ppi, a sum of the second length b and the third length r may be 15 μm , and the second length b may range from 10 μm to 11 μm .

[0055] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 25 μm , the pixel density of the pixel arrangement structure may be 200 ppi, a sum of the second length b and the third length r may be 35 μm , and the second length b may range from 10 μm to 26 μm .

[0056] Optionally, in yet another specific implementa-

tion of the second embodiment of the present invention, the first length Gap may be 25 μm , the pixel density of the pixel arrangement structure may be 250 ppi, a sum of the second length b and the third length r may be 23 μm , and the second length b may range from 10 μm to 17 μm .

[0057] Optionally, in yet another specific implementation of the second embodiment of the present invention, the first length Gap may be 25 μm , the pixel density of the pixel arrangement structure may be 300 ppi, a sum of the second length b and the third length r may be 15 μm , and the second length b may range from 10 μm to 11 μm .

[0058] Referring to FIG. 3 and FIG. 4, in a pixel arrangement structure according to a third embodiment of the present invention, the pixel arrangement structure comprises a plurality of first pixel rows 10" and a plurality of second pixel rows 20" arranged alternately. Each first pixel row 10" comprises a plurality of first sub-pixels 31" and a plurality of second sub-pixels 32" arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels 33" arranged at intervals. Two first sub-pixels 31" and two second sub-pixels 32" adjacent to the third sub-pixel 33" form a virtual box SQ", and the third sub-pixel 33" is arranged in the virtual box SQ" formed by the two first sub-pixels 31" and the two second sub-pixels 32" adjacent thereto, an area of the first sub-pixel 31" is same as an area of the second sub-pixel 32", and the area of the first sub-pixel 31" and the area of the second sub-pixel 32" are greater than an area of the third sub-pixel 33".

[0059] In details, as illustrated in FIG. 3 or FIG. 4, in the third embodiment, a center of the first sub-pixel 31" coincides with a first vertex P1" of the virtual box SQ", a center of the second sub-pixel 32" coincides with a second vertex P2" adjacent to the first vertex P1" of the virtual box SQ", and a center of the third sub-pixel 33" coincides with a center C" of the virtual box SQ". A shape of the third sub-pixel 33" is a circle. The first sub-pixel 31" and the second sub-pixel 32" have the same shape and both comprise four first sides 301" and four second sides 302", and the first sides 301" and the second sides 302" are alternately connected to form a closed figure. The first side 301" is an arc recessed toward an inside of the closed figure, and the second side 302" is an arc protruding toward the inside of the closed figure, that is, the shape of the second side 302" is similar to a bow arm of an arrow.

[0060] The specific arrangement, as illustrated in FIG. 3, four first sides 301" of each first sub-pixel 31" can be set to be respectively opposite to four third sub-pixels 33" adjacent to the first sub-pixel 31", four first sides 301" of each second sub-pixel 32" can be set to be respectively opposite to four third sub-pixels 33" adjacent to the second sub-pixel 32". Alternatively, as illustrated in FIG. 4, four second sides 302" of each first sub-pixel 31" are set to be respectively opposite to four third sub-pixels 33" adjacent to the first sub-pixel 31", four second

sides 302" of each second sub-pixel 32" are set to be respectively opposite to fourth third sub-pixels 33" adjacent to the second sub-pixel 32".

[0061] It is worth mentioning that, in an ideal state of the third embodiment of the pixel arrangement structure, shapes of the first sub-pixel 31" and the second sub-pixel 32" should be exactly the same, radii of curvature of four first sides 301" of each first sub-pixel 31" and each second sub-pixel 32" are also the same, and radii of curvature of four second sides 302" of each first sub-pixel 31" and each second sub-pixel 32" are also the same. In an actual manufacturing process of the third embodiment of the pixel arrangement structure, due to existence of manufacturing errors (for example, manufacturing errors caused by different states of a vapor deposition machine), shapes of the first sub-pixel 31" and the second sub-pixel 32" may be slightly different, radii of curvature of four first sides 301" of each first sub-pixel 31" and each second sub-pixel 32" may also be slightly deviated, and radii of curvature of four second sides 302" of each first sub-pixel 31" and each second sub-pixel 32" may also be slightly deviated, which will not affect the implementation of the present invention.

[0062] It should be noted that, in an actual production of FMM, it is difficult to produce square or pointed graphics regardless of etching or laser. In an embodiment of the present invention, the third sub-pixel 33" is set to a circle, and arc-shaped first sides 301" and arc-shaped second sides 302" are set in the first sub-pixel 31" and the second sub-pixel 32", which is more in line with the principle of diffusion and can effectively reduce difficulty of making the FFM. In addition, the circular third sub-pixel 33" is the shape with the smallest side length and the highest efficiency in the same area. Due to halo effect of human eyes, a tiny non-circular luminous body will also be regarded as an approximately circular shape. Setting the third sub-pixel 33" as a circular shape can maximize efficiency of the third sub-pixel 33" and meet the halo effect of human eyes.

[0063] Preferably, the first sub-pixel 31", the second sub-pixel 32", and the third sub-pixel 33" have different colors and are one of a red sub-pixel, a blue sub-pixel, and a green sub-pixel. In the third embodiment, the first sub-pixel 31", the second sub-pixel 32", and the third sub-pixel 33" emit red light, blue light, and green light respectively. Corresponding to the OLED display panel, that is, the first sub-pixel 31", the second sub-pixel 32", and the third sub-pixel 33" comprise organic light emitting diodes that emit red light, blue light, and green light, respectively. It is understood that, in other embodiments of the present invention, the first sub-pixel 31", the second sub-pixel 32", and the third sub-pixel 33" may also emit light of other colors.

[0064] Further, compared with the second embodiment, in the third embodiment, the second side 302" is an arc protruding toward an outside of the closed figure, so that areas of the first sub-pixel 31" and the second sub-pixel 32" are larger and lifetime can be further im-

proved compared with the second embodiment.

[0065] Meanwhile, in each embodiment of the present invention, shapes and areas of the first sub-pixel and the second sub-pixel are completely the same, such that in the manufacturing process of the OLED display panel adopting the pixel arrangement structure of the present invention, the first sub-pixel and the second sub-pixel can be manufactured by using the same metal mask. This reduces production costs and enhances product competitiveness.

[0066] In summary, an embodiment of the present invention provides a pixel arrangement structure comprising a plurality of first pixel rows and a plurality of second pixel rows arranged alternately. Each first pixel row comprises a plurality of first sub-pixels and a plurality of second sub-pixels arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels arranged at intervals. Two first sub-pixels and two second sub-pixels adjacent to the third sub-pixel form a virtual box, and the third sub-pixel is arranged in the virtual box formed by the two first sub-pixels and the two second sub-pixels adjacent to the third sub-pixel, an area of the first sub-pixel is same as an area of the second sub-pixel, and the area of the first sub-pixel and the area of the second sub-pixel are greater than an area of the third sub-pixel. The pixel arrangement structure is applicable in an OLED display panel, which can reduce difficulty of manufacturing the OLED display panel and extend lifetime of the OLED display panel.

[0067] As mentioned above, those of ordinary skill in the art can make various other corresponding changes and modifications according to technical solutions and technical concepts of the present invention, and all such changes and modifications should fall within the protection scope of the claims of the present invention.

Claims

1. A pixel arrangement structure, comprising: a plurality of first pixel rows and a plurality of second pixel rows arranged alternately; wherein:

each first pixel row comprises a plurality of first sub-pixels and a plurality of second sub-pixels arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels arranged at intervals; and two first sub-pixels and two second sub-pixels adjacent to the third sub-pixel form a virtual box, and the third sub-pixel is arranged in the virtual box formed by the two first sub-pixels and the two second sub-pixels adjacent to the third sub-pixel, an area of the first sub-pixel is same as an area of the second sub-pixel, and the area of the first sub-pixel and the area of the second sub-pixel are greater than an area of the third sub-pixel.

2. The pixel arrangement structure according to Claim 1, wherein shapes of the first sub-pixel, the second sub-pixel, and the third sub-pixel are all square.
3. The pixel arrangement structure according to Claim 1, wherein a shape of the third sub-pixel is a circle.
4. The pixel arrangement structure according to Claim 3, wherein the first sub-pixel and the second sub-pixel both comprise four first sides and four second sides, and the first sides and the second sides are alternately connected to form a closed figure.
5. The pixel arrangement structure according to Claim 4, wherein the first side is an arc recessed toward an inside of the closed figure, and the second side is a straight line.
6. The pixel arrangement structure according to Claim 4, wherein the first side is an arc recessed toward an inside of the closed figure, and the second side is an arc protruding toward an outside of the closed figure.
7. The pixel arrangement structure according to Claim 6, wherein the four first sides have different radii of curvature, and the four second sides have different radii of curvature.
8. The pixel arrangement structure according to Claim 2, wherein diagonal lines of each first sub-pixel and each second sub-pixel in each first pixel row are on a same straight line, and a diagonal line of the third sub-pixel is parallel to a diagonal line of the first sub-pixel.
9. The pixel arrangement structure according to Claim 1, wherein a center of the first sub-pixel coincides with a first vertex of the virtual box, a center of the second sub-pixel coincides with a second vertex adjacent to the first vertex of the virtual box, and a center of the third sub-pixel coincides with a center of the virtual box.
10. The pixel arrangement structure according to Claim 1, wherein the first sub-pixel, the second sub-pixel, and the third sub-pixel have different colors and are one of a red sub-pixel, a blue sub-pixel, and a green sub-pixel.
11. The pixel arrangement structure according to Claim 5, wherein the four first sides of each first sub-pixel are respectively opposite to four third sub-pixels adjacent to the first sub-pixel, and the four first sides of each second sub-pixel are respectively opposite to four third sub-pixels adjacent to the second sub-pixel.
12. The pixel arrangement structure according to Claim 11, wherein a distance from the first side of the first sub-pixel to an edge of the third sub-pixel opposite to the first side thereof and a distance from the first side of the second sub-pixel to an edge of the third sub-pixel opposite to the first side thereof are both a first length, a distance from a center of the first sub-pixel to the first side thereof and a distance from a center of the second sub-pixel to the first side thereof are both a second length, and a radius of the third sub-pixel is a third length; and a pixel density of the pixel arrangement structure ranges from 200 ppi to 600 ppi, the first length ranges from 10 μm to 30 μm , the second length ranges from 10 μm to 50 μm , and the third length ranges from 4 μm to 40 μm .
13. A pixel arrangement structure, comprising:
a plurality of first pixel rows and a plurality of second pixel rows arranged alternately; wherein:
each first pixel row comprises a plurality of first sub-pixels and a plurality of second sub-pixels arranged alternately and at intervals, and each second pixel row comprises a plurality of third sub-pixels arranged at intervals;
two first sub-pixels and two second sub-pixels adjacent to the third sub-pixel form a virtual box, and the third sub-pixel is arranged in the virtual box formed by the two first sub-pixels and the two second sub-pixels adjacent to the third sub-pixel, an area of the first sub-pixel is same as an area of the second sub-pixel, and the area of the first sub-pixel and the area of the second sub-pixel are greater than an area of the third sub-pixel;
a shape of the third sub-pixel is a circle;
the first sub-pixel and the second sub-pixel both comprise four first sides and four second sides, and the first sides and the second sides are alternately connected to form a closed figure;
the first side is an arc recessed toward an inside of the closed figure, and the second side is a straight line; and
a center of the first sub-pixel coincides with a first vertex of the virtual box, a center of the second sub-pixel coincides with a second vertex adjacent to the first vertex of the virtual box, and a center of the third sub-pixel coincides with a center of the virtual box.
14. The pixel arrangement structure according to Claim 13, wherein the first sub-pixel, the second sub-pixel, and the third sub-pixel have different colors and are one of a red sub-pixel, a blue sub-pixel, and a green sub-pixel.
15. The pixel arrangement structure according to Claim

13, wherein the four first sides of each first sub-pixel are respectively opposite to four third sub-pixels adjacent to the first sub-pixel, and the four first sides of each second sub-pixel are respectively opposite to four third sub-pixels adjacent to the second sub-pixel. 5

16. The pixel arrangement structure according to Claim 15, wherein a distance from the first side of the first sub-pixel to an edge of the third sub-pixel opposite to the first side thereof and a distance from the first side of the second sub-pixel to an edge of the third sub-pixel opposite to the first side thereof are both a first length, a distance from a center of the first sub-pixel to the first side thereof and a distance from a center of the second sub-pixel to the first side thereof are both a second length, and a radius of the third sub-pixel is a third length; and a pixel density of the pixel arrangement structure ranges from 200 ppi to 600 ppi, the first length ranges from 10 μ m to 30 μ m, the second length ranges from 10 μ m to 50 μ m, and the third length ranges from 4 μ m to 40 μ m.

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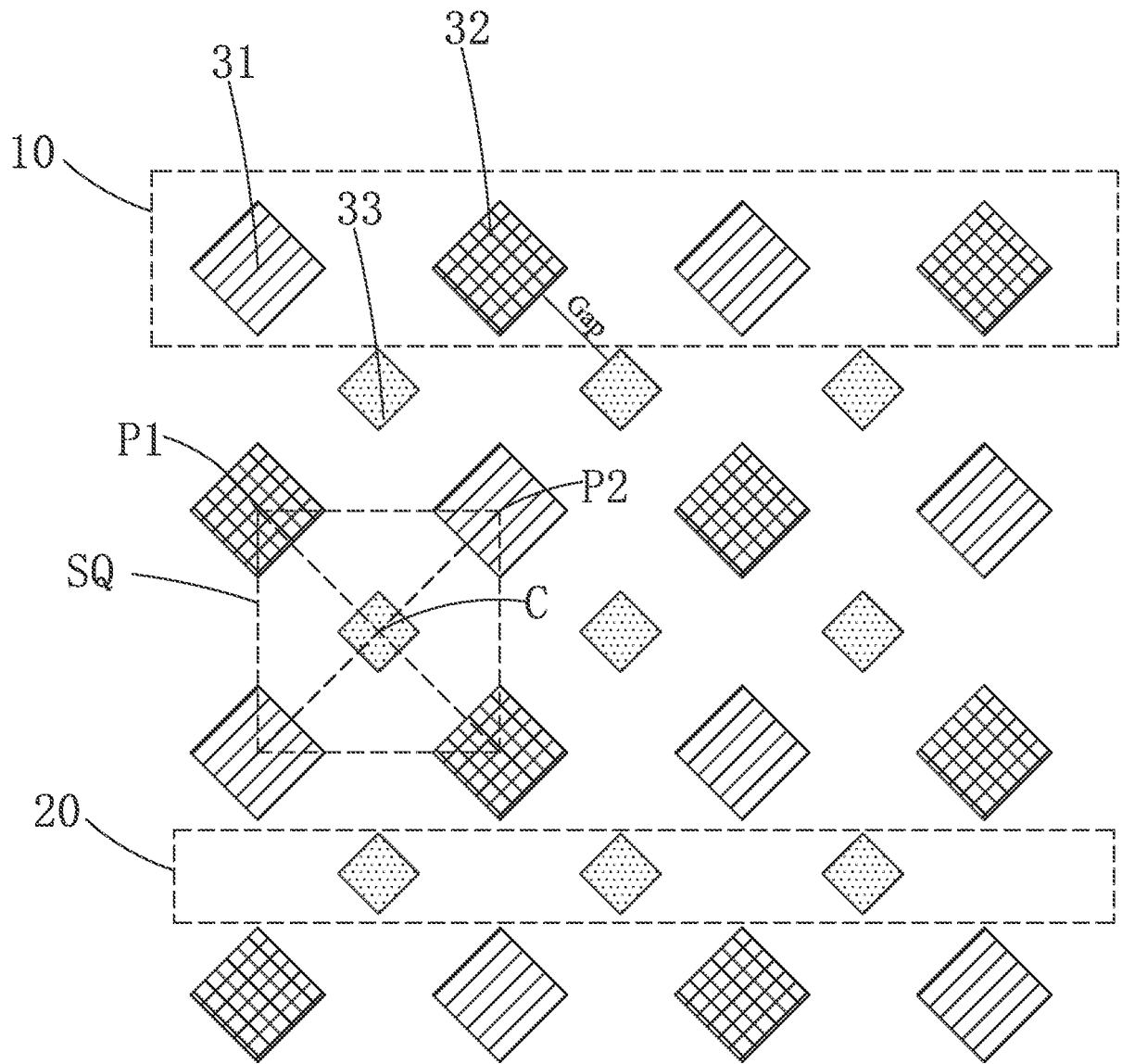


Fig. 1

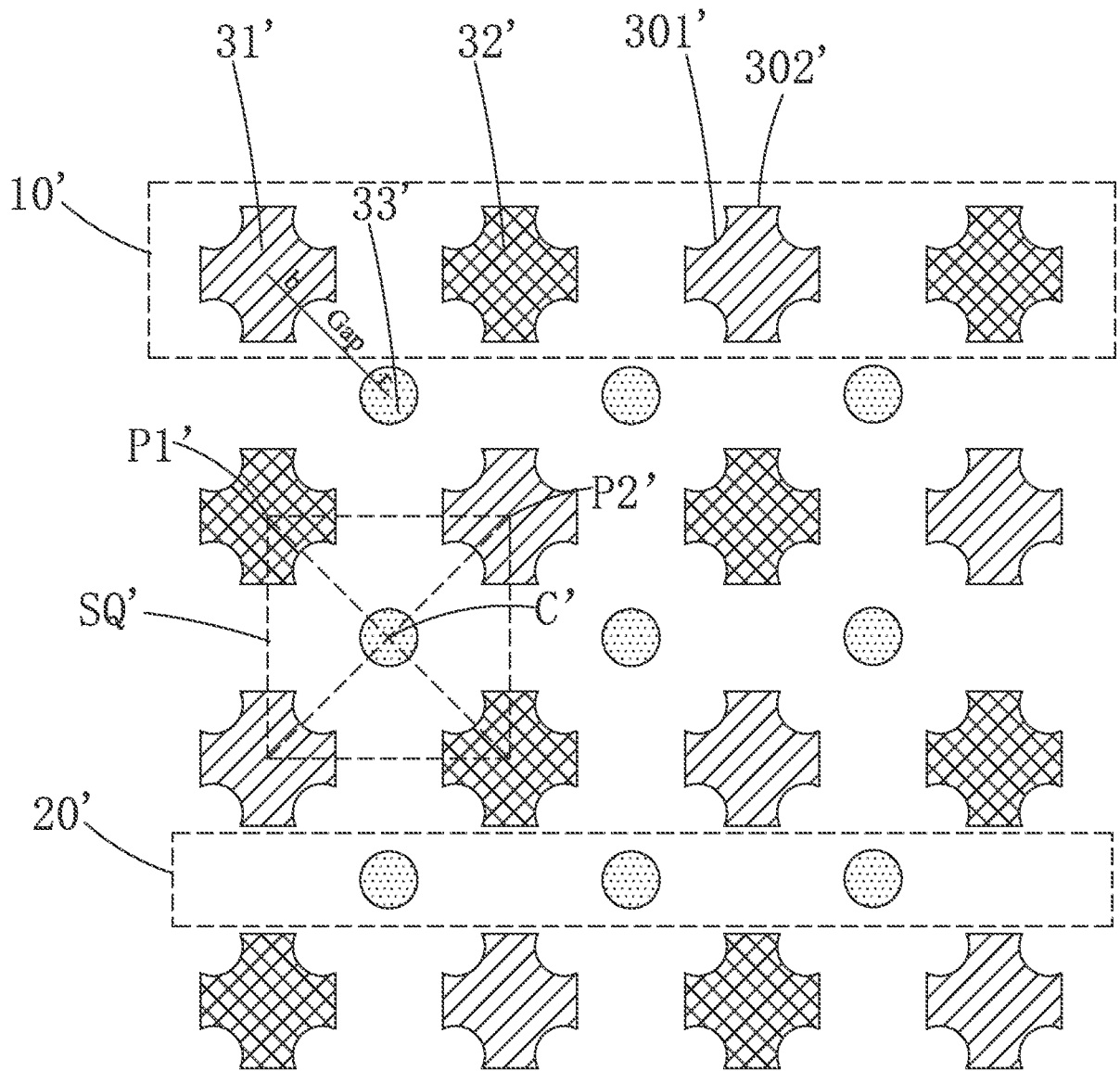


Fig. 2

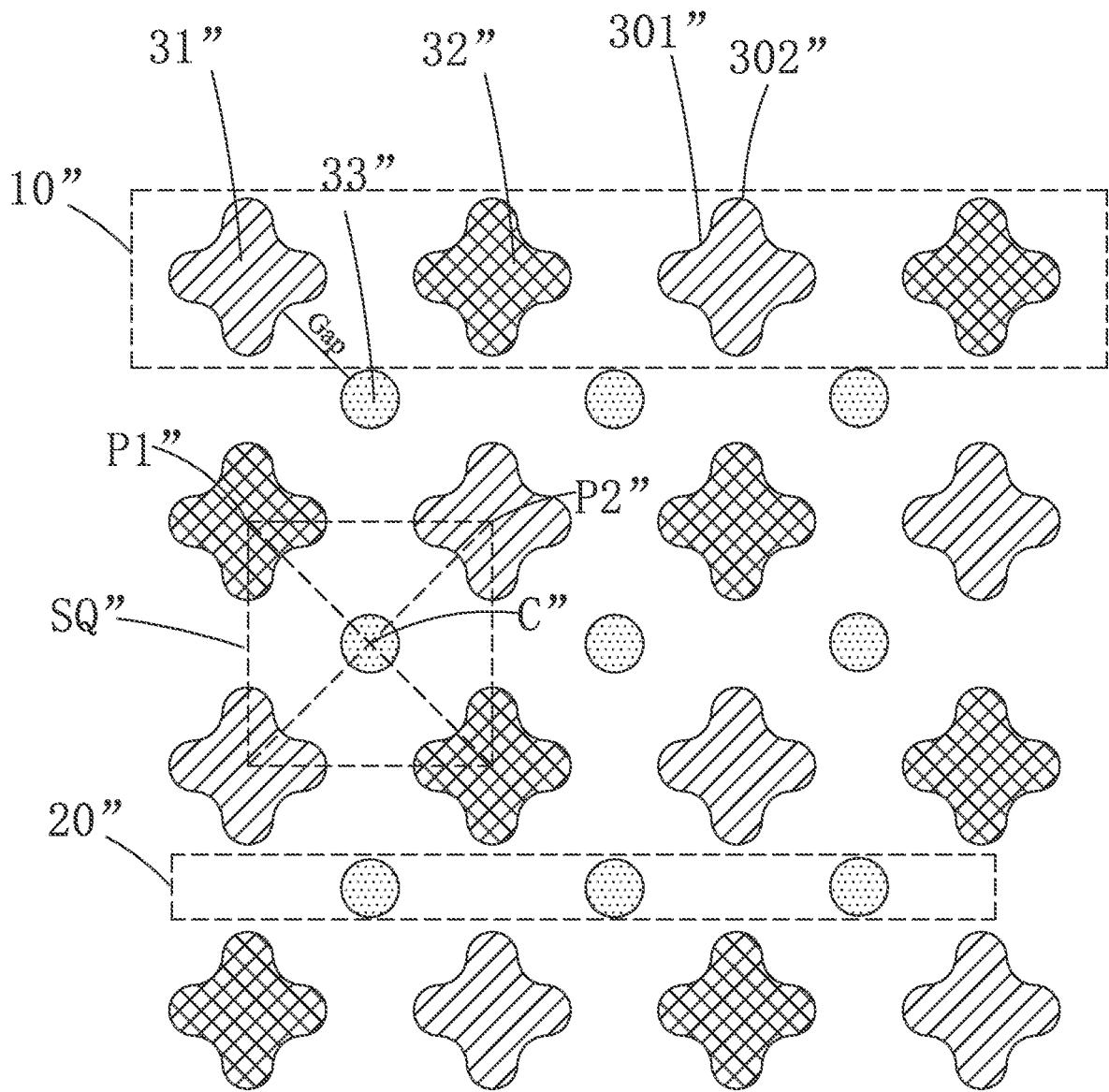


Fig. 3

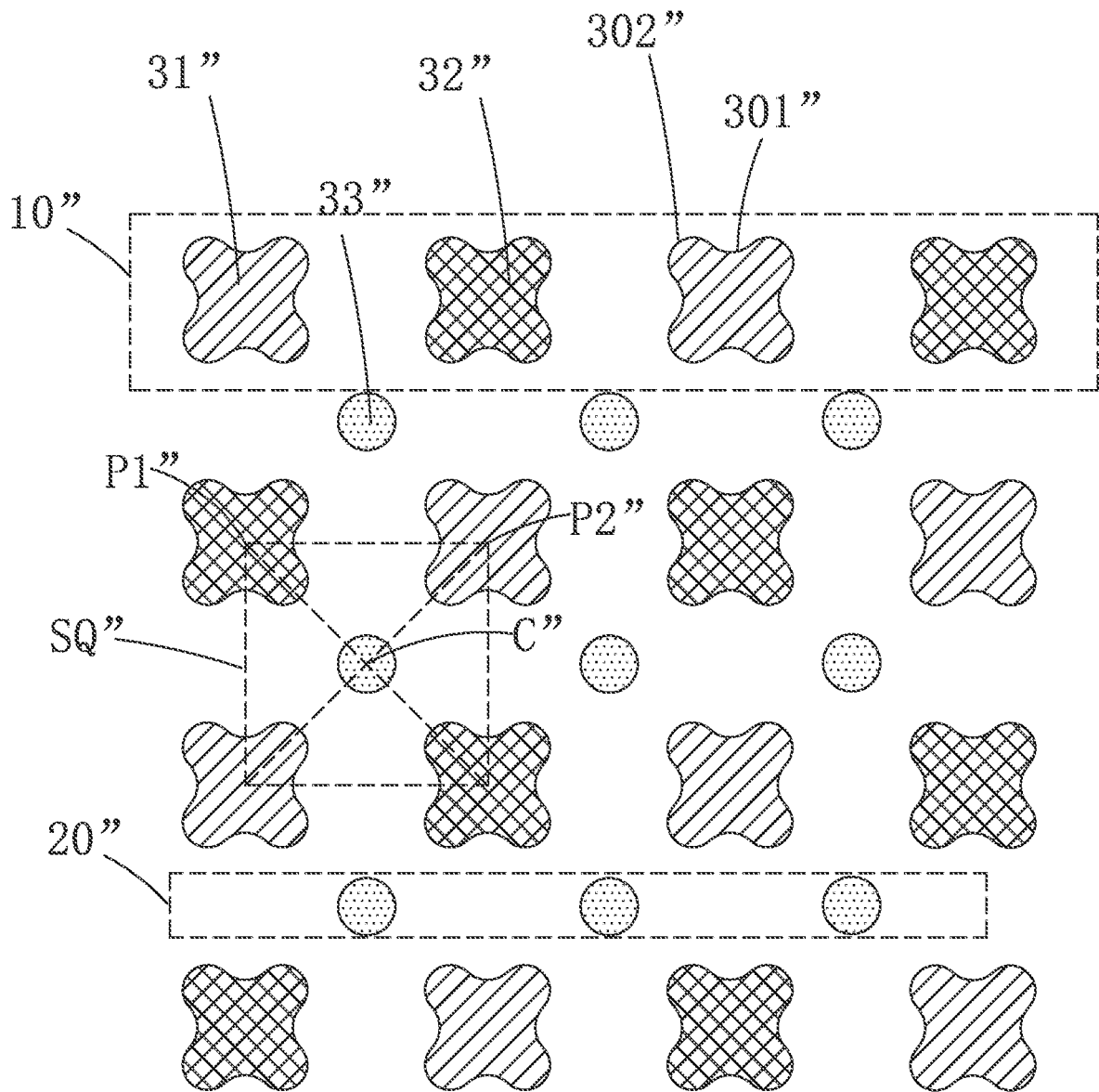


Fig. 4

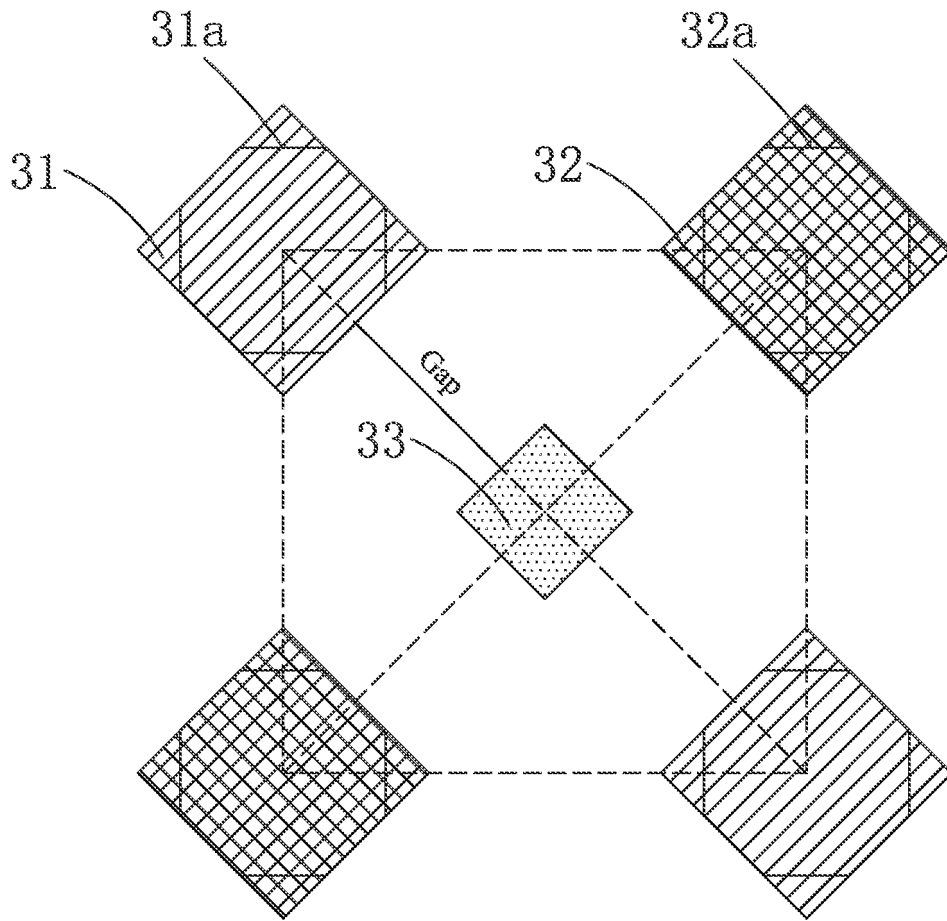


Fig. 5

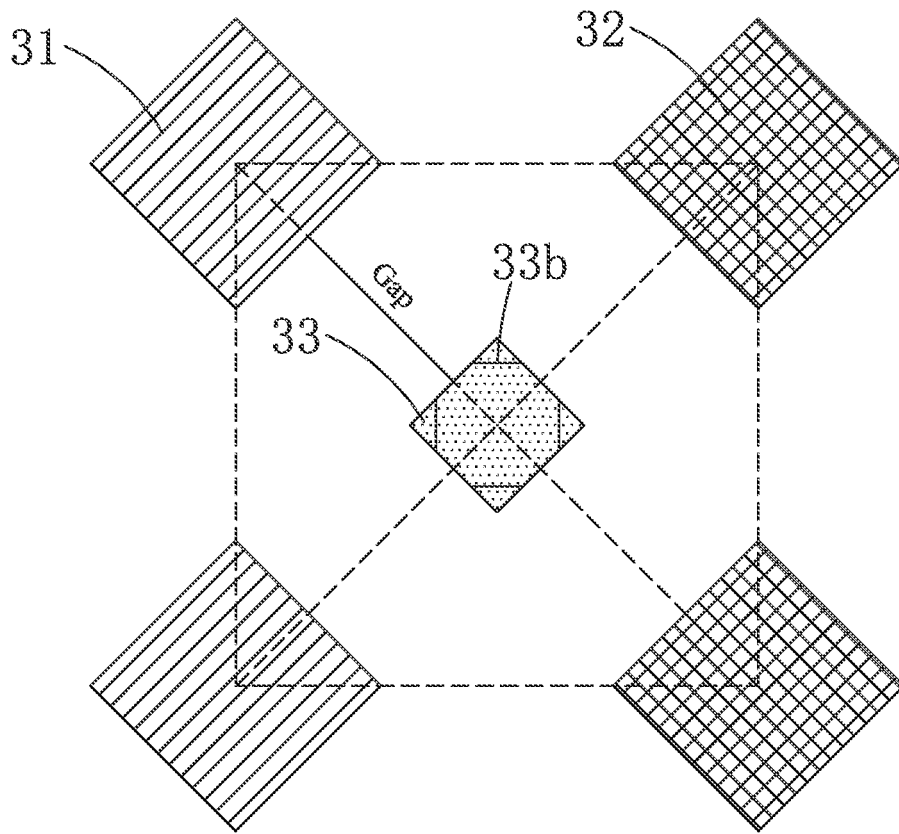


Fig. 6

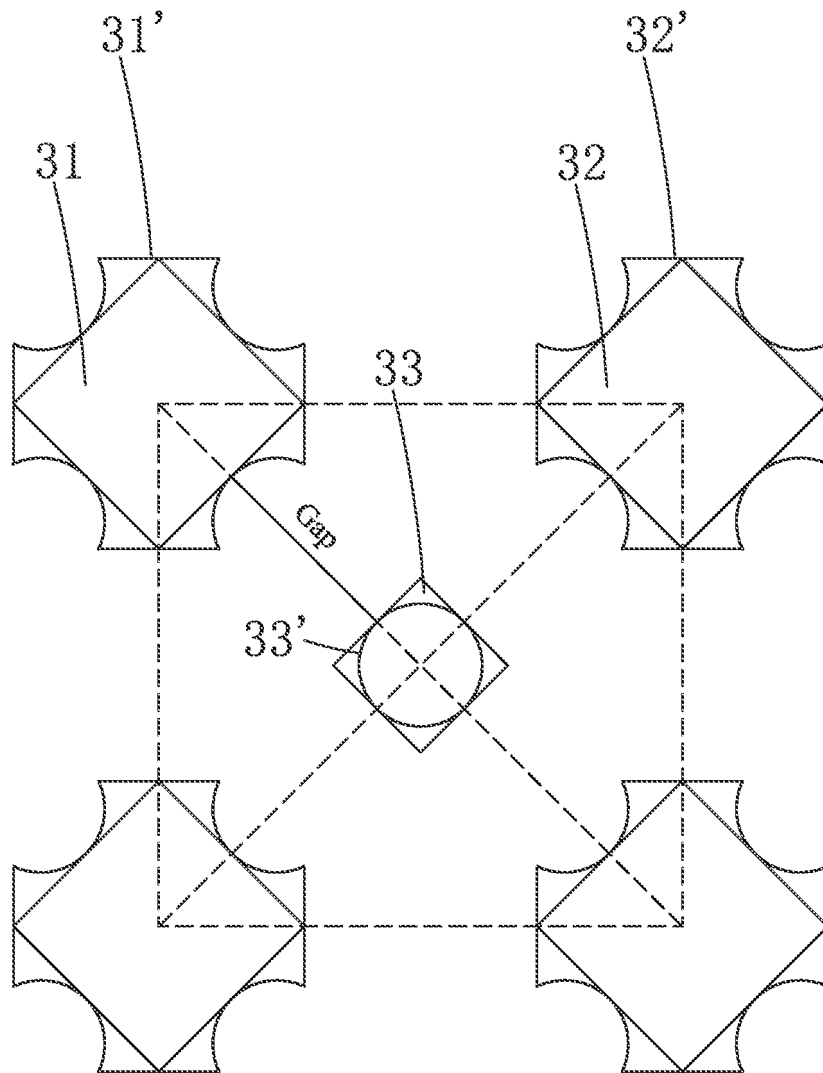


Fig. 7

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2018/077253

5	A. CLASSIFICATION OF SUBJECT MATTER H01L 27/32(2006.01)i According to International Patent Classification (IPC) or to both national classification and IPC	
10	B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) H01L; G02F; G06F; H04N Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched	
15	Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) CNPAT, DWPI, SIPOABS, CNABS, CNTXT, CNKI: 像素, 排列, 排布, 布置, OLED, 显示面板, 间隔, 交替, 行, 相邻, 虚拟, 方框, 面积, 形状, 四边形, 八边形, 彩色, 凹陷, 弧线, pixel, arrangement, display panel, virtual, box, frame, structure, area, alternat+, row, spac+, shape, adjacent, quadrilateral, octagon, color, concave, arc	
20	C. DOCUMENTS CONSIDERED TO BE RELEVANT	
25	Category*	Citation of document, with indication, where appropriate, of the relevant passages
30	X	CN 103311266 A (SAMSUNG DISPLAY CO., LTD.) 18 September 2013 (2013-09-18) description, paragraphs [0034]-[0047], and figure 1
35	A	CN 104282727 A (BOE TECHNOLOGY GROUP CO., LTD.) 14 January 2015 (2015-01-14) entire document
40	A	CN 104576699 A (TRULY (HUIZHOU) SMART DISPLAY LIMITED) 29 April 2015 (2015-04-29) entire document
45	A	CN 106298855 A (KUNSHAN GOVISIONOX OPTOELECTRONICS CO., LTD.) 04 January 2017 (2017-01-04) entire document
50	A	CN 204885166 U (KUNSHAN GOVISIONOX OPTOELECTRONICS CO., LTD.) 16 December 2015 (2015-12-16) entire document
55	<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.	
60	* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family
65	Date of the actual completion of the international search 30 October 2018	Date of mailing of the international search report 09 November 2018
70	Name and mailing address of the ISA/CN State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088 China Facsimile No. (86-10)62019451	Authorized officer Telephone No.

Form PCT/ISA/210 (second sheet) (January 2015)

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2018/077253

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